

## *Curriculum Vitae*

**Shri. SHANKARAGOUDA J. PATIL** M.Tech

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### **Objective:**

To be a part of a result oriented institute that seeks an ambitious and career conscious person where acquired skills and knowledge will be well utilized towards continuous growth and advancement of the organization and to get an opportunity to work for the progress of the institute

### **Present Status:**

Have been working as a HOD of Electrical And Electronics Engg. at K.L.E's C. B. KORE POLYTECHNIC, Chikodi (15.12.2015 –Till Date)

### **Education:**

- Completed **M.TECH** in **Industrial Electronics Engg.** (First Class with Distinction) at K.L.S V.D.I.T HALIYAL in the Year 2022
- Completed **B.E.** in **Electrical & Electronics Engg.** (First Class) at Hirasugar Institute of Technology, Nidasoshi, Belgaum in the year 2011.
- Completed **Diploma** in **Electrical & Electronics Engg.** (First Class) at Govt. Polytechnic Belgaum in the year 2008.
- Completed **S.S.L.C** (68.64%) at Govt. High School, Kerur in the year 2002.

## Administrative Experience:

### **HOD :**

- 1) Responsibility of Administrative duties at department level.
- 2) Maintaining good relationship with other departments to achieve common goal.
- 3) Implementation of instructional procedure from higher authority to promote teaching and learning activities.
- 4) Implementation of procedure that promote organization efficiency.
- 5) Maintaining good harmony in department by taking confidence of associates into account.

### Project Undertaking (M.Tech):

- “Smart Agri system”

### Project Undertaken During BE :

- “Energy Audit of the College”

### Project Undertaken During Diploma :

- “Industrial Monitoring and Control System using Micro-Controller”

## Achievements:

- ❖ Presented Technical paper on “**QUASI Z SOURCE INVERTER FOR PHOTOVOLTAIC POWER GENERATION SYSTEMS**” Bagged II<sup>nd</sup> prize in **NATIONAL LEVEL PAPER PRESENTATION** held at HIT Nidasoshi in 2010.
- ❖ Presented Technical paper on “**POWER QUALITY IMPROVEMENT USING DVR**” **GYAN-11 held at HIT Nidasoshi.**
- ❖ Presented Technical paper on “**POWER CRUNCH IN KARNATAKA AND SOLUTION**” **held at C. B. Kore Polytechnic Chikodi.**
- ❖ Attended One day National Level workshop on “ **Impact of NBA Process in quality and Capacity Building of Polytechnic**” Held at KIT, College of Engineering Kolhapur.

## Computer Skills:

- ❖ **Languages:** C, Assembly Level Programming (8051), MATLAB programming (in power system analysis), Mi-power
- ❖ **DESIGN:** CAD, Circuit simulation. Pspice
- ❖ **Operating Systems:** Windows 98/ XP/ , Windows-7UI

### **Work Experience:**

- Worked as Lecturer in Electrical & Electronics Engg. Department, at B.T.Patil Son's Polytechnic Stavanidhi,Nipani (From 01/07/2011 to 10/07/2012).
- Worked as Lecturer in Electrical & Electronics Engg. Department, at KLE'S C.B.KORE Polytechnic, Chikodi..(from 11/07/2012-14.12.2015).

### **Additional Responsibilities held during previous service :**

- Setup of Laboratories in Departments
- Admission Coordinator/Officer.
- IA Test Coordinator.
- Project Coordinator.
- Mentor for students.

### **Personal Profile:**

NAME : SHANKARAGOUDA JYOTIGOUDA PATIL.  
DATE OF BIRTH : 01/06/1986  
FATHER'S NAME : JYOTIGOUDA K. PATIL  
LANGUAGES KNOWN : ENGLISH, HINDI, AND KANNADA

**ADDRESS FOR CORRESPONDENCE** : **SHANKARAGOUDA JYOTIGOUDA PATIL**  
A/P: Kerur  
Tq: Chikodi  
Dist: Belgaum.

### **Hobbies:**

1. Households repairs
2. Reading Magazines.

**I declare that the information given above is true to the best of my knowledge & belief.**

PLACE: KERUR

DATE:

**Shri. S.J.PATIL**