



CURRICULUM VITAE

Name:	Mrs. Sujata S. Kamate	
Designation:	Asst. Prof.	
Department:	ECE Department	
Specialization & Research Interests:	VLSI Design, Digital Electronics and Data converters	
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Address:	Hirasugar Institute of Technology, Nidasoshi-591236. Dist : Belgaum, Karnataka.	

2. ACADEMIC QUALIFICATION (in reverse Chronological order):

Degree	Year	University/Board
Pursuing Ph. D	2017	VTU, Belagavi
M. Tech	2012	GIT, Belagavi
B.E	2002	HIT, Nidasoshi

3. ANY OTHER QUALIFICATION: --

4. PROFESSIONAL EXPERIENCE:

Organization/Institute/University	Position Held	Duration
HIT Nidasoshi	Assistant Professor	01-04-2018 to Till date
HIT Nidasoshi	Associate Professor	01-01-2017 to 31-03-2018
HIT Nidasoshi	Assistant Professor	01-10-2013 to 31-12-2016
HIT Nidasoshi	Lecturer	06-09-2004 to 31-01-2008
C.B. Kore polytechnic, Chikodi	Lecturer	13-11-2002 to 03-09-2004

5. ADMINISTRATIVE ASSIGNMENTS:

Position Held	Duration	Nature of Work
Convener of Women cell- Women Empowerment Committee of the Institute.	October 2018 to till date	Organizing awareness events and competitions for students.
Convener of CC- Internal Complaint Committee/Ant sexual Harassment Committee.	October 2018 to till date	Organizing awareness events
Dept. NBA criteria -3 coordinator	March 2017 to till date	Preparing complete criteria -3 file of the ECE Dept.
NAAC criteria -3 and 1 member	March 2018 to till date	Preparing NAAC criteria -1, 3 file of the ECE Dept.
Dept. Project Co-ordinatorr	September 2019 to till date	Scheduling and conducting Project reviews demonstrations / exhibitions.
Dept. IA Co-ordinatorr	March 2018 to till date	Maintaining IA documents, Hall arrangement, bluebook collection etc.
IEEE Branch counselor	June 2014 to till date	Conducting events, with students and reporting to IEEE Bangalore section for section reimbursement
Dept. NBA coordinator	October 2013 to March 2017	Preparing documents for NBA as per the instructions of higher authority.

6. COURSES TAUGHT:

- VLSI Design and Testing
- Signals and Systems
- Engineering Electromagnetics

- Microwaves and Antennas
- VLSI Design
- Computer Organization
- Engineering Statistics and Linear Algebra
- Logic Design
- Microelectronic Circuits
- Control Systems
- Analog and Mixed mode VLSI
- Analog Communication
- Antenna and wave propagation
- Basic Electronics
- Transmission lines and waveguides
- Optical fiber Communication
- *Universal Human Values*
- *Satellite and Optical Communication*
- *Advanced VLSI*
- *Indian Knowledge System*

7. RESEARCH SUPERVISION:

A. Ph.D:

- i. Awarded : Not yet
- ii. Submitted : Not yet
- iii. Ongoing : yes (VLSI Design)

B: M.Tech.

- Awarded : 2012 (Digital Electronics)

C. BE/B. Tech Degree Oriented (Bachelor Level Dissertation):

- Awarded : 2002 (Electronics and Communication)
- Submitted : 28 (4 KSCST selected and 2 for Shruti)
- Ongoing : 03

8. CONTRIBUTION TO CORPORATE LIFE OF THE INSTITUTE:

- As a Branch counselor of **IEEE HIT SB**, successfully organized the events under SB, submitted the yearly reports to IEEE Bangalore section in a prescribed format for section rebate and got totally **Rs. 55, 665/-** sanctioned for the years 2015, 2016 and 2017. Successfully submitted the report for the year 2019.
- Convener for Women Empowerment cell HIT, Nidasoshi.
- Convener for ICC/ Antisexual Harrassment Committee HIT, Nidasoshi.
- Convener for **Women's Day Celebration functions** for all the years

- Organized “**Awareness on women safety and sexual harassment**” on 08/03/2019 on behalf of Anti-sexual Harassment committee.
- Organized talk on “**Awareness on Legitimate Provisions for Women**” on 30th October 2018.
- Successfully organized inaugural function of HIT- Quest 2018.
- Successfully organized inaugural function of Technovision- 20 in February-2020.
- Successfully organized inaugural function of Technovision- 2019.
- Successfully organized inaugural function of HIT- Quest 2019.
- Worked in registration committee for icriset-2019.
- Department Coordinator ICRASET -2016.
- Convener of Registration committee for ICRASET- 2018.
- Worked as member in various for Annual Day celebration in HIT- **Sambhrama**.
- Department Coordinator for the HIT- Quest -2013.

9. MEMBERSHIP OF SOCIETIES/PROFESSIONAL BODIES:

ISTE: Life Member of Indian Society for Technical Education (LMISTE)

10. PUBLICATIONS:

A. BOOKS/MONOGRAPHS:

1. **Authored: Nil.**

2. **Edited: Nil**

B. PAPERS IN REFEREED/PEER REVIEWED JOURNALS:

- i. Vidyavati Mallaraddi , Rajani H.P , Sujata S Kamate, “Efficient Power Optimization Technique for Dynamic and Leakage Power Reduction in N-bit MAC Architecture” published in AES journal ISSN: 2096-3246 Volume 57, Issue 03, March, 2025
- ii. Sujata Kamate, Sunita Malaj, Shruti Magadum “AI BASED AUTOMATED HARVESTING MULTIPURPOSE AGRICULTURAL ROBOT” Published in International Journal of Advanced Research in Science and Engineering, Vol. No. 14, Issue No. 03, March, 2025
- iii. Vidyavati Mallaraddi , Rajani H.P , Sujata S Kamate, “Efficient Power Optimization Technique for Dynamic and Leakage Power Reduction in N-bit MAC Architecture,” Published in Journal of Advanced Engg. Sciences, Volume 57, Issue 03, Pages: 5279-5295. March, 2025. DOI: 03.821/Aes.28.03.2025.01
- iv. S. S. Kamate, S. S. Malaj, “AI BASED AUTOMATED HARVESTING MULTIPURPOSE AGRICULTURAL ROBOT”, Published in IJARSE Volume. No. 14, Issue No. 03, March 2025, Page No. [73-78].
- v. Kamate, S S (Kamate, Sujata S.); Rajani, HP (Rajani, H. P.) ; Mallaraddi, V. (Mallaraddi, Vidyavati) “Efficient Design Techniques of Flash ADC for High Speed

- and Ultra Low Lower Applications” Published in Journal of Bioscience Biotechnology Research Communications, Volume: 13, Issue: 13, Pages: 144-149. December 2020. **(E-SCI indexed)** DOI: 10.21786/bbrc/13.13/20.
- vi. Kamate, S S (Kamate, Sujata S.); Rajani, HP (Rajani, H. P.); Mallaraddi, V (Mallaraddi, Vidyavati) Designing A Novel Architecture to Reduce Stand by and Dynamic Power Dissipation for Sleepy Keeper CMOS Logic Circuits Published in Journal of Bioscience Biotechnology Research Communications, Volume: 13, Issue: 13, Pages: 144-149 DOI: 10.21786/bbrc/13.13/20, 2020 **(E-SCI indexed)**.
- vii. S S Kamate, S S Malaj, “Design and Implementation of IoT based Electronic Voting Machine”, a journal of composition theory, Volume 12 , Issue VI, Pages: 100-105 June 2019 (ISSN : 0731-6755).
- viii. Akshata Hirekodi Rakesh Patil S S Kamate, (2018). A 4-bit, 3.2GSPS Flash ADC with a new multiplexer based encoder: Journal of Emerging Technologies and Innovative Research, 5(8), 728-733.
- ix. S. S. Malaj, S.S. Kamate “RFID & GSM Based Toll Tax System”, International Journal of Scientific and Research Publications, Volume 7, Issue 1, Pages: 110-14, IJSRP, 2017.
- x. S. S. Kamate, Akshata Naikar, S. S. Malaj, (2016). Design and Implementation of low power CMOS Analog to Digital Converter in cadence tool: Journal of Advances in Science and Technology, Vol. 12, Issue No. 25, (Special Issue), 239-242.

C. PAPERS IN CONFERENCES PROCEEDINGS:

- i. Sunita Malaj, Sujata Kamate, Vishal Haragapure “ADVANCED VEHICLE ANTI-THEFT AND SAFETY SYSTEM USING AI AND LI-FI TECHNOLOGY” 4th international Conference on Recent Trends in Engineering and Technology, March 2025 pp. 143-149.
- ii. S. S. Kamate, S. S. Malaj, “ADVANCED VEHICLE ANTI-THEFT AND SAFETY SYSTEM USING AI AND LI-FI TECHNOLOGY,” 2025 *fourth Int. Conf. on Recent Trends in Engineering and Technology*, Nidasoshi, India, 2025, pp. 79-95.
- iii. S. S. Kamate, H. P. Rajani and V. Mallaraddi, "Design and Implementation of Low Power Flash Analog to Digital Converter," *2023 Second International Conference on Electrical, Electronics, Information and Communication Technologies (ICEEICT)*, Trichirappalli, India, 2023, pp. 1-6, doi:10.1109/ICEEICT56924.2023.10157537.
- vi. V. Mallaraddi, H. P. Rajani and S. S. Kamate, "Static and Dynamic power optimization using Leakage Feedback approach for nanoscale CMOS VLSI circuits," *2022 First International Conference on Electrical, Electronics, Information and Communication Technologies (ICEEICT)*, 2022, pp. 1-5, doi: 10.1109/ICEEICT53079.2022.9768527.
- v. S. S. Kamate, Dr. H. P. Rajani, (2020). Design and Implementation of 3-bit Flash Analog to Digital Converter for Low power and High speed Applications with new Ex-OR based ROM Encoder, *Second International Conference on Advances in Electrical and Computer Technologies 2020*, 12 - 13, June 2020 Coimbatore, Tamil Nadu, India.
- vi. S. S. Kamate, Dr. H. P. Rajani, (2018). Emerging Design techniques for low power and high speed CMOS Flash ADC, *Int. Conf. on Recent Innovations in Electrical, Electronics & Communication Engg.*, 27-28 July 2018, Bhubaneshwar.

vii. Akshata Hirekodi Rakesh Patil S S Kamate, (2018). A 4-bit, 3.2GSPS Flash ADC with a new multiplexer based encoder: Journal of Emerging Technologies and Innovative Research, 5(8), 728-733.

viii. S. S. Kamate, Akshata Naikar, S. S. Malaj, (2016). Design and Implementation of low power CMOS Analog to Digital Converter in cadence tool: Journal of Advances in Science and Technology, Vol. 12, Issue No. 25, (Special Issue), 239-242.

11. Patents/Copyrights/IPR (If Any)

12. INVITED TALKS:

13. RESEARCH PROJECTS (COMPLETED/ONGOING):

14. PARTICIPATION & PRESENTATIONS IN SEMINARS /SYMPOSIA/ WORKSHOPS/ CONFERENCES:

Sl. No.	Topic	Period	Date	Venue
1.	AI/ML: Foundations, Real-World Applications and Sustainable Development	10 Days	02-04-2025 to 11-04-2025	Online FDP Sponsored by Meity Govt. of India & Conducted by NIT Warangal in association with ECE HIT
2.	Three days VTU sponsored FDP on Indian Knowledge System	3 Days	3 rd to 5 th March 2025	Online mode
3.	FDP on "AI/ML: Foundations, Real-World Applications and Sustainable Development"	10 Days	April 2 - 11, 2025,	ssociation with Hirasugar Institute of Technology, Nidasoshi
4.	International Conference on Recent Trends in Engineering	2 days	07 th & 08 th March, 2025	HIT, Nidasoshi
5.	AICTE Training and Learning (ATAL) Academy Faculty Development Program on Quantum Computing and its Applications FDP on	6 Days	09/12/2024 to 14/12/2024.	Global Academy of Technology (Online)
6.	Three days VTU Sponsored FDP on Advanced VLSI	3 Days	24/10/2024 to 26/10/2024	JCE, Belagavi
7.	Prevention of Sexual Harassment -POSH Workshop	1 Day	26 th July, 2023	Suprajiv Technical & Management Studies Pvt. Ltd., Bangalore (Online)
8.	FDP on Industry 4.0 technological Evolution.	5 Days	1st to 4th April 2024	Techno College of Engineering, Agartala (Online)
9.	Second International Conference	3 days	05 th to 07 th April,	Trichy, Tamil Nadu

	on Electrical, Electronics, Information and Communication Technologies (ICEEICT 2023)		2023	
10.	First International Conference on Electrical, Electronics, Information and Communication Technologies (ICEEICT 2023)	2 days	16 th to 17 th Feb., 2022	Trichy, Tamil Nadu
11.	Third International Conference on Recent Trends in Engineering	2 days	08 th & 09 th December, 2023	HIT, Nidasoshi
12.	Software Engineering and Testing Methodologies	4 Days	16 th to 19 th March 2021	HIT, Nidasoshi
13.	FDP on “Introduction to Microchip FPGA”	5 Days	19th to 23rd July 2021	Bapuji Institute of Engineering and Technology,
14.	Battery Management & Control Techniques in Electric Vehicles	6 days	14 th -19 th Dec 2020	HIT, Nidasoshi
15.	FDP on “VLSI Design Tools, Techniques and Applications”, by E&ICT Academy NIT, Warangal.	8 Days	10 th to 17 th August 2020	Vishnu Institute of Technology, Bhimavaram, AP
16.	International level online FDP on “Advanced Research Trends in image Processing”	3 days	14-16 th July 2020	HIT, Nidasoshi
17.	online FDP on “Skill Enhancement for Academicians	2 Days	8 th to 9 th June 2020	JCE Belagavi
18.	National Level One-Week FDP on “Machine Learning Based Project Architecture Design”, under TEQIP-III.	5 Days	15th to 19th June 2020.	B.V.V.Sangha’s Basaveshwar Engineering College (A), Bagalkot
19.	Four days workshop on “Design and Simulation of Microstrip Antenna using HFSS Tool”	4 Days	26 th to 29 th February 2020	HIT, Nidasoshi
20.	VGST sanctioned FDP on “Machine Learning and Data Analytics”	5 Days	08 th January to 11 th January 2020	HIT, Nidasoshi
21.	Second International Conference on Recent Trends in Engineering	2 days	10 th & 08 th April, 2019	HIT, Nidasoshi
22.	Workshop on “Internet of Things and it’s real time applications organized by E&ICT Academy NIT, Warangal.	6 Days	28 th to 02 th Feb 2019	HIT, Nidasoshi
23.	STTP on IoT and DSP Applications Design	6 Days	01st January to 06 th January 2019	KLE MSS CET, Belagavi
24.	International Conference on Recent Trends in Engineering	2 Days	18 & 19 th May, 2018	HIT, Nidasoshi

25.	STTP on Artificial Intelligence and Medical Image processing	7 Days	16 th to 22 nd January 2018	KLE MSS CET, Belagavi
26.	Workshop on “Research Methodology”	1 Day	11 th November 201	GIT, Belagavi
27.	Workshop on “NBA SAR Filling and preparedness for Assessment”	3 Days	15 th to 17 th September 2017	HIT, Nidasoshi
28.	Two week ISTE STTP on “CMOS Mixed Signal and RF VLSI Design”	6 Days	26 th December, 2016 to 04 th February 2017	GIT, Belagavi
29.	International Conference on Recent Innovations in Engineering-2016	2 days	08 th and 09 th October 2016	HIT, Nidasoshi
30.	Workshop on Micro-nano-Photonics	1 Day	30 th January, 2015	Jain College of Engg. and Technology, Belagavi.
31.	Workshop on “Advanced VLSI Design using Cadence Design Suite”	3 Days	16 th to 18 th July 2014	JSSATE, Bagalore
32.	Workshop on “Latest Trends in EDA for Digital Electronics and VLSI Technology”	2 Days	22 nd -23 rd Sept. 2013	MMEC, Belgaum
33.	Three days Workshop on Signals & Systems	3 Days	4 th - 6 th October, 2012	HIT, Nidasoshi
34.	Workshop on “Application of Mathematics in Engineering”.	2 Days	30 th - 31 st August, 2010	HIT, Nidasoshi
35.	Workshop on “Hands on Workshop on VLSI Design Using Cadence Tools site”	3 Days	28 th -30 th July, 2010	Sambhram Institute of Technology, Bangalore
36.	Workshop on “CMOS VLSI and ASIC designs using MICROWIND tool”	2 Days	27 th – 28 th August 2009	SIT, Tumkur
37.	Workshop on “CMOS VLSI and ASIC designs using MICROWIND”	2 Days	31 st August, to 2009 1st Sept., 2007	BMS Bangalore
38.	STTP on “Role of Women in Technical Education”	5 Days	2 nd – 6 th August 2004	AIT, Bangalore
39.	One day workshop on “Embedded System Development using ARM Controller”.	1 Day	19 th June 2004	ISM, Bangalore

Conferences:

1. Second International Conference on Advances in Electrical and Computer Technologies 2020, 12 - 13, June 2020 Coimbatore, Tamil Nadu, India.

2. International Conference on Recent Advances in Science and Technology (ICRISET-2019)
3. International Conference on Recent Innovations in Electrical, Electronics & Communication Engineering 27-28 July 2018, Bhubaneswar.
4. International Conference on Recent Advances in Science and Technology (ICRISET-2018)
5. International conference on smart electrical Systems ICSES-2016, Belagavi.

Workshops:

1. Organized Workshop on Basic Arduino programming to department prefinal year students from 17th to 19th January 2023.
2. Organized Workshop on Basic Arduino programming & it's applications to Dept. prefinal year students from 16th to 18th December 2021.

Organized Webinars on

1. Professional Communication Skills for all ECE students on 02.09.2020 from 2.30 to 4.00pm by JayPrakash Rau, IRS.
2. Ethics in Engineering Profession for all ECE students on 02.09.2020 from 2.30 to 4.00pm by JayPrakash Rau, IRS

Attended Webinars on

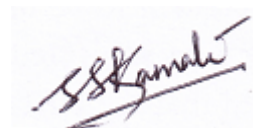
- "HOW TO CRACK GATE IN FIRST ATTEMPT" organized by Electronics and Communication Engineering Department in Association with ACE Engineering Academy, Hyderabad on 28th June 2020.
- Career after Covid-19" organized by Electronics and Communication Engineering Department on 21st June 2020.

15. AWARDS, FELLOWSHIPS & OTHER DISTINCTIONS:

Nil

16. ANY OTHER SIGNIFICANT INFORMATION:

- Achieved 100% results in many subjects.
- Always achieved very good results, in critical courses like Engineering Electromagnetics, Signals and Systems and VLSI Design.



(Prof. S. S. Kamate)