



KALPATARU INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belagavi & Recognised by AICTE, New Delhi)

DEPARTMENT OF CSE

NBA accredited



ORGANIZING

Five Days Online National Level Workshop

On

"Deep Learning & its Applications"

From  *6th* to  *10th*
2023

In Association with



ISTE Karnataka
Section



Deep Learning For A Better Tomorrow

Who can Attend?

Faculties from all AICTE approved Institutions, Research Scholars, PG Students, UG Students.

Registration Link for Workshop

Registration form submission is mandatory to all

<https://forms.gle/XNZp8WFLMjZDv1TH6>

Registration Fee

Academicians and Students: Rs 200/-

Industry delegates: Rs 400/-

E-Certificate will be issued to all the participants who attend every sessions.

Payment Details:

Beneficiary Name: Principal, KIT, Tiptur

Canara Bank, KVS branch, Tiptur

Account No: 20102200000221

IFSC : CNRB0012010

Participants can make payments in any NEFT/RTGS/Account Transfer to the above account.

Resource Persons

Dr. Venkatesh

Assoc. Prof., Dept. of CSE,
UVCE, Bengaluru

Dr. Pushpa C N

Assoc. Prof., Dept. of CSE,
UVCE, Bengaluru

Dr. Rajesh I S

Assist. Prof., Dept. of AI-ML,
BMSIT, Bengaluru

Mr. Nagaraju Y

Research Scholar, Dept. of CSE,
UVCE, Bengaluru

Dr. Maithri C

Professor, Dept. of CSE,
KIT, Tiptur



Deep Learning



Deep Learning



Chief Patrons

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Convenors

Principal

Dr. G D Gurusurthy

HOD

Prof. Shashidhara M S

IEEE Student branch Counselor

Dr. Raviprakash M L

Associate Professor, Dept. of CSE, KIT

Co-ordinators

Dr. Maithri C

Professor, Dept. of CSE, KIT, Tiptur

Prof. Sanjay Kumar N V

Associate Professor, Dept. of CSE, KIT, Tiptur

Prof. Anitha C L

Associate Professor, Dept. of CSE, KIT, Tiptur

Prof. Vani K B

Associate Professor, Dept. of CSE, KIT, Tiptur

Abstract

Deep Learning is extremely indispensable technology for all professionals, especially for researchers, designers and analysts in future. This technology is often considered to be a set of algorithms that 'mimics the brain' but, more appropriate definition will be a set of algorithms which 'learns from layers'. Deep learning is an artificial intelligence function that imitates the working of human brain in processing data and creating patterns for use in decision making systems. Deep learning is a subfield of machine learning concerned with algorithms inspired by the structures and functions of the brain which is called as artificial neural network (ANN). Many high-end innovations in the fields of computer vision and data science is driven by deep learning technology. The algorithms of Deep learning are capable of learning unsupervised or unstructured data and ability to execute feature engineering by itself. This workshop will mainly focus on the Introduction to Deep Learning, Deep Learning Approaches, Architectures, Models and its applications.

Objectives of the Program

1. To provide a forum to exchange views, ideas and the latest innovations in the field of Deep learning.
2. To offer learning on basics, emerging trends & challenges in the field of deep learning.
3. To improve faculties and students to gain ability to carry out research, testing & consultancy in the area of Deep learning.
4. To provide hands-on training using different statistical software's to undertake research in the field of Deep learning.

Important Dates and Information

Last Date for Online Registration	01-03-2023
Date of intimation to the Selected Candidates by (Email or Phone)	02-03-2023
Confirmation from Participants by (Email or Phone)	03-03-2023

1. Seats are limited to 100 participants and they are selected on first come first serve basis.
2. Two Sessions per day will be conducted.
3. A Quiz will be conducted at the end of Workshop.



Program Schedule

06-03-2023	9:30 to 10.00 am	Inauguration.
	10.00 to 12.00 pm	Introduction to Deep Learning, Algorithms and Activation Functions.
	2.00 to 4.00 pm	Introduction to Python Programming.
07-03-2023	10.00 to 12.00 pm	Image formation and pre-processing.
	2.00 to 4.00 pm	Introduction to CNN and Learning Algorithms .
08-03-2023	10.00 to 12.00 pm	Hands-on Image Classification.
	2.00 to 4.00 pm	Hands-on Segmentation Using UNET.
09-03-2023	10.00 to 12.00 pm	Hands on Object Detection using YOLO V3 video.
	2.00 to 4.00 pm	Hands-on Data Visualization.
10-03-2023	10.00 to 12.00 pm	Introduction to Medical Image Processing.
	12.30 to 1.30 pm	Valedictory.



Contact us

Dr. Maithri C
Professor

Email : maithri.c.prashanth@gmail.com
Ph : 8073270693

Prof. Anitha C L
Associate Professor

Email : kitanitha1@gmail.com
Ph : 9620158365

Prof. Vani K B
Associate Professor

Email : kit.vani@gmail.com
Ph : 9164978551



workshopcse@kittiptur.ac.in



Organizing Committee

Dr. Keshava Munegowda
Prof. Santhosh S
Dr. Chethan Chandra S Basava Raddi
Dr. Manoj Kumar D P
Prof. Harish B M
Prof. Niranjan S J
Prof. Kavitha M G
Prof. Supreetha Patel T P
Prof. Deepika G L
Prof. Anusha K
Prof. Vidya H A
Prof. Shilpa S
Prof. Husna Banu
Prof. Sapna S Basava Raddi

Technical Team

Mr. M C Mahalingaiah
Mr. Naveen Kumar A S
Mrs. Pavithra B
Mrs Sunitha H U

About Campus

Kalpataru Institute of Technology was established in 1986 with a view to provide quality technical education. The college is affiliated to Visvesvaraya Technological University, Belagavi, recognized by All India Council for Technical Education (AICTE), New Delhi. The college has an excellent team of qualified, experienced and dedicated faculty members. The 35 years old Institution is equipped with all required infrastructure. The college campus is located within a distance of 2 kms from railway station and bus stand.

Computer Science and Engineering is NBA accredited for three years (2022-23, 2023-24 and 2024-25).

The Department was established in the year 1986. The department aims to provide quality education with Bachelor of Engineering(B.E.) in Computer Science and Engineering. The Department offers a healthy atmosphere for students to build their talents in the respective advanced areas of Computer Science, Engineering and Technology.





VISVESVARAYA TECHNOLOGICAL UNIVERSITY

Belagavi – 590018, Karnataka, India
(State Technological University, Govt. of Karnataka)
Regional Centre, Nagarabhavi, Bengaluru

AICTE-VTU Joint Training Programme for AICTE Approved & VTU Affiliated Institution Teachers

5 Days Training Programme on

“Introduction to Web Programming”

27th to 31st March 2023

Programme Schedule

27.03.2023	10 AM - 10.20 AM	Inauguration of Analog VLSI design using EDA tools Training Programme	<p>Mr. B V Purushotham AGM, COMVIVA Technologies, Bengaluru</p> <p>Dr. N.Chikkanna Technical University Coordinator-AICTE & VTU – Joint Training Programme, Professor VTU PG Centre, VIAT, Muddenahalli</p> <p>Dr. Shiva Murthy G Associate Professor & Programme Coordinator Dept of CSE, VTU PG Centre Muddenahalli</p>
	10.20 AM – 12 Noon	Keynote Address/ Plenary Talk on “Web Programming”,	Dr. Sailaja Kumar Manager, Jigsaw
	11.15 AM – 11.30 AM	Tea Break	
	12 Noon - 01.30PM	HTML, XHTML Programming	Dr. Sailaja Kumar Manager, Jigsaw
	1.30 PM-2.00 PM	Lunch Break	
	2.00 PM - 4.30 PM	HTML 5	Dr. Sailaja Kumar Manager, Jigsaw
28.03.2023	9.30 AM - 11.15 AM	CSS	Mr. Ajay Kumar, Zelton Technologies
	11.15 AM – 11.30 AM	Tea Break	
	11.30 AM - 01.30 PM	CSS	Mr. Ajay Kumar, Zelton Technologies
	1.30 PM - 2.00 PM	Lunch Break	
	2.00 PM - 4.00 PM	CSS	Mr. Ajay Kumar, Zelton Technologies
29.03.2023	9.30 AM - 11.15 AM	CSS: Tables, Images and Links	Mr. Santhosh Kumar, Zelton Technologies
	11.15 AM – 11.30 AM	Tea Break	
	11.30 AM - 01.30 PM	CSS: Tables, Images and Links	Mr. Santhosh Kumar, Zelton Technologies
	1.30 PM - 2.00 PM	Lunch Break	
	2.00 PM - 4.00 PM	CSS: Tables, Images and Links	Mr. Santhosh Kumar, Zelton Technologies
30.03.2023	9.30 AM - 11.15 AM	Java Scripts	Mr. Pavan, Assistant Professor, MSRIT, Bangalore
	11.15 AM – 11.30 AM	Tea Break	
	11.30 AM - 01.30 PM	Java Scripts: Functions, DOM	Mr. Pavan, Assistant Professor, MSRIT, Bangalore
	1.30 PM - 2.00 PM	Lunch Break	
	2.00 PM - 4.00 PM	Java Scripts: Forms, Handlers	Mr. Pavan, Assistant Professor, MSRIT, Bangalore
31.03.2023	9.30 AM - 11.15 AM	Angular JS	Mr. Abhishek, Assistant Professor, MSRIT, Bangalore

	11.15 AM – 11.30 AM	Tea Break	
	11.30 AM - 01.30 PM	Angular JS	Mr. Abhishek, Assistant Professor, MSRIT, Bangalore
	1.30 PM - 2.00 PM	Lunch Break	
	2.00 PM - 3.30 PM	Ruby on Rails	Mr. Abhishek, Assistant Professor, MSRIT, Bangalore
	3:30 PM -4:00 PM	Valedictory Function	
	4.00 PM -4.30 PM	Online Test	

<p>Dr. Shiva Murthy G Associate Professor & Programme Coordinator Dept of CSE, VTU PG Centre Muddenahalli Mob: 9845603236 Email: kgshivam@gmail.com</p>	<p>Dr. N.Chikkanna TU-Coordinator, Professor & Chairman, Dept. of Aerospace Engineering, VTU PG Centre, VIAT, Muddenahalli. Mob: 9845629287 Email – nchikkanna1967@gmail.com</p>
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ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ

"ವಿ ಟಿ ಯು ಅಧಿನಿಯಮ ೧೯೯೪" ರ ಅಡಿಯಲ್ಲಿ ಕರ್ನಾಟಕ ಸರ್ಕಾರದಿಂದ ಸ್ಥಾಪಿತವಾದ ರಾಜ್ಯ ವಿಶ್ವವಿದ್ಯಾಲಯ



Visvesvaraya Technological University

(State University of Government of Karnataka Established as per the VTU Act, 1994)



Prof. B. E. Rangaswamy Ph.D.

Phone No: (0831) – 2498100

Registrar

Fax No. : (0831) – 2405467

Ref. No.: VTU/ROB/2022-23/7188

Date: 8 MAR 2023

CIRCULAR

Sub: Deputation of Teaching Faculty Members of AICTE approved and VTU Affiliated Colleges for the AICTE-VTU Joint Training Programs (Online).
Ref: Hon'ble Vice Chancellor's approval dated 07.03.2023.

With reference to the above subject, this is to inform you that, AICTE-VTU is jointly organizing the Training programme on the following courses. The objective of the Training programme is to enhance the faculty's expertise and teaching skills in the courses introduced as per NEP 2020.

Sl. No	Topic	Date	Programme Coordinators	Online Registration Link/ QR Code
1	Introduction to Python Programming & its Applications	13 th -17 th March 2023 (5 Days)	Dr. K Thippeswamy, Professor, Dept. of Computer Science & Engineering, VTU PG Center, Mysore Mob: +91-7019605611 Email: thippeswamy@vtu.ac.in	 https://forms.gle/nw7QrX7cA4XXbP2UA
2	Analog VLSI design using EDA Tools	20 th – 25 th March 2023 (22/03/2023 Holiday)	Dr. Sarika Raga, Associate Professor & Programme Coordinator, Dept. of Electronics & Communications Engineering, VTU CPGS, Muddenahalli. Mob: +91-9481470671 Email: raga.sarika@gmail.com	 https://forms.gle/VxBVDc8WXChTkNJq9
3	Introduction to Web Programming	27 th – 31 st March 2023 (5 Days)	Dr. Shivamurthy G, Associate Professor & Programme Coordinator, Dept. of Computer Science & Engineering, VTU CPGS, Muddenahalli. Mob: +91-9845603236 Email: kgshivam@gmail.com	 https://forms.gle/F9fXzurMHT4ZBRhPA
4	Introduction to Internet of Things (IoT)	17 th -21 st April 2023 (5 Days)	Dr. Nirmala Hiremani, Assistant Professor, Dept. of Computer Science & Engineering, VTU CPGS, Muddenahalli. Mob: +91-9483570925 Email: nirmalavtupg@gmail.com	 https://forms.gle/FT1fBXKVnKwvfv4w9
5	Robotics & Artificial Intelligence	24 th – 28 th April 2023 (5 Days)	Dr. Basawaraj, Associate Professor Dept. of Aerospace Engineering, VTU CPGS, Muddenahalli. Mob: +91-9986157177 Email: basawaraj.2009@gmail.com	 https://forms.gle/YYG8NAWDAFDafUVE6

For more information you may contact, Dr. N Chikkanna, TU Coordinator & Professor, Dept. of Aerospace Engineering, VTU CPGS, Muddenahalli, Mob: 9845629287

Hence, you are hereby requested to depute one or two Faculty Members of your college to the above said **Online** training program and encourage the faculty members to enrich their teaching skills.
(There will be no Registration Fee)

By Order,

Ras 08/03/23 BE
Registrar
A

To,

1. The Principals of all the Affiliated / Constituent / Autonomous Engineering Colleges and School of Architecture of VTU.
2. The Chairpersons / Programme Coordinators of all the Departments, Centres for PG Studies of VTU in Muddenahalli, Belagavi, Kalaburgi and Mysuru.

Copy to:

1. The Hon'ble Vice Chancellor through his secretary, VTU, Belagavi – for kind information.
2. The Finance Officer, VTU, Belagavi - for kind information.
3. The Regional Directors (I/c), VTU Regional Centres at Bengaluru, Belagavi, Kalaburgi and Mysuru for information

4-Days VGST Sponsored
Faculty Development Program
On

Machine Learning and Data Analytics
(8th to 11th January 2020)
Registration Form

1. Name: _____
2. Qualification: _____
3. Area of specialization: _____
4. Designation: _____
5. Department: _____
6. Institute: _____
7. Experience (in years):
Teaching: Research: Industry:
8. Address for Communication:

Mobile: _____
E-mail: _____
9. Accommodation required: YES /NO

Declaration

The above mentioned information is true to the best of my knowledge and belief. I agree to abide by the rules and regulations of the FDP. I shall attend the Program for the entire duration.

Applicant's Signature

Dr./Mr./Mrs./Miss _____

_____ is deputed to attend the FDP during 8th to 11th of Jan-2020

Signature of Principal with Seal

Organizing Committee

Prof. N K Honnagoudar, Dept. of CSE
Prof. R R Patil, Dept. of CSE
Prof. N M Patel, Dept. of CSE
Prof. A A Daptardar, Dept. of CSE
Prof. C R Belavi, Dept. of CSE
Prof. M A Chitale, Dept. of CSE
Prof. M A Gholap, Dept. of CSE

Resource Persons

Resource persons are from the reputed Institutions like IIT, IIIT, NIT and the Industries.

Who Can Apply

Faculty members from the University/ Engineering Colleges recognized by AICTE/ UGC. Maximum number of participants is limited to 50 only.

Important Instructions

- ◆ **There is no registration fee for the program.**
- ◆ Last date to receive filled in application form(soft/hard): **31st December 2019.**
- ◆ Send scanned copy of filled in application to **fdp.cse@hsit.ac.in** on or before due date.
- ◆ Selection on first come first serve basis and intimation by e-mail on: **2nd January 2020.**
- ◆ Accommodation will be provided for outstation participants upon prior request in Institute Hostels.
- ◆ **Participants are requested to bring Laptop for hands on sessions.**
- ◆ As needed, please feel free to make duplicate copies of this form for additional registration & can also be downloaded from Institute website.

Address for Correspondence
Prof. S. G. Gollagi

Department of CSE, HIT, Nidasoshi,
Tq:- Hukkeri, Dt:- Belagavi, Karnataka – 591 236
Mobile: +91-9880383883, E-mail: sggollagi.cse@hsit.ac.in



S. J. P. N. Trust's

HIRASUGAR INSTITUTE OF TECHNOLOGY,
NIDASOSHI – 591 236

Accredited at 'A' Grade by NAAC
Programs Accredited by NBA: CSE, ECE, EEE, ME
www.hsit.ac.in

4-Days VGST Sponsored
Faculty Development Program
On
Machine Learning and Data Analytics
(8th to 11th January 2020)



Organized by:
Department of Computer Science & Engineering



About the Institute

It was with the vision and foresight of His Holiness Shri Pancham Nijalingeshwar Mahaswamiji, the Ninth pontiff of Shri. Siddha Sounsthan Math, Nidasoshi, that Shri. Jagadguru Pancham Nijalingeshwar (SJPN) Trust was founded in 1984 with the sole purpose of imparting technical education to the needy masses. Under this trust, Hirasugar Institute of Technology, Nidasoshi was established in the year 1996 with the sole purpose of providing quality technical education in various disciplines of engineering. The management of HIT is a benevolent body of individuals drawn from various walks of life. Eminent personalities from both industry & academic field are managing the institution. The college is spread over a sprawling 48.26 acres of greenery & natural surroundings. The Institute offers 4 years BE Degree courses in Computer Science & Engineering, Mechanical Engineering, Electrical & Electronics Engineering and Electronics & Communication Engineering. The Institute also runs a Post Graduate Course in Thermal Engineering and all the four departments are recognized as a Research Centre by VTU, Belagavi. The college is affiliated to Visvesvaraya Technological University (VTU), Belagavi and approved by All India Council for Technical Education (AICTE), New Delhi, Recognized by the Government of Karnataka, and Recognized under section 2(f) of UGC Act, 1956. Institute is also accredited at 'A' grade by NAAC and all the four programs are accredited by NBA.

About the Department

The Department of Computer Science and Engineering was established in the year 1996 with an intake of 60 students. The Department has also been recognized as a Research Centre by Visvesvaraya Technological University (VTU), Belagavi. Department has been accredited by NBA. The department has qualified & well experienced teaching faculty with state of the art infrastructure and modern teaching aids catering to the needs of VTU curriculum. The department has well equipped laboratories and Computer Center with latest configuration and licensed software which provides excellent facilities for learning and 50 Mbps leased Line 1:1 internet connection with secured Wi-Fi.

About VGST

Vision Group on Science and Technology (VGST) was constituted in October 2008 under the Chairmanship of distinguished scientist Bharat Ratna Prof. C. N. R. Rao, F.R.S. Chairman of the Science Advisory Council to Hon'ble Prime Minister, Govt. of India and National Research Professor, JNCASR. VGST is an apex advisory body to recommend Science and Technology Programs relevant to mandate of DST. The objective is to encourage and promote Science and Technology education and research in the state. VGST plays a vital role in catalyzing, strengthening educational institutions to meet scientific and technological needs. To accomplish this, VGST is involved in developing, in stages, mechanisms to bring a paradigm shift in the Basic Science Education, research and taking science to the masses through a wide range of initiatives.

About the FDP

To meet the ever-demanding needs of knowledge industry, Faculty development programme (FDP) has always been an effective tool to enhance teaching and research capabilities of educators. This Faculty Development program aims at imparting knowledge in advanced and frontier technologies for engineering college teachers & upgrading the professional development, teaching, training and research skills of participants in emerging area of Machine Learning and Data Analytics. It ensures Data Analytics knowledge in different business environments like Sales & Marketing, Finance etc. It provides a solid understanding of linear algebra and statistics that forms the foundation of Machine Learning and Data Analytics. It ensures comprehensive understanding from mathematical fundamentals to advanced topics in Machine learning and Data Analytics.

Program Content

Fundamentals of Machine Learning and Data Analytics, Basics of Python Programming and Packages like Scikit, Learn Pandas, Numpy, Matplotlib and Keras, Classification, Linear Regression Techniques and Applications, Research Avenues in Machine Learning & Data Analytics, Data Analytics using IBM SPSS tool.

Chief Patron

His Holiness Jagadguru Panchama
Shri. Shivalingeshwar Mahaswamiji
Siddha Sounsthana Math, Nidasoshi

Patrons

Shri. G. M. Patil
Honourable Secretary, S.J.P.N. Trust, Nidasoshi
Dr. S. G. Sreekanteswara Swamy,
Consultant, VGST, Govt. of Karnataka
Prof. S. K. Muttagi,
Scientific Officer, VGST, Govt. of Karnataka

Organizing Chairman

Dr. S. C. Kamate
Principal, HIT, Nidasoshi

Convener

Prof. S. V. Manjaragi
H.O.D., Dept. of CSE

Program Coordinators

Prof. S. G. Gollagi
Prof. Mahesh G. Huddar

Advisory Committee

Dr. C. B. Akki
Professor, IIIT, Dharwad
Dr. Piyush Kumar Pareek
Professor, EWIT, Bangaluru
Dr. S. A. Alur
PG Coordinator, Dept. of ME, HIT, Nidasoshi
Dr. B. V. Madiggond
H.O.D., Dept. of EEE, HIT, Nidasoshi
Dr. V. G. Kasabegoudar
H.O.D., Dept. of ECE, HIT, Nidasoshi
Dr. B. M. Shrigiri
H.O.D., Dept. of ME, HIT, Nidasoshi
Dr. S. N. Topannavar
First Year Coordinator, HIT, Nidasoshi

Schedule of the STTP

This STTP program will be conducted in three phases from November 2020 to December 2020.

The schedules for the phases are as indicated below:

Phase 1: 23rd - 28th Nov 2020

<https://forms.gle/J8VnaD9Wb46EZGxh6>

(Last date for registration: 16th Nov 2020)

Phase 2: 7th -12th Dec 2020

<https://forms.gle/E6FAGKspQTnm4Gk87>

(Last date for registration: 30th Nov 2020)

Phase 3: 14th -19th Dec 2020

<https://forms.gle/xWi1m5Lh5dnHqgsEA>

(Last date for registration: 7th Dec 2020)

Participants can join this program by selecting the above mentioned schedules. Intimation to the participant about confirmation of Registration shall be given through mail.

Important Information for the Participants

- No Registration fee.
- This STTP is only for faculty members of the AICTE approved institutions, research scholars, PG students, participants from Government and Industry. The participants who have attendance 80% and more and also score minimum 60% in the online test will be issued a digital certificate.
- The FDP will be conducted in online mode. Participants willing to participate in this online STTP should arrange the relevant online gadgets viz., laptop/desktop/smart phone with adequate internet connectivity and other audio visual facilities, as required for online training.

• CHIEF PATRONS:

His Holiness Jagadguru Pancham
Sri Shivalingeshwar Mahaswamiji

Siddha Sansthan Math Nidasoshi

Prof. Anil D. Sahasrabudhe
Chairman, AICTE, New Delhi

• PATRONS:

Prof. M.P. Poonia

Vice Chairman, AICTE, New Delhi

Col. B. Venkat

Director, Faculty Development Cell
AICTE, New Delhi

Shri. G. M. Patil

Secretary, S.J.P.N. Trust Nidasoshi

• CONVENER:

Dr. S. C. Kamate

Principal, HSIT Nidasoshi

• PROGRAM COORDINATOR:

Dr. Basavaraj. V. Madiggond

Prof. & HOD, EEE, HSIT Nidasoshi

• ORGANIZING TEAM:

EEE Department Faculty & Staff

• CONTACT DETAILS:

Prof. Hemalata R Zinage

Prof. Mahesh P Yanagimath

Prof. Onkar B Heddurshetti

e-mail: eee.sttp@hsit.ac.in

mob: 8073512609, 9341449466, 9448420509



AICTE Sponsored
One Week Online

Short Term Training Program (STTP)

on

“Battery Management and Control
Techniques in Electric Vehicles”

Organized by



Department of
Electrical & Electronics Engineering



S J P N Trust's
Hirasugar Institute of Technology
Nidasoshi – 591236, Dist- Belagavi
Karnataka

Accredited at 'A' Grade by NAAC
Programmes Accredited By NBA: CSE, ECE, EEE & ME

About the Institute

Vision

"To be a preferred institution in Engineering Education by achieving excellence in teaching and research and to remain as a source of pride for its commitment to holistic development of individual and society"

Mission

"To continuously strive for the overall development of students, educating them in a state of the art infrastructure, by retaining the best practices, people and inspire them to imbibe real time problem solving skills, leadership qualities, human values and societal commitments, so that they emerge as competent professionals"

With the vision & foresight of His Holiness Shri Pancham Nijalingeshwar Mahaswamiji, the Ninth pontiff of Shri Siddha Sansthan Math, Nidasoshi, Shri Jagadguru Pancham Nijalingeshwar (SJPN) Trust was founded in the year 1984 with the sole purpose of imparting quality education to the needy masses. Under this trust, Hirasugar Institute of Technology, Nidasoshi was established in the year 1996. The management of HSIT is a benevolent body of individuals drawn from various walks of life. Eminent personalities from both industry & academia are overseeing the activities of institution. The college is spread over a sprawling 48.26 acres of greenery & natural surroundings. The college is affiliated to Visvesvaraya Technological University (VTU), Belagavi & approved by All India Council for Technical Education (AICTE), New Delhi & recognized by the Government of Karnataka. The institute offers 4 years BE Degree courses in 1. Electrical & Electronics Engineering 2. Mechanical Engineering 3. Electronics & Communication Engineering 4. Computer Science & Engineering. All the above mentioned courses are accredited by NBA. The Institute is accredited at "A" grade by NAAC. From AY 2020-21 a new program in Civil Engineering has been

started. The institute also runs a Post Graduate Course in Thermal Engineering. The E&E, Mechanical Engineering, E&C, and Chemistry Department have been recognized as Research Centers by V.T.U.

About the Department

The department of Electrical & Electronics Engineering was established in the year 1996 with the intake of 60. The department is accredited by National Board of Accreditation (NBA). The department is recognized as research centre by VTU, Belagavi. Department has signed MoU with HESCOM Hubballi, PATCO Transformer Industries Nipani, Renutron Power Solutions, Kolhapur and InfySky Belagavi. The Department houses world class Laboratories to satisfy the thirst of budding technocrats. We believe in holistic approach in teaching - learning methods. Dedicated and experienced faculty members provide the best services in uplifting the standards of students.

About the STTP

Electric vehicles are becoming an inevitable part in larger transportation system. They offer a cleaner mode of transportation by reducing green-house gas emissions and local air pollutants as well as energy dependence without scarifying performance. This One Week AICTE Sponsored STTP is serving to be a great platform for participants to upgrade their knowledge in the area of "Battery Management and Control Techniques in Electric Vehicles". Where-in participants will get an insight and hands-on exposure to advanced improvements, research avenues and detailed aspects of electric vehicles.

Content to be covered

- Environmental Impacts and History of Modern transportation. / Climate angle to EV.
- EV/HEV Configuration and Architecture.
- Control Applications to HEV.
- Battery Technology and Future trends.
- Battery Management Systems.
- Energy Management in EVs
- Electric Drives for EV applications.
- SRM Drives for EVs.

- Suitability of BLDC Motor for EV Application
- P. E. Converters for EVs.
- Automotive P. E. Converters for EVs.
- Safety, Testing, Regulation & Standards in EVs.
- Control Techniques for P. E. Converters.
- Modeling and Simulations of EV/HEV.
- Modeling and Simulations of PE Converter for EVs.
- Modeling and Simulations of Batteries & Balancing.
- G2V, V2G Communication
- Chargers for EVs.
- Power Electronics Applications to EV Charging Stations.
- Research Avenues in EVs.

Resource Persons

Key Note Speakers:

Dr. S. Rajashekharaiya Founder VC, VTU Belagavi

Dr. Ashok. Rao Former Head, Network Project, CEDT, IISc Bangalore

Dr. H. P. Kincha Former VC, VTU Belagavi & Ex. Chairman, Manipal University Jaipur

- Dr. H N Nagaraja Pro-VC GE University, Dehradun
- Dr. Ashok Rao, IISc, Bangalore
- Dr L Ashok Kumar PSG Tech
- Dr Bhanu Pratap NIT Kurukshetra
- Mr. Gopal Athani TATA TECHNOLOGIES
- Dr. Abhijeet Kshirsagar IIT Dharwad
- Dr Chandrasekhar P IIT Bhuvaneshwar
- Dr. Ramulu NIT Warangal
- Dr. D S More Walchand College of Engg
- Dr. Ragavan IIT Gandhinagar
- Dr. B. V. Madiggond HSIT Nidasoshi
- Dr. Satish Naik IIT Dharwad
- Dr. Sanjeevkumar R. A. PDA CoE, Gulbarga
- Dr. Suryanarayana K NMAMIT Nitte
- Dr. Pramod Naik AI Edge Innovations
- Mr. Jayaprakash Sabarad, GE Trans, Bangalore
- Dr. K. Selvajyoti, IIITDM, Kancheepuram
- Dr. Dharavath Kishan NIT, Surthkal
- Dr. Sanjeev Nayak L&T Tech. services
- Dr. D. B. Kulkarni, GIT, Belagavi
- Dr. Sreejith S. NIT Silcher



FACULTY DEVELOPMENT PROGRAMME (FDP)
ON
INTERNET OF THINGS AND ITS REAL TIME APPLICATIONS
(28th January 2019 to 02nd February 2019)



Organized by
Department of CS&E
HIRASUGAR INSTITUTE OF TECHNOLOGY, NIDASOSHI, KARNATAKA
In association with
NATIONAL INSTITUTE OF TECHNOLOGY, WARANGAL.
Under the Scheme of Electronics & ICT Academy
(Sponsored by Department of Electronics and Information Technology (DeitY), GOI)

Preamble:

"Electronics & ICT Academy" is being set up at NIT Warangal with financial assistance from DeitY, MCIT, Govt. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa States and Pondicherry and Andaman & Nicobar Islands UTs. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies; training & consultancy services for Industry; Curriculum development for Industry; CEP for working professionals; Advice and support for technical incubation and entrepreneurial activities.

The aim of this programme is to introduce the participants to the amazing world of sensor networks and other IOT platforms. IOT is the idea of making devices (namely sensors, actuators and other objects) smarter by linking them to the Internet. This FDP serves as a platform to train and educate the research scholars, faculty and people from industry to interact on cutting-edge technologies in this emerging area.

Major Course Contents

- Introduction to Wireless Sensor Networks and IOT
- Design of sensor network protocols including MAC and Routing protocols
- Sensor Web and Cloud
- Smart IoT devices and applications
- Programming with IoT devices
- Communication protocols, interconnections, system design and development
- Service Discovery and device to device communication in IOT
- Data management and big data analytics in IOT.
- Security in IOT
- Research issues and case studies in IOT.

Faculty conducting this programme:

The programme will be conducted by the faculty members from NIT Warangal. Academicians in the concerned field from IITs/NITs/IIITs are invited to deliver lectures in the programme. Speakers from industries are also expected to deliver as part of the course.

Eligibility:

The programme is open to the teachers/research scholars of Engineering Colleges, MCA Colleges and other allied disciplines in Telangana, Andhra Pradesh, Karnataka, Goa states and Pondicherry, Andaman and Nicobar Islands UTs. Industry personnel working in the concerned/ allied discipline can also attend.

Organized at:

The Programme will be coordinated and conducted by Department of Computer Science and Engineering, Hirasugar Institute of Technology, Nidasoshi, Belgaum district, Karnataka. The whole course is handled by academicians and industry experts.

Registration Fee Particulars:

Faculty members (Rs. 2500/- Only) and Industry participants (Rs. 7500/-Only)

The participants need to send a crossed demand draft (DD) drawn in favor of "Director, NIT Warangal" and payable at SBH, NIT Warangal branch.

Accommodation:

Accommodation will be provided to the selected outside participants on prior request. No TA/DA will be paid for the participants. Working Lunch, Tea & Snacks would be provided during the Training in the campus.

Venue: Seminar Hall & CSE Labs, HSIT

How to apply:

A filled-in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the host institute coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through email parashuram.baraki@gmail.com as selection will be intimated only through mail. Program duration is 6 (six) working days.

Selection Criteria:

Selection will be done based on **first-come-first-serve basis to a maximum number of 50 (fifty)**. Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the DD will be sent back. Candidates will be issued certificates on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

Important dates:

Last date (Application & DD)	23 rd January 2019
Selection List by E-mail	25 th January 2019
Duration	28 th January to 02 nd February 2019

About the Institutes:

National Institute of Technology (formerly Regional Engineering College), Warangal is an Institute of National Importance and fully funded by MHRD, Govt. of India. It offers various programmes B.Tech. in eight branches, PG and M.Tech. Programmes and also Ph.D. programmes.

About HSIT:

Hirasugar Institute of Technology (HSIT) was established in the year 1996, by SJPN Trust Nidasoshi of Belgaum district. The Institute is Approved by AICTE, Recognized by Govt. of Karnataka, Affiliated to VTU Belagavi and Recognized Under Section 2(f) of UGC Act. 1956. It has undergraduate programmes in Departments of Computer Science and Engineering (CSE), Electronics and Communications Engineering (ECE), Electrical and Electronics Engineering (EEE), Mechanical Engineering (ME), Post graduation programme M.Tech(Thermal Engg.) in Mechanical Engineering department and Research Centre in ECE,EEE, ME & Engg. Chemistry departments.

Dr.S.C.Kamate is the Principal.



APPLICATION FORM
FACULTY DEVELOPMENT PROGRAMME (FDP)
ON
INTERNET OF THINGS AND ITS REAL TIME APPLICATIONS
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Department of CS&E
HIRASUGAR INSTITUTE OF TECHNOLOGY, NIDASOSHI, KARNATAKA
in association with
NATIONAL INSTITUTE OF TECHNOLOGY, WARANGAL.
Under the Scheme of Electronics & ICT Academy
(Sponsored by Department of Electronics and Information Technology (DeitY), GOI)

1. Name :
2. Designation :
3. Institution :
4. Email :
5. DD No: Bank: Date:
6. Address for Correspondence

Dr/Mr./Ms. is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on "Internet of Things and its Real Time Applications" sponsored by Electronics & ICT Academy during 25th March– 31st March 2017 at Hirasugar Institute of Technology, Nidasoshi, Belgaum district, Karnataka.

Signature of Head of Institution
(with seal)

7. Educational Qualification:
8. Subjects taught so far:
9. No. of refresher courses/workshops attended:
10. Experience (in years):
Teaching: Research: Industry:
11. Accommodation required: YES / NO
12. Do you belong to SC/ST: YES/NO

Address for correspondence
Post your application form with DD to
Dr. Parashuram Baraki
Professor & HOD
Coordinator for FDP on IoT and Its Real Time Applications
Department of CS&E,
Hirasugar Institute of Technology,
Nidasoshi, Belgaum district,
Karnataka.– 591 236

E-mail the scanned copies of filled-in and duly signed application form along with DD to parashuram.baraki@gmail.com

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

For more details about Electronics & ICT Academy, NITW, please visit: <http://nitw.ac.in/eict>

*For more enquiries please contact:
Mobile: +91 9686042385
E-mail: parashuram.baraki@gmail.com*

Signature of the Applicant

SPONSORSHIP CERTIFICATE

Coordinators

Dr. Rashmi Ranjan Rout
Department of CS&E
National Institute of Technology
WARANGAL -506 004

Dr. Parashuram Baraki
Professor & HOD
Department of CS&E,
Hirasugar Institute of Technology,
Nidasoshi, Karnataka- 591236