

Ref - TFT/INT/2018/146

10<sup>th</sup> August 2018



**TO WHOM IT MAY CONCERN**

This is to certify that Ms. Pallavi Patil USN-2HN16CS401 a student of Hirasugar Institute of Technology Nidasoshi, Under VTU Belagavi has successfully completed her Internship in Our Organization "Tech Fortune Technologies" from 11<sup>th</sup> July 2018 to 10<sup>th</sup> August 2018.

She was working on the Facebook Chat with ESP 8266-IOT Project using Node MCU. During the period of her internship programme with us she was found punctual, hardworking and inquisitive.

We wish her all the very best for a successful career ahead.

For Tech Fortune Technologies

Supervisor

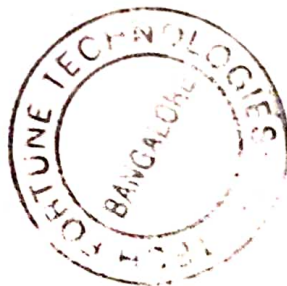
S. V. Soranalli

Manjushree.M

Tech Director- Tech Fortune

Authorised Signatory

Place: Bangalore



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