	S J P N Trust's	MECH. ENGG
	Hirasugar Institute of Technology, Nidasoshi.	NAAC
	Inculcating Values, Promoting Prosperity	Activities
	Approved by AICTE and Affiliated to VTU Belagavi. &	
	Accredited at "A" Grade by NAAC and Recognized Under Section 2(f) of UGC Act,1956	2018-19

Conducted Training Program on CATIA and GD & T software Tools

Date of activity held	From 21-01-2019 To 07-02-2019
No. of Days	15
Time	9.00 AM to 05.00 PM
Type of Activity	Departmental Level Certificate Programme
Resource Person	Mr. Shivanand Daroji
Professional details of resource person	Technical Head, CADD Centre Tanning Services Private Limited, Belagavi.
Year/Class	All the students
No. of students	27
Activity In-charge	Prof: Mahantesh Tanodi

Activity Description:
  ➢ To awareness on CATIA and GD& T software.
  ➢ To awareness on computer aided 3D interactive applications and use of physical modeling in industries.
  ➢ To awareness on part design, assembly, drafting, wireframe and other work benches.

Activity Photograph:

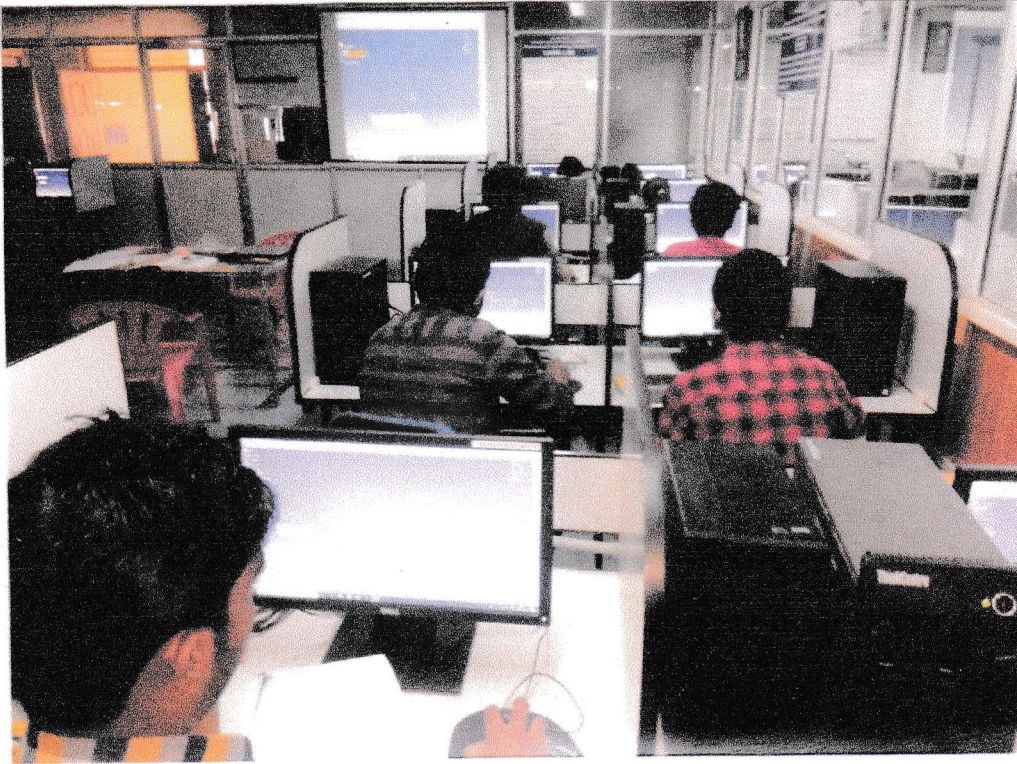| **Budget Details:**	Students Contribution

Photo showing students attending the CATIA and GD & T Training Session

Photo showing MOU between ME Department and CADD Centre Training Services Private Limited, Belagavi

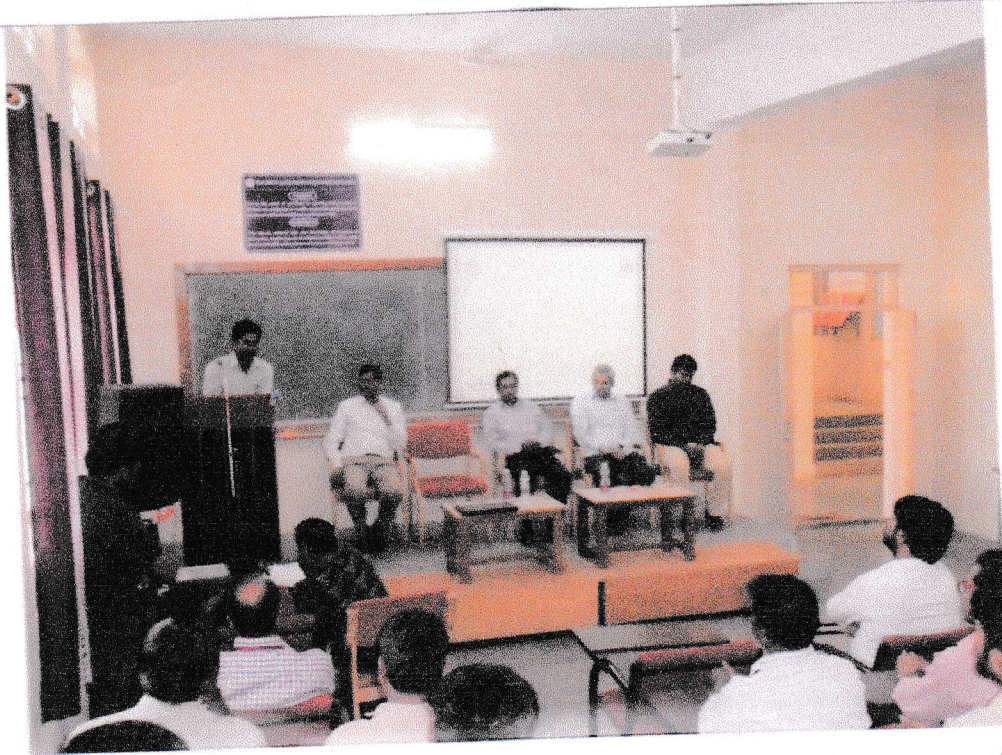HOD
Mechanical Engg.
HIT, Nidasoshi

Photo showing certificate distribution ceremony of CATIA and GD & T Training programme function

Photo showing student receiving certificate during certificate distribution ceremony of CATIA and GD & T
Training programme

HOD
Mechanical Engg.
HIT, Nidasoshi

INSTITUTIONAL COURSE
CERTIFICATE OF COMPLETION

CADD CENTRE

Skills Driven Job Led

QUALITY FIRST

Awarded to : Basavaraj Vajjaramatti

In : GD & T

At : HIT Nidasoshi Engineering College
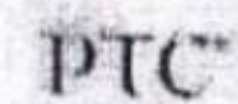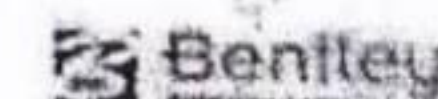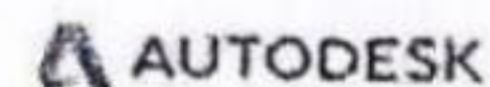
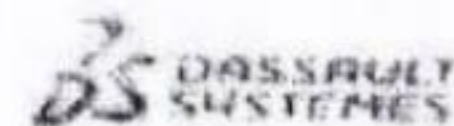By : CADD Centre Training Services, Belgaum, Nehru Nagar

During : Feb-2019   Duration : 16 Hrs   Student ID: C190147657

Ketaki Anteshwar Narvane

18-02-2019

MANAGING DIRECTOR   CENTRE HEAD   DATE OF ISSUE

DASSAULT SUSTEMES   AUTODESK   ANSYS   Bentley   PTC

The course is offered by CADD Centre on the software developed by the respective companies. All brand names and trademarks belong to respective owners. Institutional Course are specially designed for meeting the academic curriculum requirements. The course offered at the institutes, by qualified trainers and supporting study materials.

Corporate Office : 8" Floor, GEE GEE Crystal, Office No. 8C & 8D, #91, Dr. Radhakrishnan Salai, Mylapore, Chennai-600 004, India.

www.caddcentre.com

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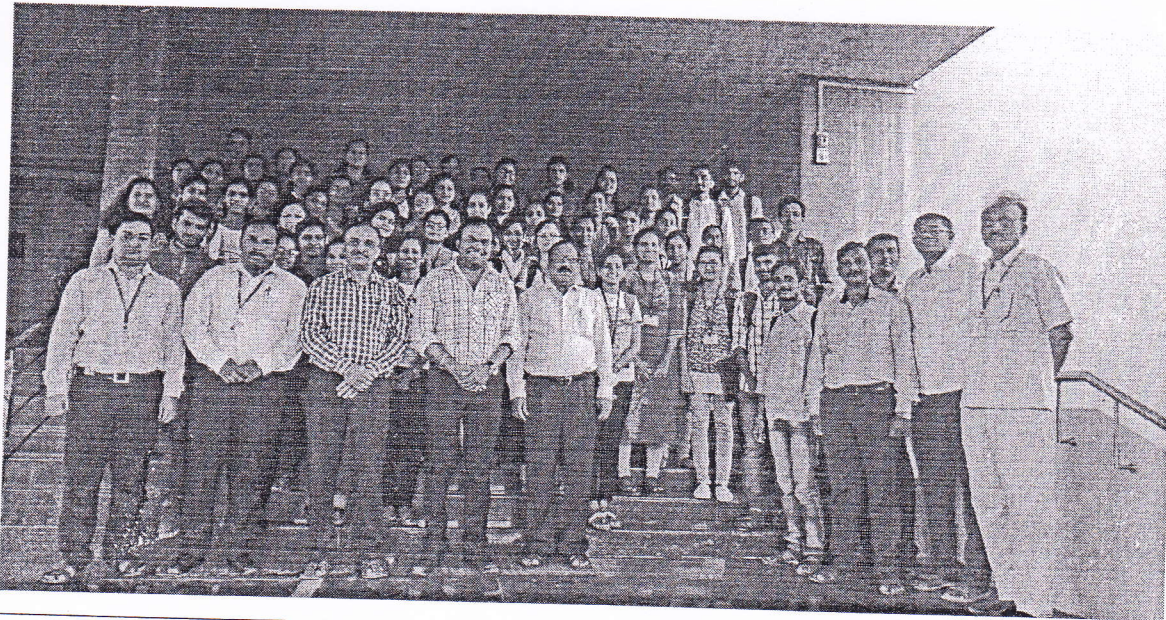
Department of Computer Science and Engineering

Date of Activity held	07-09-2018 to 16-11-2018
Time	2.00 PM to 6 PM
Type of Activity (Cultural/Curricular/Co-Curricular)	Co-Curricular
Resource Person	* Mr. Muralidhar Deshpande and ** Mr. Ashok Wathore
Professional Details Of Resource Person	* Director, GRP, Infokyam, Bangalore ** Director Avirat Knowledge Systems, Pune
Year/ class	5th and 7th Semester CSE Students
No. of students	72
Activity in-charge	Dr. Parashuram Baraki

Description of Activity:

Department has organized 30 hours training program on "Artificial Intelligence and Machine Learning" for 5th and 7th semester students of Computer Science and Engineering department. This activity is scheduled from 07-09-2018 to 16-11-2018. Mr. Muralidhar Deshpande and Mr. Ashok Wathore introduced the concepts of artificial intelligence and machine learning in simple terms. Students practiced machine learning algorithms using R tool.

Activity photographs:

STAC Coordinator

HOD
H.O.D
Computer Science & Engg.
HIT, Nidasoshi.

Certificate
of Course Completion

SI 2018

Shradha Infotech

No 1756, 5th Floor, Shanbhag Chambers, Kirloskar Road, Belagavi -590002

E mail: shradha_infotech1@yahoo.co.in

This is to certify that Mr./Miss. _Shashikala Hulakund_

of _I_ sem CSE, has undergone 30 hours course on **"Artificial Intelligence and Machine Learning"**

conducted during the academic year 2018-19, in Association with Computer Science and Engineering Department,

Hirasugar Institute of Technology, Nidasoshi.

Mr. Sanjeev G. Bhadale

Dr. Parashuram Baraki

Dr S C Kamate

	S J P N Trust's	ECE Dept.
	Hirasugar Institute of Technology, Nidasoshi.	IIIC, Alumni
	Inculcating Values, Promoting Prosperity	Workshop
	Approved by AICTE, New Delhi Recognized by Govt. of Karnataka and Affiliated to VTU Belagavi.	
	Accredited at "A" Grade by NAAC and Recognized Under Section 2(f) of UGC Act, 1956	2018-19 (Even)

Date of Activity held and Time:	22/02/2019 to 24/02/2019 **8.00 a.m to 6.00 p.m**
Name of Activity:	"IoT Workshop using NodeMCU"
Type of Activity: (cultural/curricular/co-curricular)	Co-curricular
Resource Person/Invitee:	Mr. Shitalkumar Borganve
Professional Details of Resource Person:	Senior Technical Lead, HCL Bangalore.
Year / Class:	6th Sem and 8th Sem Students of ECE Dept.
No. of students:	44
No. of Staff:	-
Activity In charge:	Prof. D. M. Kumbhar, Prof. N. M. Patel and Prof. D. B. Madihalli

Description of Activity: An "IoT Workshop using NodeMCU" was jointly organized by Institute Industry Interaction Cell (IIIC) ECE Department and Alumni Association HIT, Nidasoshi for 6th and 8th Semester ECE students from Friday 22nd to 24th, February 2019. Resource person was Mr. Shitalkumar Borganve, Senior Technical Lead, HCL Bangalore.

The workshop focused on cutting edge technology "Internet of Things (IOT)". It provided hands on exposure about interface of various sensors and driver circuit with embedded system and then it is connected to real word things through internet.

The workshop was inaugurated by Dr. V. G. Kasabegoudar, HOD ECE Department, he encouraged students to participate such activities to enhance their knowledge beyond syllabus. Resource person Mr. Shitalkumar Borganve introduced embedded system architecture and IoT outline in first session. Afternoon session he explained about Arduino IDE and its features. Second day students interfaced various sensors with NodeMCU. On third day participates developed a simple project of home appliance control i.e. lamp control and fan control. Last session was an interactive one and students gave their feedback.

IIIC Convener   Alumni Association Coordinator   HOD
 Electronics & Commn. Engg. Dept.
 HIT, Nidasoshi   PRINCIPAL
 HIT, Nidasoshi

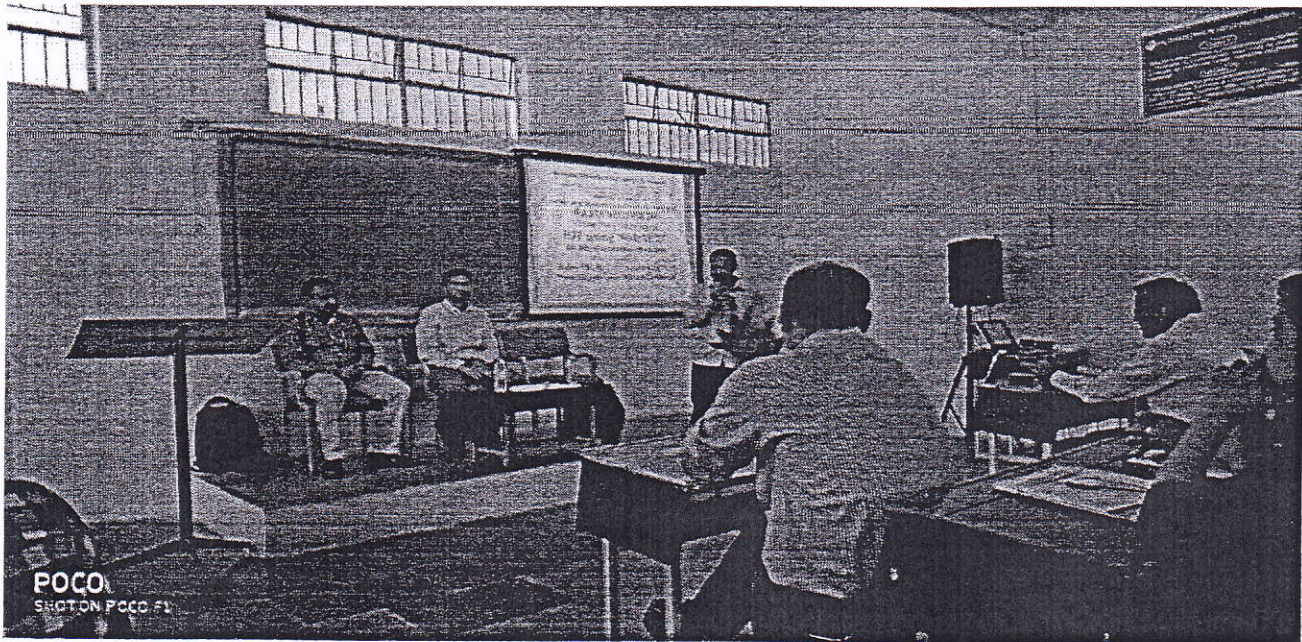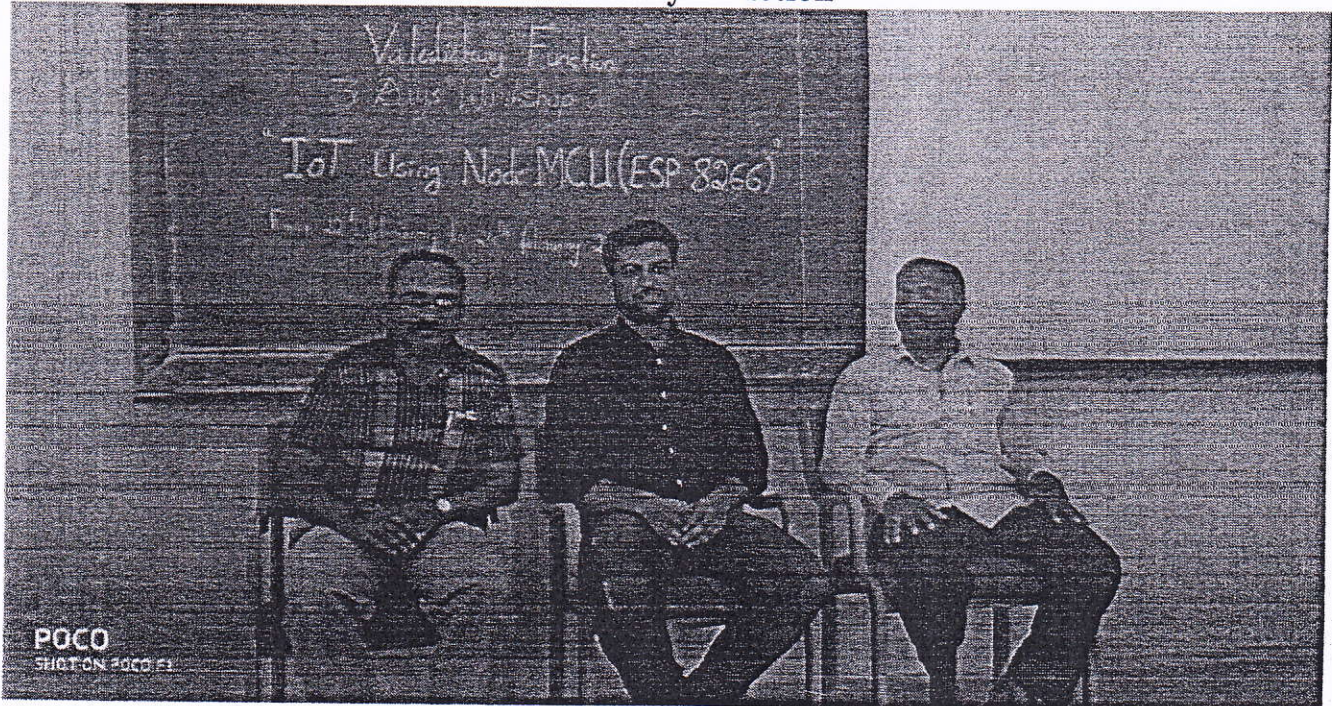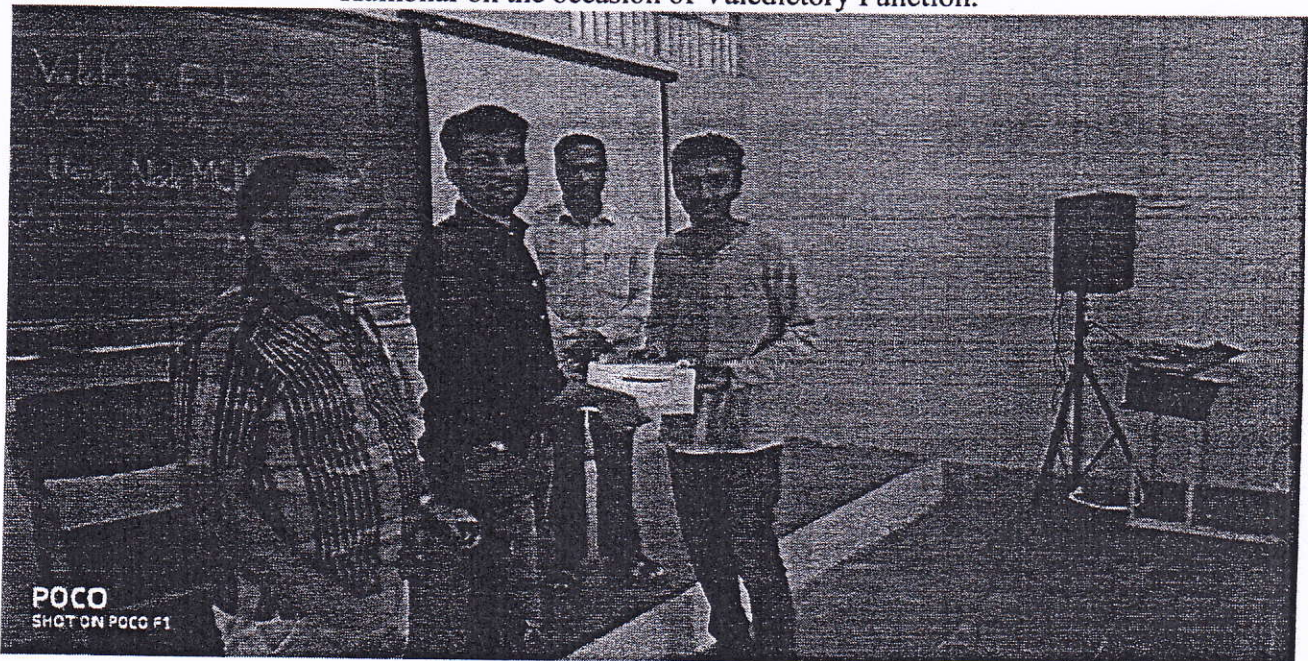|	S J P N Trust's	ECE Dept.
	Hirasugar Institute of Technology, Nidasoshi.	IIIC, Alumni
	Inculcating Values, Promoting Prosperity	Workshop
	Approved by AICTE, New Delhi Recognized by Govt. of Karnataka and Affiliated to VTU Belagavi.	
	Accredited at "A" Grade by NAAC and Recognized Under Section 2(f) of UGC Act, 1956	2018-19 (Even)

Inauguration of "IoT workshop using NodeMCU"

Presidential remarks by Dr. V. G. Kasbegoudar HOD ECE Dept. at the time of inauguration of workshop on "IoT Workshop using NodeMCU".

Students attending introductory session of IoT workshop.

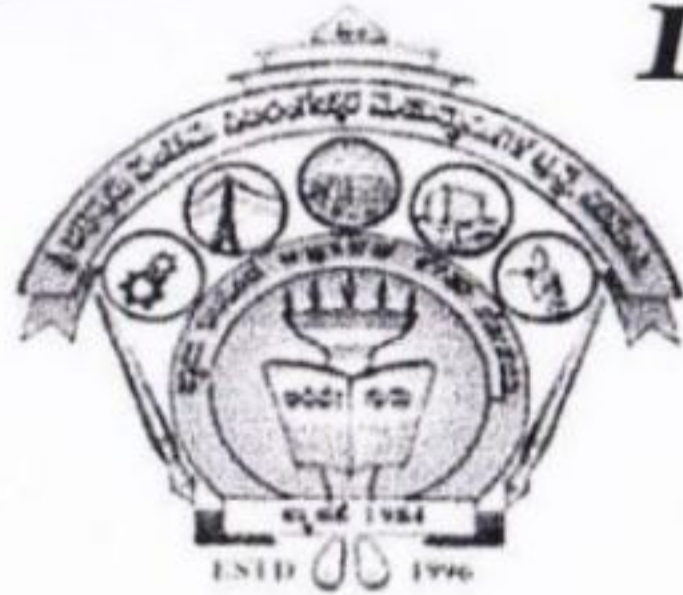|	S J P N Trust's	ECE Dept.
	Hirasugar Institute of Technology, Nidasoshi.	IIIC, Alumni
	Inculcating Values, Promoting Prosperity	Workshop
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Valedictory Function

HOD Dr. V. G. Kasbegoudar, resources person Mr. Shitalkumar Boreganv and IIIc coordinator Prof D M Kumbhar on the occasion of Valedictory Function.

Workshop participation certificate distribution by resource person Mr. Shitalkumar Boreganve.

IIIC ECE Dept, Alumni Association HIT Nidasoshi
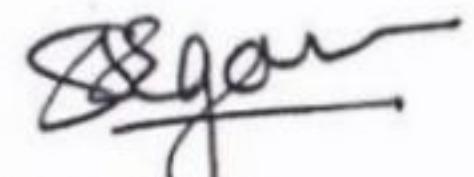
And

Youth Innovations Bangalore

Jointly Organised Three Days Workshop on
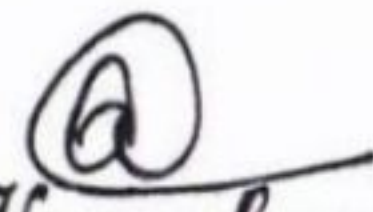
INTERNET OF THINGS BASED ON NodeMCU

Certificate of Participation

This is to Certify that

Mr/Mrs _Sushmita . S. Masadi_

of _VI_ Sem ECE department HSIT NIDASOSHI, has actively participated in Three days workshop on "IoT based on NodeMCU" by YOUTH INNOVATIONS from 22nd to 24th Feb 2019 held at Hirasugar Institute of Technology, Nidasoshi.

Mr. Shital Borganve
Resource Person

Dr. V. G. Kasabegoudar
HOD

Dr. S. C. Kamate
Principal

Youth Innovations

Date of Activity held and Time:	01/03/2019 to 03/03/2019 8.00 am to 6.00 pm
Name of Activity:	Workshop on "PCB Designing and Testing"
Type of Activity: (cultural/curricular/co-curricular)	Co-curricular
Resource Person/Invitee:	Mr. Tulasidas Salunkhe
Professional Details of Resource Person:	CEO Electrosal Hi Tech Pvt. Ltd. Nipani
Year / Class:	4th Sem and 6th Sem Students of ECE Dept.
No. of students:	41
No. of Staff:	--
Activity In charge:	Prof. D M Kumbhar, Prof. S S.Ittannavar, Prof.S.S.Patilj

Description of Activity: A workshop on "PCB Designing and Testing" was arranged by ECE Department for 4th and 6th Semester ECE students from Friday 1st to 3rd March which was delivered by Mr. Tulasidas Salunkhe, Electrosal Hi Tech Pvt. Ltd. Nipani. In this workshop he gave tricks on PCB designing by using eagle software.

The workshop focused on the basics of PCB designing tools. Students used Eagle PCB tool and completed various activities based on PCB designing. The same tool can be used for designing the schematic layout of the circuit which can be then converted into an equivalent PCB design. At the end, students successfully completed their PCB designs. After the session on design, the participants were taught PCB fabrication process and the steps involved in PCB designing printing, etching, drilling, component mounting, soldering and testing of circuits. Further these designed circuits were printed on the copper clads for the fabrication process. Next session students did component mounting, soldering and testing. In one of the sessions he highlighted on how to generate a Gerber file. Last session was an interactive one and also gave their feedback.

Objectives	RBT level	PO	PSO
To learn software tool for PCB design, and to fabricate PCB layout, component mounting, soldering and testing.	L5	1,2,5,9,10,12	2

IIIC Convener	ISTE Coordinator	ECSA Coordinator	HOD Electronics & Commn. Engg. Dept. HSIT NIDASOSHI

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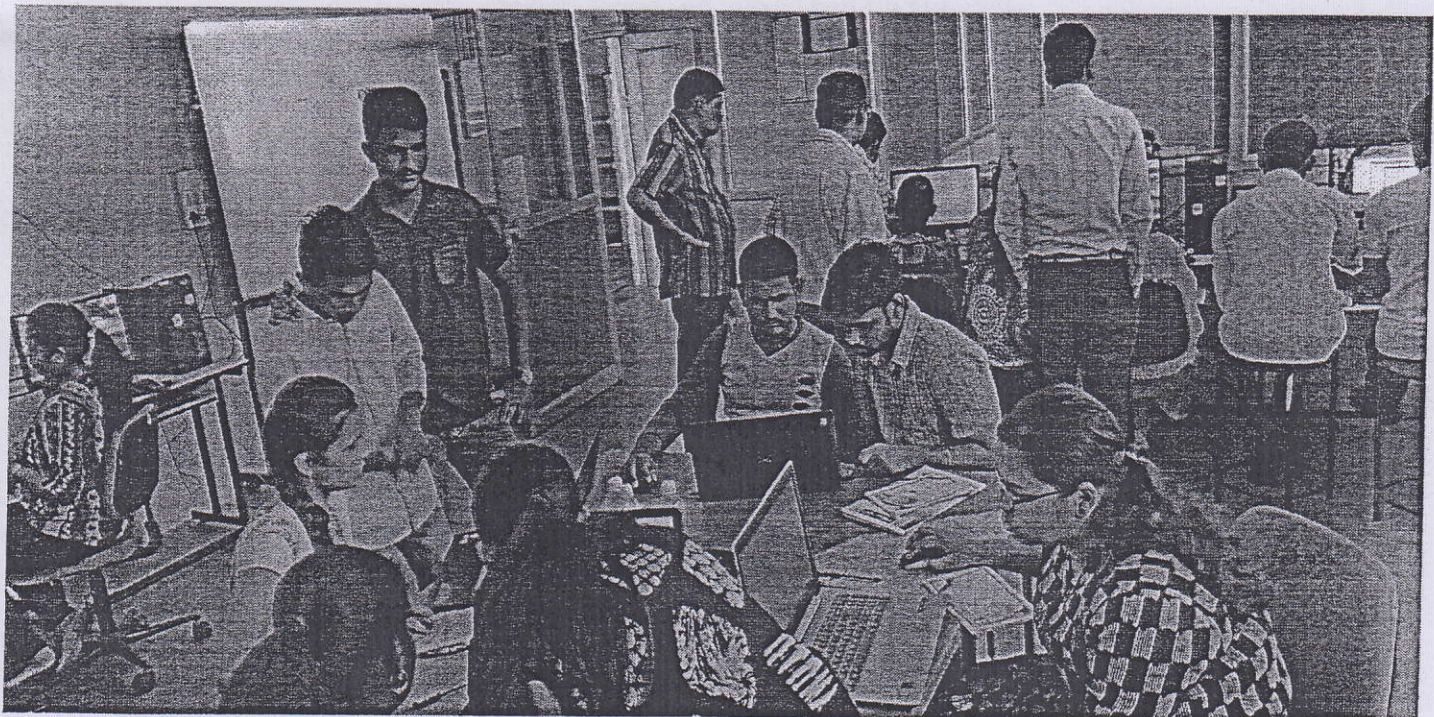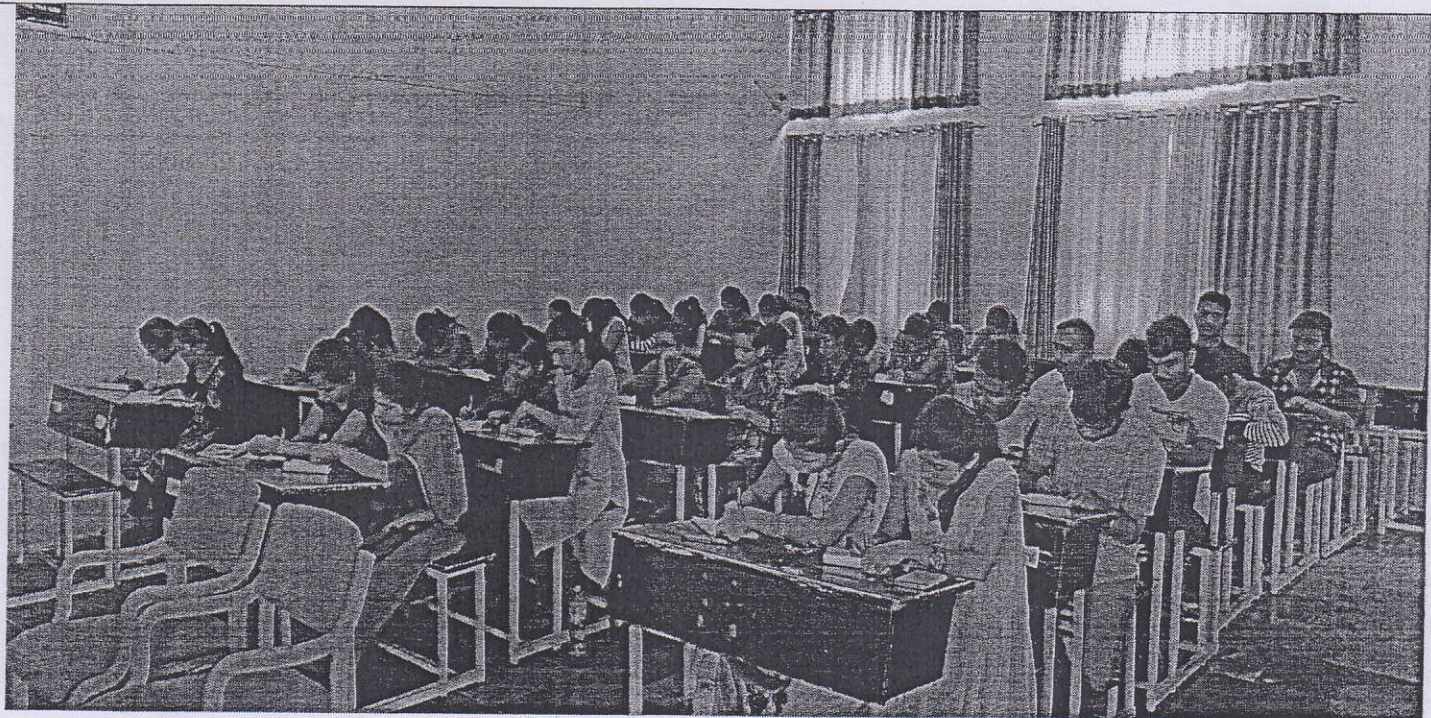
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| ECE Dept. |
| IIIC, ISTE |
| Workshop |
| 2018-19 (Even) |

Students designing their own PCB by using Eagle tool for given power supply design.

	S J P N Trust's	ECE Dept.
	Hirasugar Institute of Technology, Nidasoshi.	IIIC, ISTE
	Inculcating Values, Promoting Prosperity	Workshop
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	Accredited at "A" Grade by NAAC and Recognized Under Section 2(f) of UGC Act, 1956	2018-19 (Even)

Inauguration of "PCB Design and Testing"

Presidential remarks by our beloved Principal Dr. S C Kamate at the time of inauguration of workshop on "PCB Design and Testing"

Mr. Tulasidas Salunkhe guiding students to design PCB layout.

S J P N Trust's

Hirasugar Institute of Technology, Nidasoshi.

Inculcating Values, Promoting Prosperity

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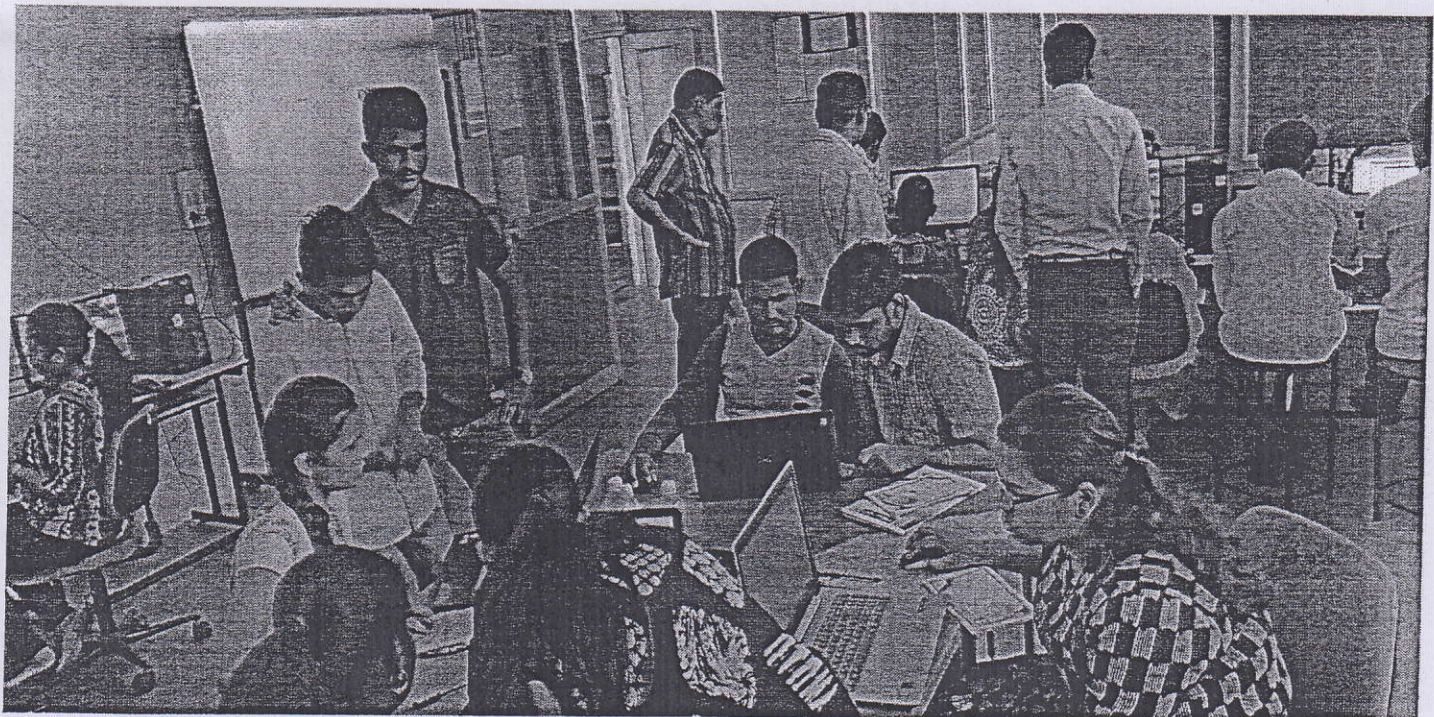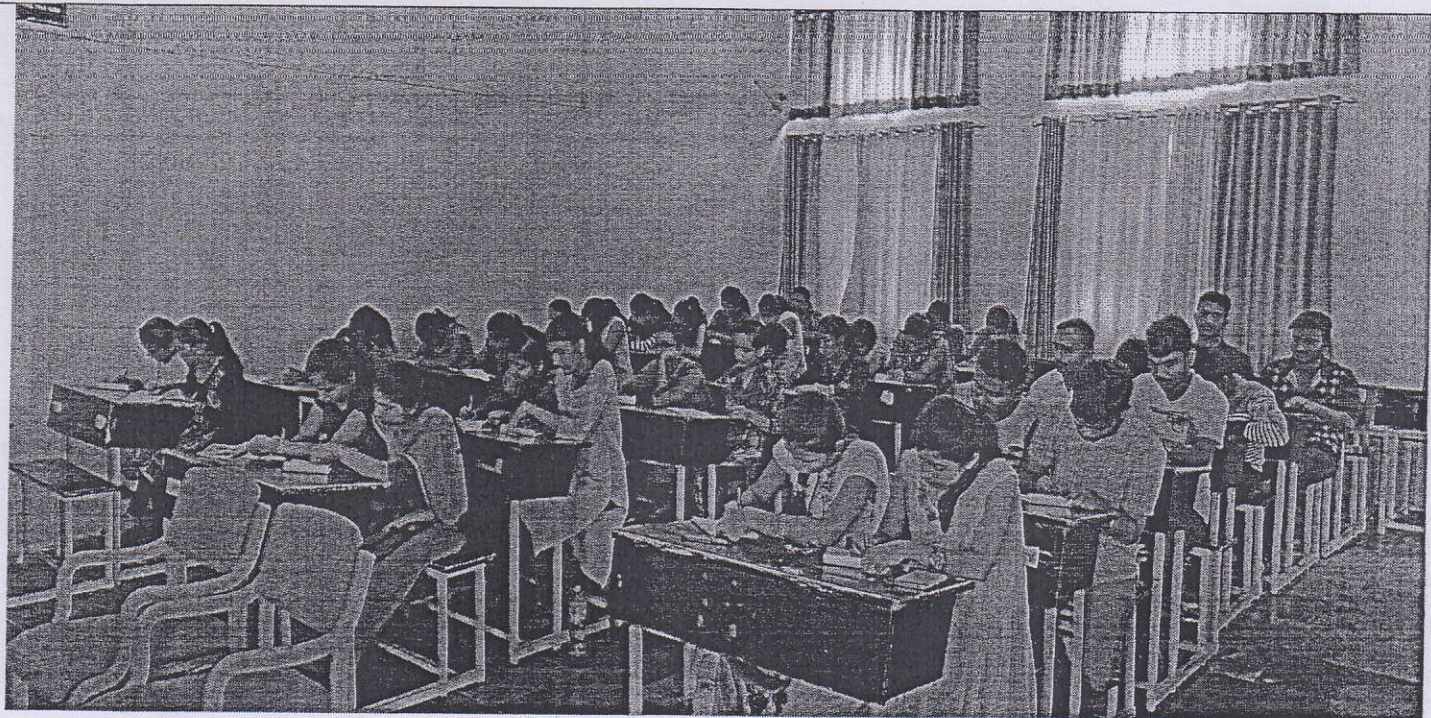
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| ECE Dept. |
| IIIC, ISTE |
| Workshop |
| 2018-19 (Even) |

Students designing their own PCB by using Eagle tool for given power supply design.

S J P N Trust's
Hirasugar Institute of Technology, Nidasoshi.
Inculcating Values, Promoting Prosperity
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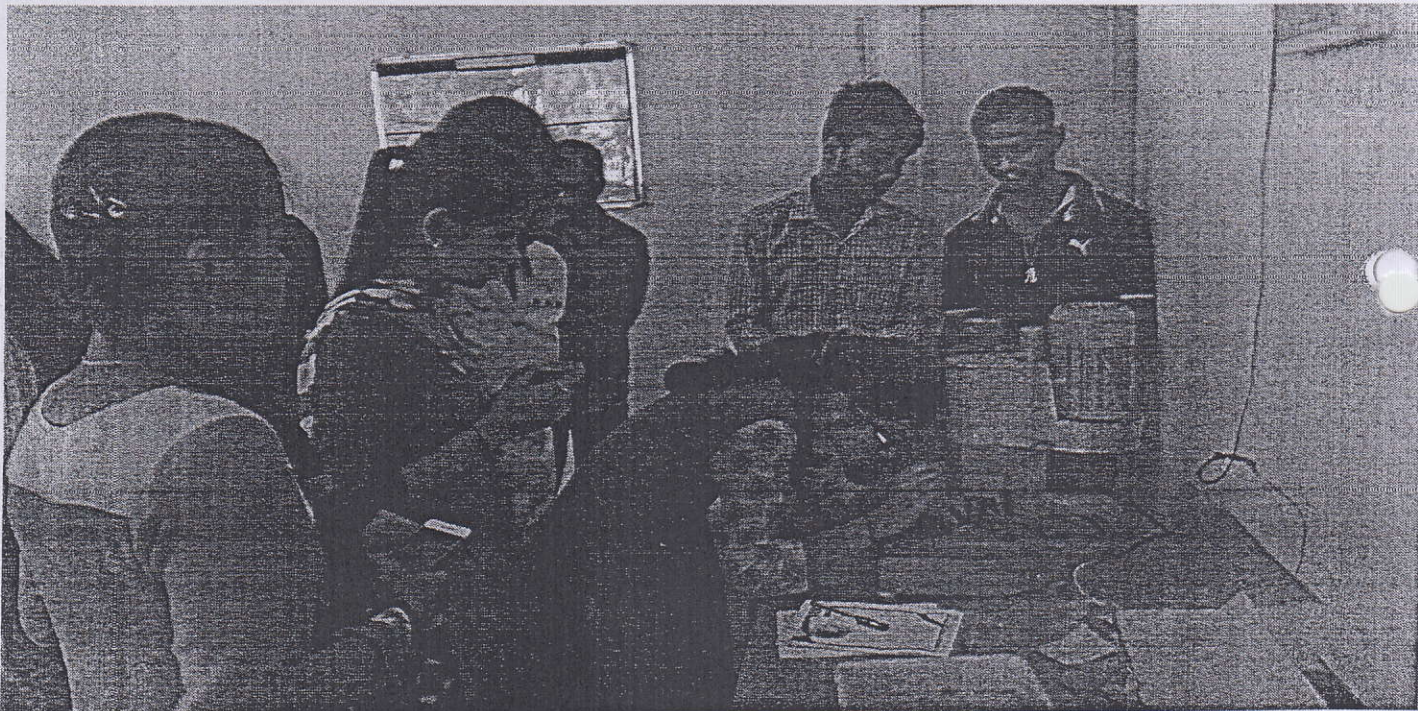Accredited at "A" Grade by NAAC and Recognized Under Section 2(f) of UGC Act, 1956

| ECE Dept. |
| IIIC, ISTE |
| Workshop |
| 2018-19 (Even) |

Pressing printed design on copper clad and etching process.

Participants learning drilling process.

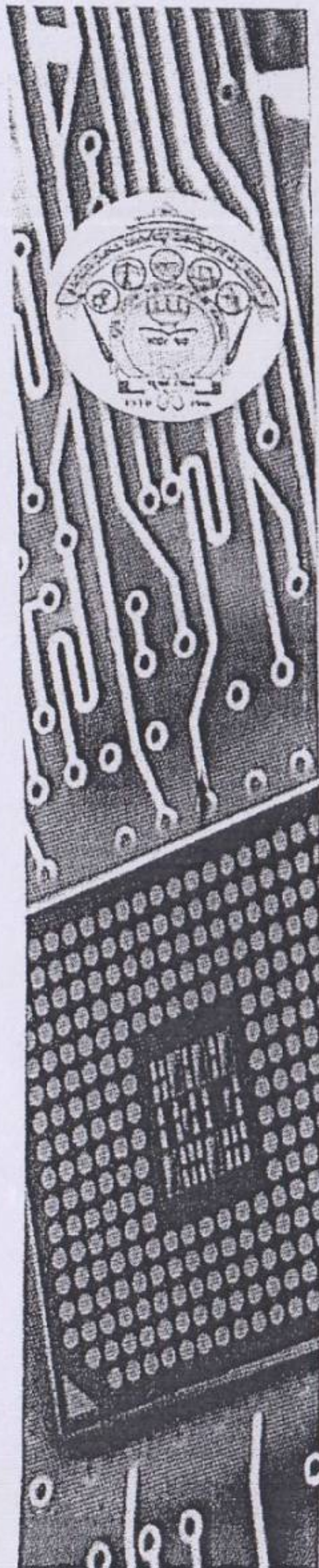|		
	S J P N Trust's	**ECE Dept.**
	Hirasugar Institute of Technology, Nidasoshi.	IIIC, ISTE
	Inculcating Values, Promoting Prosperity	Workshop
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Valedictory Function

Presenting flowers to Mr. Tulasidas Salunkhe CEO Electrosal Hi Tech Pvt. Ltd. Nipani, on the occasion of Valedictory Function.

Workshop participation certificate distribution by resource person Mr. Tulasidas Salunkhe.

S.J.P.N. Trust's

HIRASUGAR INSTITUTE OF TECHNOLOGY
NIDASOSHI-591236

(Approved by A.I.C.T.E., New Delhi and Affiliated to VTU, Belagavi, Karnataka)
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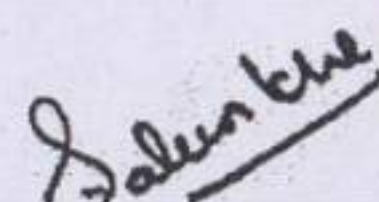
Department of Electronics and Communication Engineering
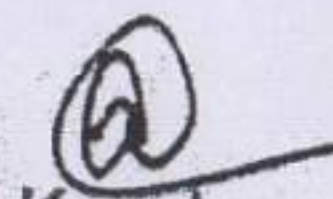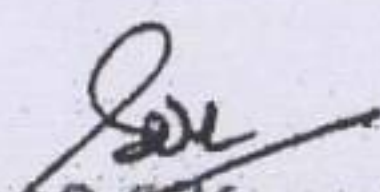In Association with ECSA, IIIC & ISTE Student Chapter

Three Day's Workshop On

"PCB Design & Testing"

Certificate

This is to certify that Mr./Miss _Nityashru P. Patil_

of _4th Sem ECE Dept. HIT Nidasoshi_ has actively

participated three days' workshop on "PCB Design & Testing" Conducted by Electrosal HiTech

Pvt. Ltd. Nippani on 1ˢᵗ to 3ʳᵈ March 2019 at Hirasugar Institute of Technology, Nidasoshi.

Mr. T D Salunkhe
CEO Electrosal Pvt. Ltd. Nippani

Dr. V G Kasabegoudar
HOD

Dr. S C Kamate
Principal

FOUNDER PRESIDENT