

ELECTROSAL HI-TECH PVT. LTD.

CIN : U32303KA2013PTCO72576

Regd. Office : No. 238, At. Shripewadi Post. Nangnur - 591241 Tal. Chikodi Dist. Belgaum (Karnataka)
Ph. No. (08338) 223310 Mob. 08880027297. www.electrosal.com email : electrosalhitech@gmail.com.

Ref. No. EHT 18/23.

Date : 14 / 05 / 2018

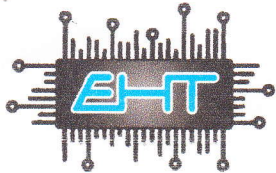
MEMORANDUM OF UNDERSTANDING

This Memorandum of Understanding entered into on 14/05/2018 at Nipani between the Hirasugar Institute of Technology, Nidasoshi, an Institution imparting and offering four year B.E degree courses in the discipline of i) Computer Science & Engineering ii) Mechanical Engineering iii) Electrical and Electronics Engineering iv) Electronics and Communication Engineering., under Visvesvaraya Technological University (VTU) Belgaum, Karnataka & is approved by AICTE New-Delhi, Hereinafter referred to as "**HSIT Nidasoshi**" located at Nidasoshi - 591 236, in this MOU, represented by Dr. S. C. Kamate, Principal and Prof. V. G. Kasagegoudar H.O.D (E&CE) of the Institute which expression shall unless repugnant to the context means and include its successors and assignees of the **ONE PART**.

Electrosal Hi Tech Pvt. Ltd. with registered no: U32303KA2013PTCO72576 functioning at Nipani, since 2013, currently focusing on develop the electronics projects, and to offer advanced electronics technical solutions to the industries, colleges and technical students is represented herein by Shri. Tulshidas Salunke, Managing Director, hereinafter referred to as Electrosal Hi Tech PVT. LTD., which expression shall unless repugnant to the context mean and intrude its successors and assignees of the **OTHER PART**.

WHEREAS

The **HSIT Nidasoshi** has initiated to promote Industry Institute Interaction to conduct various Industries/Job oriented training programmes/placements to the students, Mutual sharing of resource persons and students for advanced/innovative/future technologies/research activities as well as industries technical related problems and to know industry work-culture/updates along with the regular academic work.



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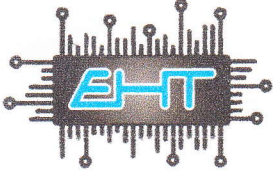
Date : / / 20

The parties have had preliminary discussions in this matter and have ascertained areas of broad consensus. The parties now, have therefore, agreed to enter in writing these areas of consensus, under a Memorandum of Understanding.

Now this Memorandum of Understanding witnesses that:

The parties have following areas of consensus for mutual benefits.

- 1) **Human Resource Sharing:** Visiting HSIT Nidasoshi sponsored course lectures/seminars/workshops/conferences related to new trends/updates in industry by Electrosal Hi Tech PVT. LTD. representatives as a visiting faculty/participant and to suggest new ideas to reduce technological gap between Electrosal Hi Tech PVT. LTD. and HSIT Nidasoshi. Similarly attending Electrosal Hi Tech PVT. LTD. organized training/workshop/conference by HSIT Nidasoshi faculty/students to upgrade/understand the industrial requirements and to implement the same in the regular academic activities through conducting industry need based trainings/talks.
- 2) **Industry Visit:** Providing an opportunity to the HSIT Nidasoshi student community to understand /monitor the live functioning of the Electrosal Hi Tech PVT. LTD. to ignite/create new ideas in the HSIT Nidasoshi students' minds' by demonstrating Electrosal Hi Tech PVT. LTD. resource persons.
- 3) **Internship/Training:** Inclusion of HSIT Nidasoshi student community in the internships/trainings conducted by Electrosal Hi Tech PVT. LTD. on regular basis during HSIT Nidasoshi students' vacation/holidays to get industry experience and to study the academic subjects/labs/projects in the style of industries requirements.
- 4) **Solving Industries Technical problems:** Identification and accounting of the Electrosal Hi Tech PVT. LTD. technological problems by Electrosal Hi Tech PVT. LTD. /HSIT Nidasoshi faculty/students and providing solutions in stipulated time as a project work by HSIT Nidasoshi faculty/student community.



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5) **Recruitments:** Intimating vacancies with numbers required, eligibility criteria, designation and abilities required for the post by Electrosal Hi Tech PVT. LTD. and facilitating best suitable candidates by HSIT Nidasoshi to the interview/selection process by comprehensively preparing candidates according to the need of the Electrosal Hi Tech PVT. LTD.. The HSIT Nidasoshi cordially invites Electrosal Hi Tech PVT. LTD. for in campus selection process or make all arrangements to send HSIT students to Electrosal Hi Tech PVT. LTD. said place for the selection process.

Responsibilities of the HSIT Nidasoshi

- Conduct/host/sponsor workshops/seminars in the HSIT Nidasoshi.
- Facilitate quality Faculty/students to the Electrosal Hi Tech PVT. LTD. for above said process/events.
- Providing hospitality/logistics to the Electrosal Hi Tech PVT. LTD. resource persons for workshops/seminars/recruitment process by HSIT Nidasoshi.
- Taking all precautionary actions for industrial visit by the HSIT Nidasoshi students.

Both parties here mutually understand the above said areas of consensus in the view of mutual benefits.

For Hirasugar Institute of Technology
Nidasoshi.

For Electrosal Hi Tech PVT. LTD.
Nipani.

1) Dr. S. C. KAMATE

PRINCIPAL
HIT, Nidasoshi.

1) Shri Tulshidas Salunke

CEO and MD

ELECTROSAL HI-TECH PVT. LTD.

MANAGING DIRECTOR
TULSIDAS SALUNKHE

2) Prof. V. G. KASABEGUDAR

H.O.D. (E) (HOD)

Electronics & Commn. Dept.
HIT NIDASOSHI



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Hirasugar Institute of Technology, Nidasoshi.

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ECE Dept.

IIIC, ISTE

Workshop

2018-19 (Even)

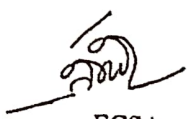
Date of Activity held and Time:	01/03/2019 to 03/03/2019 9.00am to 5.00pm
Name of Activity:	Workshop on "PCB Designing and Testing"
Type of Activity: (cultural/curricular/co-curricular)	Co-curricular
Resource Person/Invitee:	Mr. Tulasidas Salunkhe
Professional Details of Resource Person:	CEO Electrosal Hi Tech Pvt. Ltd. Nipani
Year / Class:	4 th Sem and 6 th Sem Students of ECE & EEE Dept.
No. of students:	41
No. of Staff:	-
Activity In charge:	Prof. D M Kumbhar, Prof. S S Ittannavar, Prof. S S Patil

Description of Activity: A workshop on "PCB Designing and Testing" was arranged by ECE Department for 4th and 6th Semester ECE students from Friday 1st to 3rd March 2018 which was delivered by Mr. Tulasidas Salunkhe, Electrosal Hi Tech Pvt. Ltd. Nipani. In this workshop he gave tricks on PCB designing by using eagle software.

The workshop focused on the basics of PCB designing tools. Students used Eagle PCB tool and completed various activities based on PCB designing. The same tool can be used for designing the schematic layout of the circuit which can be then converted into an equivalent PCB design. At the end, students successfully completed their PCB designs. After the session on design, the participants were taught PCB fabrication process and the steps involved in PCB designing printing, etching, drilling, component mounting, soldering and testing of circuits. Further these designed circuits were printed on the copper clads for the fabrication process. Next session students did component mounting, soldering and testing. In one of the sessions he highlighted on how to generate a Gerber file. Last session was an interactive one and also gave their feedback.


IIIC
Convener


ISTE
Convener


ECSA
Coordinator


HOD
Electronics & Commn. Engg. Dept.
HSIT NIDASOSHI


PRINCIPAL
HIT, Nidasoshi.



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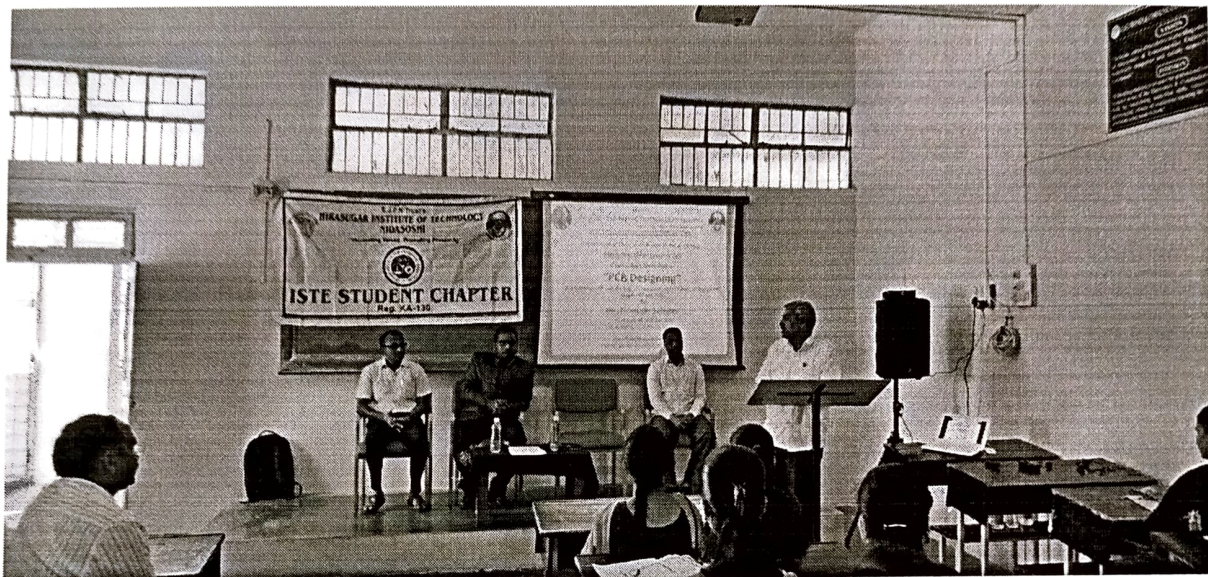
ECE Dept.

IIIC, ISTE

Workshop

2018-19 (Even)

Inauguration of "PCB Design and Testing"



Presidential remarks by our beloved Principal Dr. S C Kamate at the time of inauguration of workshop on "PCB Design and Testing"



Mr. Tulasidas Salunkhe guiding students to design PCB layout.



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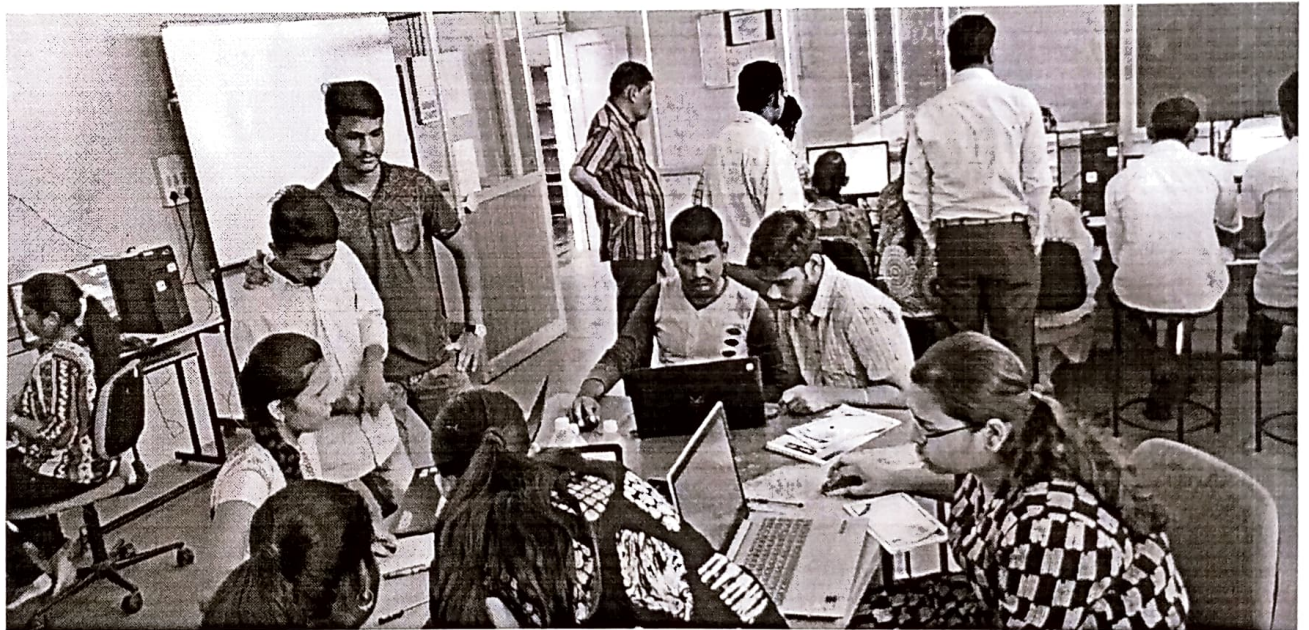
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ECE Dept.

IIIC, ISTE

Workshop

2018-19 (Even)



Students designing their own PCB design by using Eagle tool for given power supply design.



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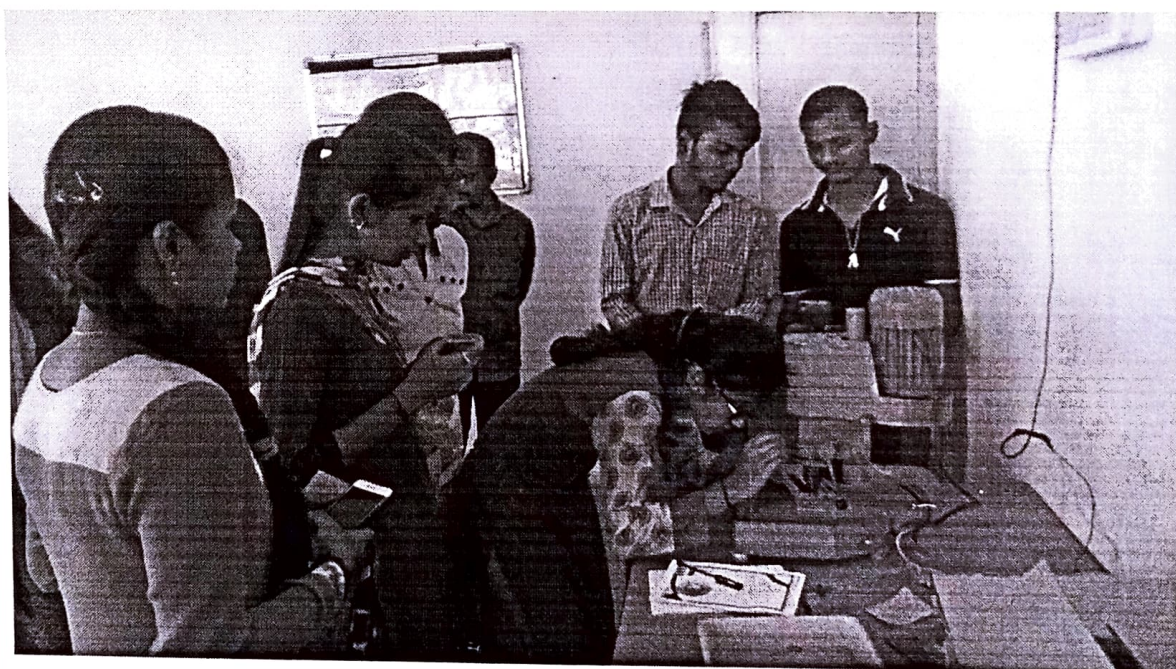
IIIC, ISTE

Workshop

2018-19 (Even)



Design print pressing on copper clad and etching process.



Drilling process.



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ECE Dept.

IIIC, ISTE

Workshop

2018-19 (Even)

Valedictory Function



4th Sem student Amogh presenting flowers to Mr. Tulasidas Salunkhe CEO Electrosal Hi Tech Pvt. Ltd. Nipani, Prof. D M Kumbhar IIIC Coordinator and Prof. S S Patil ECSA Coordinator on the occasion of Valedictory Function.



Workshop participation certificate distribution by resource person Mr. Tulasidas Salunkhe.

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TECH FORTUNE Technologies with registered no: 2BJOPS5632Q1Z2 functioning at Bangalore, since 2016, currently focusing on engineering services, software training & development is represented herein by Shri. Mallikarjun V S, C.E.O, hereinafter referred to as Tech Fortune, which expression shall unless repugnant to the context mean and intrude its successors and assignees of the **OTHER PART**.

WHEREAS

The **HSIT Nidasoshi** has initiated to promote Industry Institute Interaction to conduct various Industries/Job oriented training programmes/placements to the students, Mutual sharing of resource persons and students for advanced/innovative/future technologies/research activities as well as industries technical related problems and to know industry work-culture/updates along with the regular academic work.

Joe
6/9/18

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For
6/9/18

Page 2 of 3

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My

Tech Fortune for in campus selection process or make all arrangements to send HSIT students to Tech Fortune said place for the selection process.

Responsibilities of the HSIT Nidasoshi

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For Hirasugar Institute of Technology
Nidasoshi.

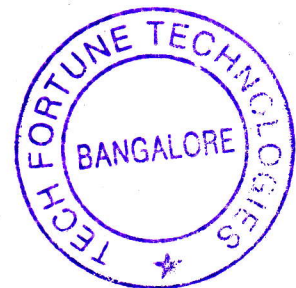
1) Dr. S. C. KAMATE
Principal



2) Dr. V. G. KASABEGUDAR
H.O.D(E&CE)

For TECH FORTUNE Technologies
Bangalore.

1) Shri Mallikarjun V S
C.E.O.





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TRAINING AND PLACEMENT CELL

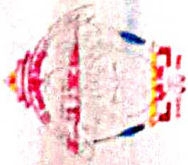


Tech Fortune

24.	PAVAN DESHIPANDE	2HN16EC025	<i>Pavan</i>	<i>Pavan</i>	<i>Pavan</i>	
25.	PRIYANKA DANOLLI	2HN16EC026	<i>Priyanka</i>	<i>Priyanka</i>		
26.	SACHIN NINGANUR	2HN16EC028	<i>Sachin</i>	<i>Sachin</i>	<i>Sachin</i>	
27.	SANA ATTAR	2HN16EC030	<i>Sana</i>	<i>Sana</i>	<i>Sana</i>	
28.	SEEMA GANI	2HN16EC031				
29.	SHRADDHA M. HIPPARAGI	2HN16EC032	<i>Shraddha</i>			
30.	SHWETA SONTANAVAR	2HN16EC033				
31.	SOU MYA METAGUDLI	2HN16EC034	<i>Soumya</i>			
32.	SOUNDARYA PATIL	2HN16EC035	<i>Soundarya</i>			
33.	SUNITA KARABASANNAVAR	2HN16EC036	<i>Sunita</i>	<i>Sunita</i>	<i>Sunita</i>	
34.	SUPREETA D. HIRAGAPPAGOL	2HN16EC037				
35.	SUPRIYA KADAPPA CHOUUGALA	2HN16EC038	<i>Supriya</i>	<i>Supriya</i>		
36.	SURESH MUNJE	2HN16EC039	<i>Suresh</i>	<i>Suresh</i>	<i>Suresh</i>	
37.	SUSHMITA SHIVAPUTRA MARADI	2HN16EC041				
38.	VEENASHRI BORAGALLI	2HN16EC042	<i>Veenashri</i>	<i>Veenashri</i>	<i>Veenashri</i>	
39.	VIDYA SHEETI	2HN16EC043	<i>Vidya</i>	<i>Vidya</i>	<i>Vidya</i>	
40.	DEEPAK KARNE	2HN16EC403				
41.	LINGANAGOUDA PATIL	2HN16EC405				
42.	SANGAMESH .K	2HN16EC413				
43.	SANTOSH SHEDBAL	2HN16EC415				
44.	SHITAL KHEMANNAVAR	2HN16EC416				
45.	REVANASIDDAYYA V.M	2HN16EC411	<i>D.V. Madhupati</i>	<i>D.V. Madhupati</i>	<i>D.V. Madhupati</i>	

23 Attended
 04 Selected.

30



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TRAINING AND PLACEMENT CELL



Founder of S J P N Trust's

Hearty Congratulations To



Mr. Muralidhar K
CSE Branch



Miss. Sandhya Nemagoud
CSE Branch



Miss. Soumya Patil
CSE Branch



Miss. Kaveri Daddimani
ECE Branch



Mr. Pavan Deshpande
ECE Branch



Miss. Sunita K
ECE Branch



Miss. Vidya Shetti
ECE Branch



Miss. Nikita patil
EEE Branch



Miss. Spoorti Sattigeri
EEE Branch



Mr. Iliyas Nadaf
MECH Branch



Miss. Priyanka Koppad
MECH Branch



Mr. Shrishail Badiger
MECH Branch



Mr. Vijaykumar B
MECH Branch

For Being Selected in

Tech Fortune Technologies & CAD Vision R & D, Bengaluru

Drive Held on 14th November 2019 at HSIT, Nidasoshi

By: Management, Principal, TP Cell & Staff

(Signature)
22/11/2019

TRAINING & PLACEMENT OFFICER
Hirasugar Institute of Technology
Nidasoshi- 591236, Dt.: Belgaum

Nidasoshi-591 236, Taq: Hukkeri, Dist: Belagavi, Karnataka, India.
Phone: +91-8333-278887, Fax: 278886, Web: www.hsit.ac.in, E-mail: principal@hsit.ac.in



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TRAINING AND PLACEMENT CELL

TP Cell

Placement

On Campus
Drive

2019-20 (ODD)

Notice/Circular

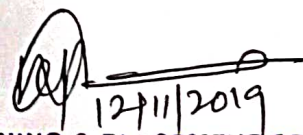
Date: 12/11/2019

On Campus Drive

All the eligible, interested and willing to prepare for the requirement of the event in all respects are informed to register with HIT's Training & Placement Cell on or before 13/11/2019, 5.00 pm. without fail. Refer the following details of the event.

Eligible Branch/Class & %Age	BE (CSE, ECE, EEE & MECH) Final Year with : 50% and above aggregate marks in Engineering Degree.
Company/Event Name & Nature :	<ol style="list-style-type: none"> 1. Tech Fortune Technologies, Bengaluru 2. Cad Vision Research & Development 3. Techgetham
Designation & Salary:	Trainee Intern
Job Location:	Bengaluru
Detailed Eligibility:	<ul style="list-style-type: none"> ➤ Pursuing Bachelor's degree in engineering (graduating in 2020) from a premier engineering school. ➤ Good verbal and written communication
Selection Process:	<ul style="list-style-type: none"> ➤ Aptitude Test ➤ Group Discussion ➤ Face to Face
Essential Requirements:	<ul style="list-style-type: none"> ➤ Corporate Etiquette (dress code). ➤ Interview executive folder should contain followings: ➤ Institute ID card ➤ Well prepared and updated Resume ➤ Copies all marks cards & other personal documents of any other significant achievements.

Opportunity will knock your door at any time, so your best preparation for the event will make 100% successful career and helps to reach TP Cell Goal very easily.


12/11/2019

TRAINING & PLACEMENT OFFICER
Hirasugar Institute of Technology
Nidasoshi- 591236, Dt.: Belgaum

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TRAINING AND PLACEMENT CELL

TP Cell

Placement

Attendance
Sheet

2019-20 (ODD)

Date: 14/11/2019

**Tech Fortune Technologies, Bengaluru CSR Drive at HSIT,
Nidasoshi on 14th November - 2019**

Attendance Sheet

Branch: Electronics & Communication Engineering

SL. No.	STUDENT NAME	USN	Written Test 14/11/2019	Group Discussion 14/11/2019	Face to Face Interview 14/11/2019	Remarks
			Sign	Sign	Sign	
1.	SANKETH B.PIMPLE	2HN15EC031	<i>[Signature]</i>			
2.	ABHISHEK KHADAKABHAVI	2HN16EC001	<i>[Signature]</i>	<i>[Signature]</i>		
3.	AISHWARYA V.HAMBAR	2HN16EC002				
4.	AISHWARYA PATIL	2HN16EC003	<i>[Signature]</i>			
5.	AISHWARYA T.JADHAV	2HN16EC004	<i>[Signature]</i>			
6.	AKSHAY POWAR	2HN16EC005	<i>[Signature]</i>			
7.	AMRUTA HUNASHYALI	2HN16EC006	<i>[Signature]</i>			
8.	ANUPAMA MURUGUDE	2HN16EC007				
9.	ANUSHA J.BOLLARAPU	2HN16EC008	<i>[Signature]</i>			
10.	ARCHANA BALAGOUDA PATIL	2HN16EC009				
11.	ASHWINI SHETTIMANI	2HN16EC010	<i>[Signature]</i>			
12.	BHAGYASHREE PATIL	2HN16EC011	<i>[Signature]</i>			
13.	CHAITRA VEERAKTHAMATH	2HN16EC012	<i>[Signature]</i>			
14.	DALAWAI LAXMI DUNDAPPA	2HN16EC013	<i>[Signature]</i>			
15.	DHAMMAKKA PANDAV	2HN16EC014				
16.	GANGADHAR GIRGAVE	2HN16EC015	<i>[Signature]</i>			
17.	GOMATESH PATIL	2HN16EC016	<i>[Signature]</i>	<i>[Signature]</i>		
18.	KAVERI DADDIMANI	2HN16EC017	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	✓
19.	KAVITA PATIL	2HN16EC019				
20.	LAXMI AIGALI	2HN16EC020	<i>[Signature]</i>			
21.	MADHUMATI S.HAVAL	2HN16EC022	<i>[Signature]</i>			
22.	MAHAMMADMOUSEEN RANGAPUR	2HN16EC023	<i>[Signature]</i>			
23.	MUBSHIRIN BAGWAN	2HN16EC024				



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25.	PRIYANKA DANOLLI	2HN16EC026	<i>Priyanka</i>	<i>Priyanka</i>		
26.	SACHIN NINGANUR	2HN16EC028	<i>Sachin</i>	<i>Sachin</i>	<i>Sachin</i>	
27.	SANA ATTAR	2HN16EC030	<i>Sana</i>	<i>Sana</i>	<i>Sana</i>	
28.	SLEMA GANI	2HN16EC031				
29.	SHRADDHA M. HIPPARAGI	2HN16EC032	<i>Shraddha</i>			
30.	SHWETA SONTANAVAR	2HN16EC033				
31.	SOU MIYA MILTAGUDLI	2HN16EC034	<i>Soumiya</i>			
32.	SOUNDARYA PATIL	2HN16EC035	<i>Soundarya</i>	<i>Soundarya</i>	<i>Soundarya</i>	✓
33.	SUNITA KARABASANNAVAR	2HN16EC036	<i>Sunita</i>	<i>Sunita</i>	<i>Sunita</i>	
34.	SUPREETA D. HIRAGAPPAGOL	2HN16EC037				
35.	SU PRIYA KADAPPA CHOU GALA	2HN16EC038	<i>Su Priya</i>	<i>Su Priya</i>	<i>Su Priya</i>	
36.	SURESH MUNJE	2HN16EC039	<i>Suresh</i>	<i>Suresh</i>	<i>Suresh</i>	
37.	SUSHMITA SHIVAPUTRA MARADI	2HN16EC041				
38.	VEENASHRI BORAGALLI	2HN16EC042	<i>Veenashri</i>	<i>Veenashri</i>	<i>Veenashri</i>	
39.	VIDYA SHETTI	2HN16EC043	<i>Vidya</i>	<i>Vidya</i>	<i>Vidya</i>	
40.	DEEPAK KARNE	2HN16EC403				
41.	LINGANAGOUDA PATIL	2HN16EC405				
42.	SANGAMESH .K	2HN16EC413				
43.	SANTOSH SHEDBAL	2HN16EC415				
* 44.	SHITAL KHEMANNAVAR	2HN16EC416				
45.	REVANASIDDAYYA V.M	2HN16EC411	<i>D.V. Mallegol</i>	<i>D.V. Mallegol</i>	<i>D.V. Mallegol</i>	

23 Attended
 04 Absent

30



aMSa embedded solution

Imagination...Innovation...Implementation.

**MEMORANDUM OF UNDERSTANDING
THE PRINCIPAL (A)
HIRASUGAR INSTITUTE OF TECHNOLOGY
AND
AMSA EMBEDDED SOLUTIONS (B)**

DEVELOPMENT CENTER

Amsa Embedded Solution
Unkal cross stop, shirur park road,
Baligar building, Vidyanagar
HUBLI-31

CONTACT INFORMATION

9535337450,
8147384188,
8904595334
amsatechno@gmail.com

www.amsaembedded.com

Phangsh

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aMSa embedded solution

MEMORANDUM OF UNDERSTANDING

This Memorandum of Understanding (herein after referred to as (MoU) is entered into on this 30/10/2018 between **ECE DEPARTMENT OF HIRASUGAR INSTITUTE OF TECHNOLOGY** and **AMSA EMBEDDED SOLUTIONS** located at vidyanagar Hubli. The scope of the MoU, the roles and responsibilities of the parties.

BACKGROUND

This MoU is for the benefits of students for IT Industries. This MoU is for the conducting following program for the Electronics and Communication Engineering Branch

- ✓ IoT Research and development lab.
- ✓ Internship Training.
- ✓ Guide for the Projects.
- ✓ Workshop.
- ✓ Seminar on new technology.

www.amsaembedded.com

Plongadi

30/10

1. BENEFITS TO THE HIRASUGAR INSTITUTE OF TECHNOLOGY

1.1 Opportunity to emerge as one of the competent entities in the academic circles in research and development in the area of Information Technology;

1.2 Opportunity to be recognized by the industry and academic circles as one of the preferred locations for acquiring training and skills development in latest technology and software

1.3 Opportunity to utilize the Program to train students on Latest technology Software products and technologies.


1.4 Opportunity for the faculty and students of **HIRASUGAR INSTITUTE OF TECHNOLOGY** to design world class curriculum capable of delivering the caliber of IT skills required at present and also for the future

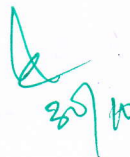
1.2 BENEFITS FROM AES

1.2.1. Students and faculty will get the technical support on latest technology along with paper presentations

1.2.2. Students and faculty will get the coding level workshop program on latest technologies and workshop program will be on hands on work.

1.2.3. AES will support for student final year academic project with training and development on as per student problem statement and also latest technology.




25/10

MONITORING OF IMPLEMENTATION

A Co-ordination Committee consisting of two faculty members of **HIRASUGAR INSTITUTE OF TECHNOLOGY** and two officers nominated by **AES** will do the planning and monitoring of the implementation of the various aspects of this MoU. There will be an annual review. This review would primarily focus on the curriculum progress, faculty availability and certification.

GENERAL

1. This MoU may be terminated by either party through a notice of one month. Either party may terminate this MoU if either of the parties is frustrated by reasons beyond

It's control from going ahead with the implementation of the provision of this MoU. The provisions contained in Clauses 5 ("Intellectual Property Rights"), 6 ("Limitation of Liability") and 7 will survive any such termination.

2. Neither parties will make any presentations pertaining to the other or its business or affairs, without the express written consent and approval of other.

3. It is understood that during the course of this MoU it may be necessary for either party to disclose confidential/proprietary information to the other. The disclosure of any such information or data between the parties shall be made pursuant to an executed Confidentiality Agreement.

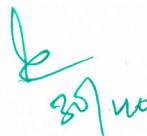
4. All costs incurred by a party pursuant to this MoU shall be borne by the party who incurred the costs.

DURATION OF MOU

1. The duration of the MOU shall be for a Period of 3 years from the effective date.

2. Any clause or article of the MOU may be modified or changed on mutual consent between both parties.

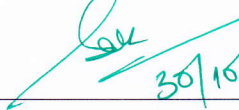



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SUMMARY:

AES recognizes the significance of the The Principal of **HIRASUGAR INSTITUTE OF TECHNOLOGY** initiative to be the leader in the field of IT related academia in the country. AES proposes to make available the appropriate technology and expertise to develop **HIRASUGAR INSTITUTE OF TECHNOLOGY** position as a leading institute providing high quality IT skilled resources. This Memorandum of Understanding is intended to express the broad understanding of the parties regarding their working with each other to the extent possible for their mutual benefit.


Signature with seal


30/10/18

HIRASUGAR INSTITUTE OF TECHNOLOGY

Date: 30 OCT 2018

For amsa Embedded Solutions,
Signature with seal


PARTNER.

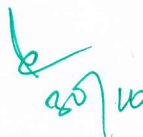
AMSA EMBEDDED SOLUTION ,HUBLI

Date: 30/10/2018

CORPORATE6131b8436e2401630648387

www.amsaembedded.com




30/10



amsa EMBEDDED SOLUTIONS

Certification of Internship



This is to certify that **Mr. Suresh Munje**, student of 7th Semester, Department of Electronics and Communication Engineering, Hirasugar Institute of Technology, bearing USN **2HN16EC039**, has successfully completed 30days Internship Program on **Embedded System**, from 5th July 2019 to 5th August 2019

As part of the internship program, he has been placed in the Design and Development team and involved in relative activity of the project

During the internship period of his training with us he was found punctual, hardworking and inquisitive.

for amsa Embedded Solutions.

(Signature)

PROPRIETOR.

Authorized Signature

In association with

NEX-G
AUTOMATION

www.nexgautomation.com

Coffeescript
SOFTWARES.com



amsa EMBEDDED SOLUTIONS



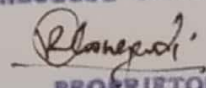
Certification of Internship

This is to certify that **Mr. Pavan Deshpande**, student of 7th Semester, Department of Electronics and Communication Engineering, Hirasugar Institute of Technology, bearing USN **2HN16EC025**, has successfully completed 30days Internship Program on **Embedded System**, from 5th July 2019 to 5th August 2019

As part of the internship program, he has been placed in the Design and Development team and involved in relative activity of the project

During the internship period of his training with us he was found punctual, hardworking and inquisitive.

For amsa Embedded Solutions.


PROPRIETOR.

Authorized Signature

In association with

NEX-G
AUTOMATION
www.nexgautomation.com

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SOFTWARES.com



S J P N Trust's
Hirasugar Institute of Technology, Nidasoshi
Inculcating Values, Promoting Prosperity
Approved by AICTE, Recognized by Govt. of Karnataka and Affiliated to VTU Belagavi.
Accredited at 'A' Grade by NAAC
Programmes Accredited by NBA: CSE, ECE, EEE & ME.

ECE Dept.

IOT Lab

Inauguration

2019-20

Date of Activity held and Time:	03 rd January 2019, 10.30am to 12.30pm
Name of Activity:	IoT lab setup
Type of Activity: (cultural/curricular/co-curricular)	Technical
Resource Person/Invitee:	Shri. Vinayak R. Dhogadi
Professional Details of Resource Person:	Co-founder aMSA embedded solutions
Year / Class:	All Staff and final and pre-final year Students
No. of students	Pre-final year - 45
No. of Staff:	13
Activity In charge:	Prof. S. S. Kamate Prof. D. B. Madihalli

Description of Activity: IoT lab is setup in the Department of Electronics and Communication on 03/01/2019, in association with aMSa Embedded solutions Hubli. This industry linked lab is been set up by aMSa Embedded Solutions at free of cost wherein they have donated IoT trainer kits, Power Adapters, Program cables, jumper wires and IoT charts to the department. These kits will be useful to our students in carrying out IoT related miniprojects and projects. Shri Vinayak R. Dhongadi co-founder of the aMSa gave awareness talk on this occasion. Principal Dr. S. C. Kamate addressed and informed the students to take the benefit of the lab. HOD and coordinators motivated the students to utilize the lab during free time. The industry linked lab setup gives a platform to our students to use latest kits and components.

CO No.	CO Defined	BLL	Relevance POs and PSOs
1	To understand and work with modern tools/devices	L2, L3	PO1, PO2, PO5



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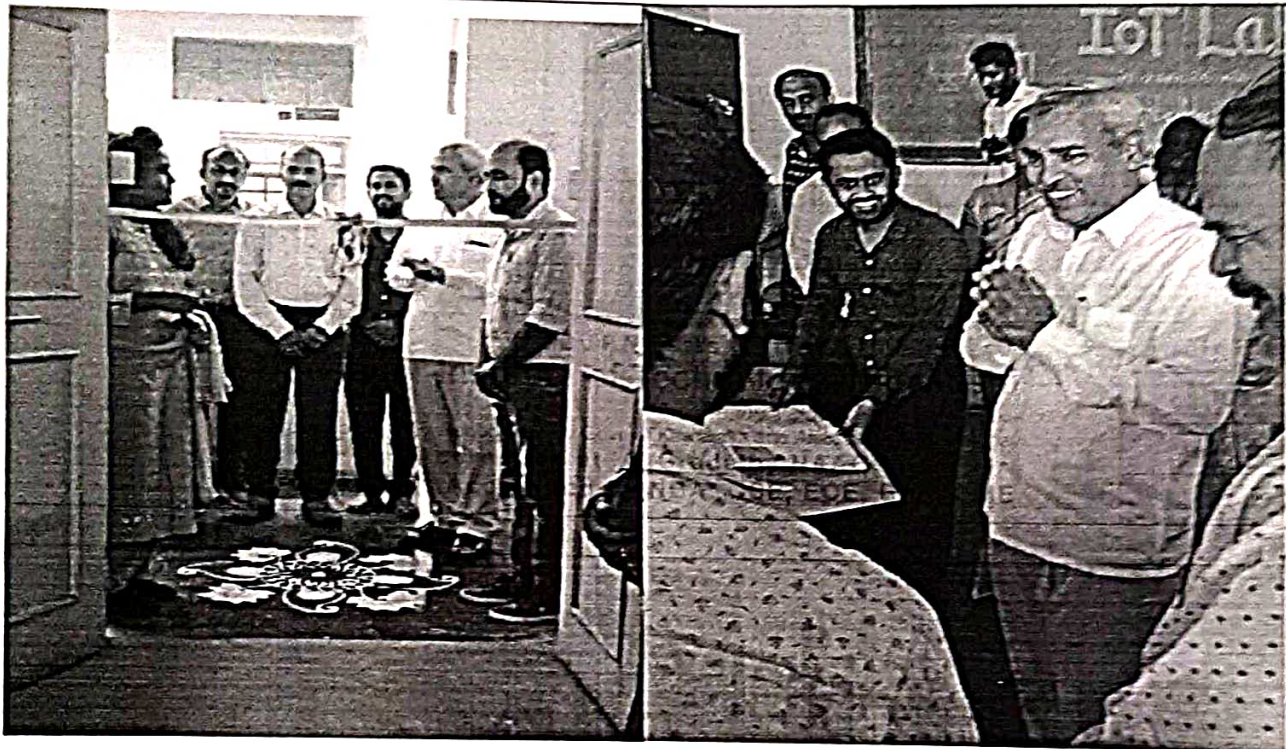
Accredited at 'A' Grade by NAAC
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ECE Dept.

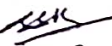
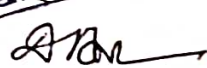
IoT Lab

Inauguration

2019-20



IoT lab inaugural

Coordinators: 1. Prof. S. S. Kamate 
2. Prof. D. B. Madihalli 


HOD
Electronics & Comm. Engrg. Dept.
HSIT NIDASOSHI

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