

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI												
Scheme of Teaching and Examination 2018 – 19												
Outcome Based Education (OBE) and Choice Based Credit System (CBCS)												
(Effective from the academic year 2018 – 19)												
Programme: B.E: Electronics & Communication Engineering												
III SEMESTER												
Sl. No	Course and Course Code		Course Title	Teaching Department	Teaching Hours/Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	BSC	18MAT31	Transform Calculus, Fourier Series and Numerical Techniques	Mathe matics	2	2	--	03	40	60	100	3
2	PCC	18EC32	Network Theory		3	2	--	03	40	60	100	4
3	PCC	18EC33	Electronic Devices		3	0	--	03	40	60	100	3
4	PCC	18EC34	Digital System Design		3	0	--	03	40	60	100	3
5	PCC	18EC35	Computer Organization & Architecture		3	0	--	03	40	60	100	3
6	PCC	18EC36	Power Electronics & Instrumentation		3	0	--	03	40	60	100	3
7	PCC	18ECL37	Electronic Devices & Instrumentation Laboratory		--	2	2	03	40	60	100	2
8	PCC	18ECL38	Digital System Design Laboratory		--	2	2	03	40	60	100	2
9	HSMC	18KVK39/49	Vyavaharika Kannada (Kannada for Communication)/	HSMC	--	2	--	--	100	--	100	1
		18KAK39/49	Aadalitha Kannada (Kannada for Administration)									
		OR										
		18CPC39/49	Constitution of India, Professional Ethics and Cyber Law		1	--	--	02	40	60		
					Examination is by objective type questions							
TOTAL					17	10	04	24	420	480	900	24
					OR	OR		OR	OR	OR		
					18	08		26	360	540		

Note: BSC: Basic Science, PCC: Professional Core, HSMC: Humanity and Social Science, NCMC: Non-credit mandatory course.

18KVK39 Vyavaharika Kannada (Kannada for communication) is for non-Kannada speaking, reading and writing students and 18KAK39 Aadalitha Kannada (Kannada for Administration) is for students who speak, read and write Kannada.

Course prescribed to lateral entry Diploma holders admitted to III semester of Engineering programs

10	NCMC	18MATDIP31	Additional Mathematics - I	Mathematics	02	01	--	03	40	60	100	0
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(a) The mandatory non – credit courses Additional Mathematics I and II prescribed for III and IV semesters respectively, to the lateral entry Diploma holders admitted to III semester of BE/B.Tech programs, shall attend the classes during the respective semesters to complete all the formalities of the course and appear for the University examination. In case, any student fails to register for the said course/fails to secure the minimum 40% of the prescribed CIE marks, he/she shall be deemed to have secured F grade. In such a case, the students have to fulfill the requirements during subsequent semester/s to appear for SEE.

(b) These Courses shall not be considered for vertical progression, but completion of the courses shall be mandatory for the award of degree.

Courses prescribed to lateral entry B. Sc degree holders admitted to III semester of Engineering programs

Lateral entrant students from B.Sc. Stream, shall clear the non-credit courses Engineering Graphics and Elements of Civil Engineering and Mechanics of the First Year Engineering Programme. These Courses shall not be considered for vertical progression, but completion of the courses shall be mandatory for the award of degree.

AICTE Activity Points to be earned by students admitted to BE/B. Tech/B. Plan day college programme (For more details refer to Chapter 6, AICTE Activity Point Programme, Model Internship Guidelines):

Over and above the academic grades, every Day College regular student admitted to the 4 years Degree programme and every student entering 4 years Degree programme through lateral entry, shall earn 100 and 75 Activity Points respectively for the award of degree through AICTE Activity Point Programme. Students transferred from other Universities to fifth semester are required to earn 50 Activity Points from the year of entry to VTU. The Activity Points earned shall be reflected on the student's eighth semester Grade Card.

The activities can be spread over the years, anytime during the semester weekends and holidays, as per the liking and convenience of the student from the year of entry to the programme. However, minimum hours' requirement should be fulfilled. Activity Points (non-credit) have no effect on SGPA/CGPA and shall not be considered for vertical progression.

In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

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Programme: B.E: Electronics & Communication Engineering																		
IV SEMESTER																		
Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits						
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks							
					L	T	P											
1	BSC	18MAT41	Complex Analysis, Probability and Statistical Methods	Mathe matics	2	2	--	03	40	60	100	3						
2	PCC	18EC42	Analog Circuits		3	2	--	03	40	60	100	4						
3	PCC	18EC43	Control Systems		3	0	--	03	40	60	100	3						
4	PCC	18EC44	Engineering Statistics & Linear Algebra		3	0	--	03	40	60	100	3						
5	PCC	18EC45	Signals & Systems		3	0	--	03	40	60	100	3						
6	PCC	18EC46	Microcontroller		3	0	--	03	40	60	100	3						
7	PCC	18ECL47	Microcontroller Laboratory		--	2	2	03	40	60	100	2						
8	PCC	18ECL48	Analog Circuits Laboratory		--	2	2	03	40	60	100	2						
9	HSMC	18KVK39/49	Vyavaharika Kannada (Kannada for Communication)	HSM C	--	2	--	--	100	--	100	1						
		18KAK39/49	Aadalitha Kannada (Kannada for Administration)															
		OR																
		18CPC39/49	Constitution of India, Professional Ethics and Cyber Law										1	--	--	02	40	60
		Examination is by objective type questions																
TOTAL					17	10	04	24	420	480	900	24						
					OR	OR		OR	OR	OR								
					18	08		26	360	540								

OPEN ELECTIVE group-B	
18EC651	Signal Processing
18EC652	Sensors & Signal Conditioning
18EC653	Virtual Instrumentation
18EC654	Microcontrollers
18EC655	Basic VLSI Design
Students can select any one of the open electives offered by other Departments except those that are offered by the parent Department (Please refer to the list of open electives under 18XX65X). Selection of an open elective shall not be allowed if, • The candidate has studied the same course during the previous semesters of the programme. • The syllabus content of open elective is similar to that of the Departmental core courses or professional electives. • A similar course, under any category, is prescribed in the higher semesters of the programme. Registration to electives shall be documented under the guidance of Programme Coordinator / Advisor/Mentor.	
Mini-project work: Based on the ability/abilities of the student/s and recommendations of the mentor, a single discipline or a multidisciplinary Mini- project can be assigned to an individual student or to a group having not more than 4 students. CIE procedure for Mini-project: (i) Single discipline:The CIE marks shall be awarded by a committee consisting of the Head of the concerned Department and two senior faculty members of the Department, one of whom shall be the Guide. The CIE marks awarded for the Mini-project work, shall be based on the evaluation of project report, project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the project report shall be the same for all the batch mates. (ii) Interdisciplinary: Continuous Internal Evaluation shall be group wise at the college level with the participation of all the guides of the college. The CIE marks awarded for the Mini-project, shall be based on the evaluation of project report, project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the project report shall be the same for all the batch mates. SEE for Mini-project: (i) Single discipline: Contribution to the Mini-project and the performance of each group member shall be assessed individually in the semester end examination (SEE) conducted at the department. (ii) Interdisciplinary: Contribution to the Mini-project and the performance of each group member shall be assessed individually in semester end examination (SEE) conducted separately at the departments to which the student/s belong to.	

Internship: All the students admitted to III year of BE/B.Tech shall have to undergo mandatory internship of 4 weeks during the vacation of VI and VII semesters and /or VII and VIII semesters. A University examination shall be conducted during VIII semester and the prescribed credit shall be included in VIII semester. Internship shall be considered as a head of passing and shall be considered for the award of degree. Those, who do not take-up/complete the internship shall be declared fail and shall have to complete during subsequent University examination after satisfying the internship requirements.

AICTE activity Points: In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

Programme: B.E: Electronics & Communication Engineering

VII SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
1	PCC	18EC71	Computer Networks		3	--	--	03	40	60	100	3
2	PCC	18EC72	VLSI Design		3	--	--	03	40	60	100	3
3	PEC	18XX73X	Professional Elective - 2		3	--	--	03	40	60	100	3
4	PEC	18XX74X	Professional Elective - 3		3	--	--	03	40	60	100	3
5	OEC	18XX75X	Open Elective -B		3	--	--	03	40	60	100	3
6	PCC	18ECL76	Computer Networks Lab		--	2	2	03	40	60	100	2
7	PCC	18ECL77	VLSI Laboratory		--	2	2	03	40	60	100	2
8	Project	18ECP78	Project Work Phase - 1		--	--	2	--	100	--	100	1
9	Internship	--	Internship	(If not completed during the vacation of VI and VII semesters, it shall be carried out during the vacation of VII and VIII semesters)								
TOTAL					15	04	06	21	38	420	800	20

Note: PCC: Professional core, PEC: Professional Elective.

Professional Elective - 2

Course code under 18XX73X	Course Title
18EC731	Real Time Systems
18EC732	Satellite Communication
18EC733	Digital Image Processing
18EC734	DSP Algorithms & Architecture

Course code under 18XX74X	Course Title
18EC741	IOT & Wireless Sensor Networks
18EC742	Automotive Electronics
18EC743	Multimedia Communication
18EC744	Cryptography
18EC745	Machine Learning with Python
Open Elective -B	
18EC751	Communication Theory
18EC752	Neural Networks
18EC753	ARM Embedded Systems
18EC754	Digital Systems Design using VHDL
Students can select any one of the open electives offered by other Departments except those that are offered by the parent Department (Please refer to the list of open electives under 18XX75X). Selection of an open elective shall not be allowed if, • The candidate has studied the same course during the previous semesters of the programme. • The syllabus content of open elective is similar to that of the Departmental core courses or professional electives. • A similar course, under any category, is prescribed in the higher semesters of the programme. Registration to electives shall be documented under the guidance of Programme Coordinator / Advisor /Mentor.	
Project work: Based on the ability/abilities of the student/s and recommendations of the mentor, a single discipline or a multidisciplinary project can be assigned to an individual student or to a group having not more than 4 students. In extraordinary cases, like the funded projects requiring students from different disciplines, the project student strength can be 5 or 6. CIE procedure for Project Work Phase - 1: (i) Single discipline:The CIE marks shall be awarded by a committee consisting of the Head of the concerned Department and two senior faculty members of the Department, one of whom shall be the Guide. The CIE marks awarded for the project work phase -1, shall be based on the evaluation of the project work phase -1 Report (covering Literature Survey, Problem identification, Objectives and Methodology), project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the Project report shall be the same for all the batch mates. (ii) Interdisciplinary: Continuous Internal Evaluation shall be group wise at the college level with the participation of all guides of the college. Participation of external guide/s, if any, is desirable.	

The CIE marks awarded for the project work phase -1, shall be based on the evaluation of project work phase -1 Report, project presentation skill and question and answer session in the ratio 50:25:25.The marks awarded for the project report shall be the same for all the batch mates.

Internship: All the students admitted to III year of BE/B.Tech shall have to undergo mandatory internship of 4 weeks during the vacation of VI and VII semesters and /or VII and VIII semesters. A University examination shall be conducted during VIII semester and the prescribed credit shall be included in VIII semester. Internship shall be considered as a head of passing and shall be considered for the award of degree. Those, who do not take-up/complete the internship shall be declared fail and shall have to complete during subsequent University examination after satisfying the internship requirements.

AICTE activity Points: In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

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Programme: B.E: Electronics & Communication Engineering												
VIII SEMESTER												
Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	PCC	18EC81	Wireless and Cellular Communication		3	--	--	03	40	60	100	3
2	PEC	18XX82X	Professional Elective - 4		3	--	--	03	40	60	100	3
3	Project	18ECP83	Project Work Phase - 2		--	--	2	03	40	60	100	8
4	Seminar	18ECS84	Technical Seminar		--	--	2	03	100	--	100	1
5	Internship	18ECI85	Internship	Completed during the vacation/s of VI and VII semesters and /or VII and VIII semesters.)				03	40	60	100	3
TOTAL					06	--	04	15	260	240	500	18
Note: PCC: Professional Core, PEC: Professional Elective.												
Professional Elective - 4												
Course code under 18XX82X		Course Title										
18EC821		Network Security										
18EC822		Micro Electro Mechanical Systems										
18EC823		Radar Engineering										
18EC824		Optical Communication Networks										
18EC825		Biomedical Signal Processing										

Project Work

CIE procedure for Project Work Phase - 2:

(i) Single discipline: The CIE marks shall be awarded by a committee consisting of the Head of the concerned Department and two senior faculty members of the Department, one of whom shall be the Guide.

The CIE marks awarded for the project work phase -2, shall be based on the evaluation of project work phase -2 Report, project presentation skill and question and answer session in the ratio 50:25:25. The marks awarded for the project report shall be the same for all the batch mates.

(ii) Interdisciplinary: Continuous Internal Evaluation shall be group wise at the college level with the participation of all guides of the college. Participation of external guide/s, if any, is desirable.

The CIE marks awarded for the project work phase -2, shall be based on the evaluation of project work phase -2 Report, project presentation skill and question and answer session in the ratio 50:25:25. The marks awarded for the project report shall be the same for all the batch mates.

SEE for Project Work Phase - 2:

(i) Single discipline: Contribution to the project and the performance of each group member shall be assessed individually in semester end examination (SEE) conducted at the department.

(ii) Interdisciplinary: Contribution to the project and the performance of each group member shall be assessed individually in semester end examination (SEE) conducted separately at the departments to which the student/s belongs to.

Internship: Those, who have not pursued /completed the internship, shall be declared as fail and have to complete during subsequent University examination after satisfying the internship requirements.

AICTE activity Points: In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

Activity points of the students who have earned the prescribed AICTE activity Points shall be sent the University along with the CIE marks of 8th semester. In case of students who have not satisfied the AICTE activity Points at the end of eighth semester, the column under activity Points shall be marked NSAP (Not Satisfied Activity Points).

B.E.: Electronics & Communication Engineering

VII SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17EC71	Microwave and Antennas	EC	04		03	60	40	100	4
2	17EC72	Digital Image Processing	EC	04		03	60	40	100	4
3	17EC73	Power Electronics	EC	04		03	60	40	100	4
4	17EC74X	Professional Elective-3	EC	03		03	60	40	100	3
5	17EC75X	Professional Elective-4	EC	03		03	60	40	100	3
6	17ECL76	Advanced Communication Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
7	17ECL77	VLSI Lab	EC	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ECP78	Project Work Phase-I + Project work Seminar	EC		03		-	100	100	2
TOTAL				Theory:18 hours Practical and Project: 09 hours		21	420	380	800	24

Professional Elective-3		Professional Elective-4	
17EC741	Multimedia Communication	17EC751	DSP Algorithms and Architecture
17EC742	Biomedical Signal Processing	17EC752	IOT and Wireless Sensor Networks
17EC743	Real Time Systems	17EC753	Pattern Recognition
17EC744	Cryptography	17EC754	Advanced Computer Architecture
17EC745	CAD for VLSI	17EC755	Satellite Communication

1. Project Phase – I and Project Seminar: Comprises of Literature Survey, Problem identification, Objectives and Methodology. CIE marks shall be based on the report covering Literature Survey, Problem identification, Objectives and Methodology and Seminar presentation skill.

B.E.: Electronics & Communication Engineering

VIII SEMESTER

Sl. No	Course Code	Title	Teaching Department	Teaching Hours /Week		Examination				Credits	
				Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks		
1	17EC81	Wireless Cellular and L TE 4G Broadband	EC	4	-	3	60	40	100	4	
2	17EC82	Fiber Optics & Networks	EC	4	-	3	60	40	100	4	
3	17EC83X	Professional Elective-5	EC	3	-	3	60	40	100	3	
4	17EC84	Internship/Professional Practice	EC	Industry Oriented		3	50	50	100	2	
5	17ECP85	Project Work	EC	-	6	3	100	100	200	6	
6	17ECS86	Seminar	EC	-	4	-	-	100	100	1	
TOTAL						Theory: 11 hours Project and Seminar: 10 hours	15	330	370	700	20

Professional Elective -5	
17EC831	Micro Electro Mechanical Systems
17EC832	Speech Processing
17EC833	Radar Engineering
17EC834	Machine learning
17EC835	Network and Cyber Security

1. Internship/ Professional Practice: 4 Weeks internship to be completed between the (VI and VII semester vacation) and/or (VII and VIII semester vacation) period.