

	S J P N Trust's Hirasugar Institute of Technology, Nidasoshi. <i>Inculcating Values, Promoting Prosperity</i> Approved by AICTE, Recognized by Govt. of Karnataka and Affiliated to VTU Belagavi. Accredited at 'A' Grade by NAAC Programmes Accredited by NBA: CSE, ECE, EEE & ME	Institute
		NAAC
		1.1.1
		2020-21

List of Supporting Documents for 1.1.1 NAAC Criteria 1

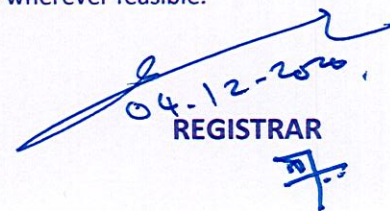
Sl.No	Name of Document
1	VTU Academic Calendar
2	Institute COE
3	Master Time Table/Consolidated Time Table
4	Department Work Load
5	Course Plan of Theory
6	Course Plan of Lab
7	Lesson Plan
8	Internal Assessment Time-Table
9	Technical Talk/Guest Lecture report
10	Feedback on Teaching Learning Process
11	Institute Weekly Meeting Resolution
12	Department Meeting Resolution
13	TP Cell Training Programs

Revised Academic Calendar of VTU, Belagavi for ODD Semester of 2020-21 (Tentative)

	I Sem B. E. / B. Tech. / B. Arch./B.Plan	I sem M.Tech./MBA /MCA/M.Arch.	III, V B. E. /B. Tech./B.Plan/ B.Arch & VII sem BPlan /BArch & IX Sem B. Arch.	VII Sem B. E. /B. Tech	III & V Sem MCA	III Sem MBA	III Sem M. Tech.	III Sem M. Arch.
Commencement of ODD Semester	14.12.2020	Will be announced later	01.09.2020	01.09.2020	01.09.2020	01.09.2020	01.09.2020	01.09.2020
Last Working day of ODD Semester	25.03.2021		16.01.2021	16.01.2021	16.01.2021	16.01.2021	16.01.2021	16.01.2021
Practical Examinations	29.03.2021 Onwards#		21.01.2021 Onwards#	21.01.2021 Onwards#	08.02.2021 Onwards#	--	21.01.2021 Onwards#	--
Theory Examinations	12.04.2021 To 30.04.2021		08.02.2021 To 27.03.2021	08.02.2021 To 27.03.2021	21.01.2021 To 06.02.2021	21.01.2021 To 19.02.2021	28.01.2021 To 13.02.2021	21.01.2021 To 06.02.2021
Internship			---	29.03.2021 To 10.04.2021	---	---	---	---
Internship Viva- Voce			---	---	---	---	15.02.2021 To 22.02.2021	---
Professional training / Organization study			---	---	---	22.02.2021 To 03.04.2021	---	---
Commencement of EVEN Semester	03.05.2021		29.03.2021	12.04.2021	15.02.2021	05.04.2021	23.02.2021	08.02.2021

NOTE:

- VII Semester B. E. / B. Tech. students shall have to undergo **Internship** as per circular of University **VTU/Aca/2019-20/85**, dated **12.05.2020**.
- I Semester B. E/ B. Tech / B. Arch Students shall compulsorily undergo **Induction Program** for 01 Weeks.
- The classroom sessions for all the semesters would be in **ONLINE mode/blended mode** until further orders.
- The Institute needs to function for **six days** a week with additional hours (**Saturday is a full working day**).
- The faculty/staff shall be available to undertake any work assigned by the university.
- If any of the above dates are declared to be a holiday then the corresponding event will come into effect on the next working day.
- (#) Notification regarding the Calendar of Events relating to the conduct of **University Examinations** will be issued by the Registrar (Evaluation) from time to time.
- Academic Calendar may be modified based on guidelines/directions issued in the future by MHRD/UGC/AICTE/State Government.
- Revised Academic Calendar is also applicable for **Autonomous Colleges**.
- The MBA students are permitted to carry out **project work** in blended mode (ONLINE/OFFLINE). More emphasis on OFFLINE mode wherever feasible.


04.12.2020
REGISTRAR



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IQAC

File I-11

2020-21 (Odd)

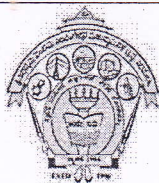
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CALENDAR OF EVENTS FOR THE ACADEMIC YEAR 2020-21 (Odd)

Date	Events	
01-09-2020	Commencement of III/V/VII Semester Classes	September-2020
05-09-2020	Teacher's Day	S M T W T F S
15-09-2020	Engineer's Day	6 7 8 9 10 11 12
02-10-2020	Gandhi Jayanthi	13 14 15 16 17 18 19
05-10-2020 to 07-10-2020	First Internal Assessment for III/V/VII Semester	20 21 22 23 24 25 26
08-10-2020	Feedback-I on Teaching-Learning	27 28 29 30
10-10-2020	Display of 1 st Internal Assessment Marks and Submission of Feedback-I to office.	17- Mahalaya Amavasye
10-10-2020	Indoor games/ Health Checkup Camp	October-2020
17-10-2020	Blood Donation Camp	S M T W T F S
27-10-2020	EDP Activities	1 2 3 4 5 6 7
01-11-2020	Kannada Rajyotsava & Commencement of I Semester & Induction Program (Phase - I)	11 12 13 14 15 16 17
11-11-2020 to 13-11-2020	Second Internal Assessment for III/V/VII Semester	18 19 20 21 22 23 24
19-11-2020	Feedback-II on Teaching-Learning	25 26 27 28 29 30 31
21-11-2020	HSIT Quest-2020 Display of 2 nd Internal Assessment Marks and Submission of Feedback-II to office.	2-Gandhi Jayanthi 25-Ayudha Pooja 26-Vijaydashami 30-Eid Milad
10-12-2020 to 12-12-2020	Third Internal Assessment for III/V/VII Semester First Internal Assessment for I Semester	November-2020
14-12-2020 to 16-12-2020	Lab Internal Assessment of III/V/VII Semester	S M T W T F S
16-12-2020	Display of final Internal Assessment Marks	1 2 3 4 5 6 7
17-12-2020	Last Working Day of III/V/VII Semester	8 9 10 11 12 13 14
21-12-2020 to 31-12-2020	Practical Examination of III/V/VII Semester	15 16 17 18 19 20 21
04-01-2021 to 23-01-2021	Theory Examination of III/V/VII Semester	22 23 24 25 26 27 28
		29 30
		December-2020
		S M T W T F S
		1 2 3 4 5
		6 7 8 9 10 11 12
		13 14 15 16 17 18 19
		20 21 22 23 24 25 26
		27 28 29 30 31
		3-Kanakadas Jayanthi, 25-Christmas
		January-2021
		S M T W T F S
		1 2 3 4 5 6 7 8 9
		10 11 12 13 14 15 16
		17 18 19 20 21 22 23
		24 25 26 27 28 29 30
		31
		26-Republic Day

Dr. B. V. Madiggond
IQAC Co-ordinator

Dr. S. C. Kamate
Principal



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ECE Dept.
 Academic
 MTT (V2)
 2020-21 (ODD)

Department of Electronics & Communication Engineering
Master Time Table for Academic Year 2020-21 (ODD) W.E.F 17/11/2020

Time / Day	S E M	09:00AM To 09:50AM	10:00AM To 10:50AM	11:00AM To 11:50AM	12:00PM To 12:50PM		02:00PM To 02:50PM	03:00PM To 03:50PM	04:00PM To 05:00PM
MON	III	CO&A (SSK)	PE&I (SSI)	SK (RBK)	MAT DIP (SIS)		ED Lab(B1)/DSD Lab(B2) (SBA & DMK / DBM & BPK)		
	V	VHDL (SSP)	PCS / OS (SBA) / (PVP)	ITC (DBM)	DSP (SSI)				
	VII	PE (DMK)	DIP (BPK)	M&A (SSM)	*		VLSI Lab(B1) / AC Lab(B2) (SSK / SSM & RRM)		
TUE	III	PE&I (SSI)	CO&A (SSK)	DSD (DBM)	NT (PVP)		ED Lab(B2) / DSD Lab(B1) (SBA & DMK / DBM & BPK)		
	V	EVS (SJW)	VHDL (SSP)	DSP (SSI)	PCS / OS (SBA) / (PVP)				
	VII	DIP (BPK)	PE (DMK)	M&A (SSM)	RTS (SSP)		VLSI Lab(B2) / AC Lab(B1) (SSK / SSM & RRM)		
WED	III	NT (PVP)	DSD (DBM)	CO&A (SSK)	PE&I (SSI)				
	V	EW / AE (SSK) / (BPK)	DSP (SSI)	VHDL (SSP)	PCS / OS (SBA) / (PVP)	LUNCH			
	VII	M&A (SSM)	SC (SBA)	DIP (BPK)	PE (DMK)		VLSI Lab(B1) / AC Lab(B2) (SSK / SSM & RRM)		
THU	III	ED (DMK)	NT (PVP)	MAT (SIS)	BK (RBK)	BREAK			
	V	ITC (DBM)	EW / AE (SSK) / (BPK)	ME (SSM)	EVS (SJW)		HDL Lab(B1) / DSP Lab(B2) (SSP & PVP / SSI)		
	VII	RTS (SSP)	SC (SBA)	PE (DMK)	DIP (BPK)		VLSI Lab(B2) / AC Lab(B1) (SSK / SSM & RRM)		
FRI	III	DSD (DBM)	NT (PVP)	ED (DMK)	MAT (SIS)				
	V	DSP (SSI)	ME (SSM)	EW / AE (SSK) / (BPK)	PCS / OS (SBA) / (PVP)		HDL Lab(B2) / DSP Lab(B1) (SSP & PVP / SSI)		
	VII	SC (SBA)	RTS (SSP)	*	M&A (SSM)				
SAT	III	MAT (SIS)	*	ED (DMK)	NT (PVP)				
	V	ME (SSM)	EW / AE (SSK) / (BPK)	DSP (SSI)	ITC (DBM)				
	VII	Project Work Phase-I							

*Free Slots: Remedial Class

Prof. Sachin S Patil

Coordinator

Prof. S B Akkole

HOD

Dr. S C Kamate

Principal



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First Year Engg.
Academic
Time-Table
2020-21
(Odd-Sem)

CONSOLIDATED TIME TABLE 2020 - 2021

w.e.f. 01/01/2021

Day / Time	Div	9.00am 9.55am	9.55am 10.50am		11.10am 12.05pm	12.05 pm 1.00 pm		2.00 pm 2.55 pm	2.55 pm 3.50 pm	3.50 pm 4.45 pm
Monday	A	CLA*	PHY*		CIV	ELE*		← PHYLA1, ELELA2, EGDLA3 →		
	B	CIV	ELE		CLA*	EGD		EGH*	EGH*	
	C	CHE	ME*		CPS*	ELN		← CHEL C1 & CPL C2 →		
	D	CPS	ELN		CLA*	ME*				
Tuesday	A	CLA	PHY*		ELE*	CIV		← PHYLA2, ELELA3, EGDLA1 →		
	B	ELE*	CIV		CLA	PHY		CIV*	EGD	
	C	CPS	CHE		ME*	CLA*		← CHEL C2 & CPL C3 →		
	D	CHE	ELN*		CLA	ME				
Wednesday	A	EGD	ELE		PHY	CLA*		← PHYLA3, ELELA1, EGDLA2 →		
	B	PHY	CLA		CIV	ELE*				
	C	ELN	CHE*		CLA*	CPS*		← CHEL C3 & CPL C1 →		
	D	ME	CHE		ELN	CPS*				
Thursday	A	PHY	CIV*		CLA	ELE*		EGH*	EGH*	
	B	EGD	ELE		PHY*	CLA*		← PHYLA2, ELELA1, EGDLA3 →		
	C	ME	CLA		CHE	ELN*		EGH*	EGH*	
	D	ELN*	ME*		CHE*	CPS*		← CHEL D1 & CPL D2 →		
Friday	A	CIV*	CLA		EGD	PHY				
	B	CLA	PHY*		CIV*	ELE*		← PHYLA3, ELELA2, EGDLA1 →		
	C	CLA	CPS		ELN*	ME*		CHE*		
	D	CHE	CPS		CLA	ME*		← CHEL D2 & CPL D3 →		
Saturday	A	ELE	EGD		CIV*	***				
	B	PHY	← PHYLA2, ELELA3, EGDLA1 →							
	C	ME*	ELN*		CLA	CPS*				
	D	CLA	← CHEL D3 & CPL D1 →							

Faculty Details

A			B			C			D		
Staff Name	Sub.	Code	Staff Name	Sub.	Code	Staff Name	Sub.	Code	Staff Name	Sub.	Code
SAP	MAT	18MAT21	SLP	MAT	18MAT21	SIS	MAT	18MAT21	SST	MAT	18MAT21
VMB	PHY	18PHY22	VMB	PHY	18PHY22	MSH	CHE	18CHE22	SJW	CHE	18CHE22
SDH	ELE	18ELE23	SDH	ELE	18ELE23	SVM	CPS	18CPS23	SGG	CPS	18CPS23
MMN	CIV	18CIV24	MMN	CIV	18CIV24	RRM	ELN	18ELN24	RRM	ELN	18ELN24
DNI	EGD	18EGD25	DNI	EGD	18EGD25	SNT	ME	18ME25	KMA	ME	18ME25
DNI	EGDL	18EGDL25	MIT	EGDL	18EGDL25	MSH	CHEL	18CHEL26	SJW	CHEL	18CHEL26
VMB	PHYL	18PHYL26	VMB	PHYL	18PHYL26	RRP	CPL	18CPL27	MAC	CPL	18CPL27
SGH	ELEL	18ELEL27	SGH	ELEL	18ELEL27	SAN	EGH	18EGH28	SAN	EGH	18EGH28
SAN	EGH	18EGH28	SAN	EGH	18EGH28						

[Signature]
 CO-ORDINATOR

[Signature]
CO-ORDINATOR
First Year Engineering
Hirasugar Institute of Technology,
NIDASOSHI-591 236

[Signature]
PRINCIPAL
Hirasugar Institute of Technology
Nidasoshi- 591 236



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ECE Dept

Academic

Workload

2020-21 (Odd)

Workload 2020-21 (Odd)

S. No	Faculty Name	Sem. / Div.	Subject/ Subject Code	Theory	Practical	Total Hrs /week	Total Units/ Week	Staff Sign.
				Hrs/ Week	Hrs/ Wee			
01	Dr. Ravi M Yadahalli	VII	Power Electronics (17EC73)	04		08	16+3*	
		I	Basic Electronics (18ELN14)	04				
02	Prof. M. M. Gadag	VII	Microwave and Antennas (17EC71)	04		16	23+3*	
		VII	Adv. Communication Lab (17ECL76) 3 Batch		09			
		III	Power Electronics & Instrumentation (18EC36)	03				
03	Prof. S. S. Kamate	VII	VLSI Lab (17ECL77)- 03 Batches		09	15	21+3*	
		V	Electromagnetic Waves (18EC55)	03				
		III	Computer Organization & Architecture (18EC35)	03				
04	Prof. S. B. Akkole	VII	Satellite Communication (17EC755)	03		16	24+3*	
		V	Principles of Communication Systems (18EC53)	05				
		III	ED& I Lab (18ECL37) -2 Batches		08			
05	Prof. S. S. Malaj	VII	Multimedia Communication (17EC741)	03		18	24+3*	
		V	Technological Innovation M & E (18ES51)	03				
		III	Digital System Design Lab (18ECL38) -03Batches		12			
06	Prof. D. M. Kumbhar	III	Electronic Devices (18EC33)	03		19	26+3*	
		III	ED& I Lab (18ECL37) -3 Batches		12			
		I	Basic Electronics (18ELN14)	04				
07	Prof. S. S. Patil	VII	Real Time Systems(17EC743)	03		18	24+3*	
		V	Verilog HDL (18EC56)	03				
		V	HDL Lab(18ECL58) -03 Batches		12			
08	Prof. D. B. Madihalli	V	Information Theory & Coding (18EC54)	03		18	24+3*	
		III	Digital System Design (18EC34)	03				
		III	Digital System Design Lab (18ECL38) -03Batches		12			
09	Prof. P. V. Patil	V	HDL Lab(18ECL58) -03 Batches		12	17	22+3*	
		III	Network Theory (18EC32)	05				
10	Prof. S. S. Ittannavar	V	Digital Signal Processing (18EC52)	05		17	22+3*	
		V	Digital Signal Processing Lab(18ECL57) -3 Batch		12			
11	Prof. B P Khot	VII	Digital Image Processing(17EC72)	04		20	24+3*	
		V	Digital Signal Processing Lab(18ECL57) -3 Batch		12			
		III	ED& I Lab (18ECL37) -01 Batches		04			

Note: Project Work Phase -I (17ECP78) *

HOD 7/8/2020
 Electronics & Comm. Engg. Dept
 HSIT NIDASOSHI

Principal

Subject Title	MICROWAVES AND ANTENNAS		
Subject Code	17EC71	CIE Marks	40
Number of Lecture Hrs /	04	SEE Marks	60
Total Number of Lecture Hrs	50(10Hrs/Module)	Exam Hours	03
CREDITS – 04			

FACULTY DETAILS:		
Name: 1) Prof. S.S.Malaj	Designation: 1) Asst. Professor	Experience: 1) 22 years
No. of times course taught:01	Specialization: E & TC	

1.0 Prerequisite Subjects:

Sl. No	Branch	Semester	Subject
01	ECE	III	Engineering Electromagnetic
02	ECE	IV	Principles of Communication Systems

2.0 Course Objectives

The course objective is to make students of ECE branch of engineering to understand the fundamentals of Microwaves and Antennas for Communication Engineering Applications.

3.0 Course Outcomes

Having successfully completed this course, the student will be able to

	Course Outcome	RBT Level	PO's
CO401.1	Describe the use and advantages of microwave transmission	L1, L2	1,2,3,4,7,10
CO401.2	Analyze various parameters related to microwave transmission lines and waveguide	L1, L2	1,2,3,4,6
CO401.3	Identify microwave devices for several applications	L1, L2, L3	1,2,3,4
CO401.4	Analyze various antenna parameters necessary for building an RF system	L1, L2, L3, L4	1,2,3,4,6
CO401.5	Recommend various antenna configurations according to the applications	L1, L2, L3	1,2,3,4,6
Total Hours of instruction		50	

4.0 Course Content

Modules	Teaching Hours	Bloom's Taxonomy (RBT) level
Module 1	10	L1, L2
Microwave Tubes: Introduction, Reflex Klystron Oscillator, Mechanism of Oscillations, Modes of Oscillations, Mode Curve (Qualitative Analysis only). (Text 1: 9.1, 9.2.2) Microwave Transmission Lines: Microwave Frequencies, Microwave devices, Microwave Systems, Transmission Line equations and solutions, Reflection Coefficient and Transmission Coefficient, Standing Wave and Standing Wave Ratio, Smith Chart, Single Stub matching. (Text 2: 0.1, 0.2, 0.3, 3.1, 3.2, 3.3, 3.5, 3.6 Except Double stub matching) L1, L2		
Module -2	10	L1, L2
Microwave Network theory: Symmetrical Z and Y-Parameters for Reciprocal Networks, S matrix representation of Multi-Port Networks. (Text 1: 6.1, 6.2, 6.3) Microwave Passive Devices: Coaxial Connectors and Adapters, Attenuators, Phase Shifters, Waveguide Tees, Magic tees. (Text 1: 6.4.2, 6.4.14, 6.4.15, 6.4.16) L1, L2		
Module-3	10	L1,L2,L3
Strip Lines: Introduction, Micro Strip lines, Parallel Strip lines, Coplanar Strip lines, Shielded Strip Lines. (Text 2: Chapter 11) Antenna Basics: Introduction, Basic Antenna Parameters, Patterns, Beam Area, Radiation Intensity, Beam Efficiency, Directivity and Gain, Antenna Apertures, Effective Height, Bandwidth, Radio Communication Link, Antenna Field Zones & Polarization. (Text 3: 2.1- 2.11, 2.13,2.15) L1, L2, L3		
Module-4	10	L1, L2,L3,L4
Point Sources and Arrays: Introduction, Point Sources, Power Patterns, Power Theorem, Radiation Intensity, Field Patterns, Phase Patterns, Arrays of Two Isotropic Point Sources, Pattern Multiplication, Linear Arrays of n Isotropic Point Sources of equal Amplitude and Spacing.(Text 3: 5.1 – 5.11, 5.13) Electric Dipoles: Introduction, Short Electric Dipole, Fields of a Short Dipole (General and Far Field Analyses), Radiation Resistance of a Short Dipole, Thin Linear Antenna (Field Analyses), Radiation Resistances of Lambda/2 Antenna. (Text 3: 6.1 -6.6) L1, L2,L3,L4		
Module-5	10	L1, L2,L3
Loop and Horn Antenna: Introduction, Small loop, Comparison of Far fields of Small Loop and Short Dipole, The Loop Antenna General Case, Far field Patterns of Circular Loop Antenna with Uniform Current, Radiation Resistance of Loops, Directivity of Circular Loop Antennas with Uniform Current, Horn antennas Rectangular Horn Antennas.(Text 3: 7.1-7.8, 7.19, 7.20) Antenna Types: Helical Antenna, Helical Geometry, Practical Design Considerations of Helical Antenna, Yagi-Uda array, Parabola General Properties, Log Periodic Antenna. (Text 3: 8.3, 8.5, 8.8, 9.5, 11.7) L1, L2, L3		

5.0 Gap Analysis and Mitigation

Sl. No	Delivery Type	Details
01	Tutorial	Topic: Antenna Simulation using Software's

6.0 Books Used and Recommended to Students

Text Books

1. Microwave Engineering – Annapurna Das, Sisir K Das TMH Publication, 2nd, 2010.
2. Microwave Devices and circuits- Liao, Pearson Education.
3. Antennas and Wave Propagation, John D. Krauss, Ronald J Marhefka and Ahmad S Khan, 4th Special Indian Edition, McGraw- Hill Education Pvt. Ltd., 2010.

Reference Books

1. Microwave Engineering – David M Pozar, John Wiley India Pvt. Ltd. 3rdEdn, 2008.
2. Microwave Engineering – Sushrut Das, Oxford Higher Education, 2ndEdn, 2015.
3. Antennas and Wave Propagation – Harish and Sachidananda: Oxford University, Press, 2007.

7.0 Relevant Websites (Reputed Universities and Others) for Notes/Animation/Videos Recommended

Website and Internet Contents References

- 1) nptel.ac.in
- 2) [VTU e-learning notes](#)

8.0 Magazines/Journals Used and Recommended to Students

Sl. No	Magazines/Journals	website
1	Microwave Devices	www.ieee.org

9.0 Examination Note

Scheme of Evaluation for CIE (40 Marks)

Internal Assessment test will be done in the same pattern as that of the main examination.

Internal Assessment: 30 Marks

Assignment: 10 Marks

SCHEME OF EXAMINATION: 100 Marks, scaled down to 60 in VTU result sheet.

The question paper will have ten questions.

- Each full question is for 20 marks.
- There will be 2 full questions (with a maximum of three sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.

10.0 Course Delivery Plan

Module No.	Lecture No.	Content of Lecture	% of Portion
1	1	Limitation of Conventional Tubes	20
	2	Reflex Klystron Oscillator	
	3	V-I Characteristics	
	4	Microwave Frequencies and Devices	
	5	Transmission Line equations	
	6	Reflection and Transmission Coefficients	
	7	Standing Waves	

	8	Smith Charts	
	9	Single Stub Matching	
	10	Numerical Problems.	
2	11	Microwave Network Theory	40
	12	Z-Y Parameters, and 'S' Parameters	
	13	Multiport Networks	
	14	Co-Axial connectors and Adapters	
	15	Attenuators	
	16	Phase Shifters	
	17	Wave guide Tees-Eplane	
	18	H-Plane Tee	
	19	Magic Tees	
	20	Numerical Problems	
3	21	Strip Lines-Introduction	60
	22	Micro-strip Lines	
	23	Parallel Strip Lines	
	24	Co-Planar strip Lines	
	25	Shielded Strip Lines	
	26	Antenna Basics	
	27	Antenna Apertures	
	28	Radio Communication Links	
	29	Antenna Zones	
	30	Numerical Problems	
4	31	Point Sources-Introduction	80
	32	Power Pattern and Power Theorem	
	33	Field Patterns and Phase Patterns	
	34	Pattern Multiplication	
	35	Antenna Arrays of n isotropic sources	
	36	Short Electric Dipole	
	37	Field and Radiation Resistance	
	38	Thin Linear Antenna	
	39	Lambda by two Antenna ($\lambda/2$)	
	40	Radiation Resistance of $\lambda/2$ antenna	
5	41	Loop Antennas-Small Loop	100
	42	Loop and Short Dipole antenna comparison	
	43	Loop Antenna-General Case	
	44	Circular Loop	
	45	Horn Antenna	
	46	Helical Antenna	
	47	Yagi-Uda Antenna	
	48	Parabolic Antenna	
	49	Log-Periodic Antenna	
	50	Numerical Problems	

11.0

QUESTION BANK

Module – 1

1. Explain the Velocity Modulation using Apple-Gate diagram.
2. Explain Mode characteristics of Reflex Klystron with suitable diagram.

3. Derive the Voltage and Current equations for a general Transmission line and obtain ABCD parameters.
4. Explain the construction and working of Smith Chart.
5. Explain different methods of impedance matching for a transmission line.
6. Derive the general transmission line equation to find voltage & current on the line in terms of position 'z' & time 't'.
7. A transmission line has a characteristic impedance of $50 + j0.01 \Omega$ & terminated in a load impedance of $73 - j42.5 \Omega$. Calculate i) reflection coefficient ii) SWR
8. Define reflection coefficient. Derive an equation for reflection coefficient at the load end at a distance 'd' from the load.
9. Describe the mechanism of oscillation of reflex klystron.
10. A transmission line has following parameters $R=2 \Omega/m$ $G=0.5 \text{ mmho/m}$, $f=1 \text{ GHz}$ $L=8 \text{ nH/m}$, $C=0.23 \text{ pF/m}$
11. Calculate i) characteristic impedance ii) Propagation Constant

Module-2

1. Explain the Scattering matrix and Scattering Parameters.
2. What are Multi-Port network? Derive the 'S' matrix.
3. Explain the working of Phase Shifters.
4. Draw the construction of E-Plane and H-Plane tee and derive the 'S' matrix for both.
5. Explain the working of a Magic Tee and derive the 'S' parameter.
6. A 20mw signal is feed into one of the Collinear port1 of a lossless H plane T Junction. Calculate the power delivered through each port when other ports are terminated in matched load.
7. 2 transmission lines of characteristic impedance Z_1 & Z_2 are joined at plane PP'. Express S parameters in terms of impedance when each line is matched terminated.

Module-3

1. Explain the construction of different types of Micro-Strip lines.
2. Explain with design equation the design of various strip-Lines.
3. Explain various terms related to antenna beam width, Radiation pattern, directivity, Gain & Efficiency.
4. Draw a radio link & explain various blocks.
5. Explain with suitable diagram, the concept of antenna polarization.
6. The effective aperture of transmitting & receiving antennas in a communication system are $8\lambda^2$ & $12\lambda^2$ respectively, with a 1.5km separation between them. The EM wave travelling with frequency of 6MHz & the total input power is 25kw. Find the power received by receiving antennas.
7. An antenna has a field pattern given by $E(\theta) = \cos^2(\theta)$ for $0 \leq \theta \leq \pi/2$. Find the beam area & directivity.

Module – 4

1. Explain power patterns & power theorem with suitable diagrams.
2. Derive the total field strength of two isotropic sources amplitude fed with same amplitude & same phase.
3. Derive the total field strength of n isotropic sources amplitude fed with same amplitude & same phase.
4. Derive the field equation for short dipole & obtain the equation of radiation resistance.
5. Explain & derive the equation of $\lambda/2$ dipole.
6. Show that the radiation resistance of $\lambda/2$ is 73Ω .
7. A source has a radiation intensity power pattern given by $U = U_m \sin^2(\theta)$ for $0 \leq \theta \leq \pi/2$; $0 \leq \phi \leq 2\pi$. Find the total power & directivity.

Module – 5

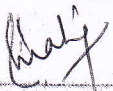
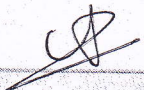
1. Give the comparison of fields of small loop & short electric dipole.
2. Obtain the fields due to square loop & circular loop.
3. Explain the design of Rectangular Horn Antenna with suitable diagrams.
4. Explain the construction & working of Helical antenna, Yagi-Uda Antenna with Suitable design

equations.

5. Explain the construction & working of Parabolic Antenna & Log-periodic antenna with suitable design equations.
6. A 16 turn helical beam antenna have a circumference of λ & turn spacing of $\lambda/4$. Find i) HPBW ii) axial ratio iii) directivity.
7. Show that the radiation resistance of small loop is $31171(\lambda/\lambda^2)^2$.
8. Determine the length L, H plane aperture & flare angles θ_L & θ_H of a pyramidal horn for which the E plane aperture $a_E = 10\lambda$. Let $\delta = 0.2\lambda$. In the E plane & 0.375λ in the H plane. Also determine the beam widths & directivity.

12.0 University Result

Examination	S+	S	A	B	C	D	E	% Passing
January 2020	01	04	20	09	04	00	00	88.09
January 2019	04	00	10	18	17	13	01	94.54
January 2018	0	1	6	10	13	04	03	98.46

Prepared by	Checked by		
			
Prof. S. S. Malaj	Mr. S B Akkole	HOD	Principal

Subject Title	DIGITAL SYSTEM DESIGN LAB		
Subject Code	18ECL38	CIE Marks	40
Number of Lecture Hrs / Week	02 Hr Tutorial (Instructions) + 02 Hrs Laboratory = 04	SEE Marks	60
		Exam Hours	03
CREDITS – 02			

FACULTY DETAILS:

Name: Prof. D B Madihalli Prof.B.P.Khot	Designation: Asst. Professor Asst. Professor	Experience: 13 years 4.5 years
No. of times course taught: 05 01	Specialization: Industrial Electronics ME & CS	

1.0 Prerequisite Subjects:

Sl. No	Branch	Semester	Subject
01	Electronics & Communication Engineering	I/II	Basic Electronics
02	Electronics & Communication Engineering	III	Digital Electronics

2.0 Course Objectives

This course will enable students to get practical experience in design , realisation & verification of

- Demorgan's Theorem , SOP , POS forms.
- Full / Parallel Adders , Subtractors & Magnitude Comparator.
- Multiplexer using logic gates.
- Demultiplexers & Decoders.
- Flip-Flops , Shift registers & Counters.

3.0 Course Outcomes

At the end of the course students will be able to:

	Course Outcome	RBT Level	POs
08.1	Design , realize and verify DeMorgan's theorems, SOP & POS forms	L1,L2,L3,L4	PO1,2,3,4,5,6,9,10,11,12.
C208.2	Demonstrate the truth table of various expressions & combinational circuits using logic gates.	L1,L2,L3,L4	PO1,2,3,4,5,6,9,10,11,12.
C208.3	Design various combinational circuits such as adders, sub tractors, comparators, multiplexers and demultiplexers.	L1,L2,L3,L4	PO1,2,3,4,5,6,9,10,11,12.
C208.4	Construct flip-flops, counters & shift registers.	L1,L2,L3,L4	PO1,2,3,4,5,6,9,10,11,12.
C208.5	Simulate serial adder & binary multiplier.	L1,L2,L3,L4	PO1,2,3,4,5,6,9,10,11,12.
Total Hours of instruction			48

4.0 Course Content

Laboratory Experiments:

1	Verify (a) Demorgan's Theorem for 2 variables. (b) The sum -of product and product -of-sum expressions using universal gates.
2	Design and implement (a) Half Adder & Full Adder using (i) basic logic gates and (ii) NAND gates. (b) Half Subtractor & Full subtractor using (i) basic logic gates and (ii) NANAD gates
3	Design and implement of (a) 4-bit Parallel Adder/ Subtractor using IC 7483. (b) BCD to Excess-3 code conversion and vice versa.
4	Design and Implementation of (a) 1-bit Comparator. (b) 5-bit Magnitude Comparator using IC 7485.
5	Realize (a) Adder & Subtractors using IC 74153. (b) 4-variable function using IC 74151(8:1MUX).
6	Realize (a) Adder & Subtractors using IC74139. (b) Binary to Gray code conversion & vice versa (IC74139).
7	Realize the following flip/flops using NAND gates. Master-Slave JK, D & T Flip-Flop.
8	Realize the following shift registers using IC7474/IC 7495 (a)SISO (b) SIPO (c) PISO (d) PIPO (e) Ring and (f) Johnson counter.
9	Realize (i) Design Mod-N Synchronous Up counter & Down Counter using 7476 JK F/F. (ii) Mod-N Asynchronous Counter using IC7490/7476. (ii) Mod-N Synchronous counter using IC74192.
10	Design Pseudo Random Sequence generator using 7495.
11	Design serial adder with accumulator and simulate using simulation tool.
12	Design binary multiplier and simulate using simulation tool.

5.0 Relevance to future subjects

Sl No	Semester	Subject	Topics
01	VIII	Project work	Digital Electronics based projects

6.0 Relevance to Real World

SL.No	Real World Mapping
01	Analyze different types of digital circuits in digital systems.
02	Design of adders , counters & multiplexers.

7.0 Gap Analysis and Mitigation

SL. No	Delivery Type	Details
01	Tutorial	Topic : Design of adders , counters & multiplexers.
02	NPTEL	Digital Electronics Circuits

8.0 Books Used and Recommended to Students

Text Books
1.Digital System Design Lab Manual

9.0 Relevant Websites (Reputed Universities and Others) for Notes /Animation / Videos Recommended

Website and Internet Contents References
4) https://vtu.ac.in
5) http://www.bookspare.com/engineering-vtu
3) http://www.slideshare.net/farohalolya/digital electronics - lab-manual
4) https://www.youtube.com/results?search_query=digital electronics circuit

10.0 Magazines/Journals Used and Recommended to Students

Sl.No	Magazines/Journals	website
1	IEEE	http://ieeexplore.ieee.org/Xplore/home.jsp

11.0 Examination Note

Scheme of Evaluation for Internal Assessment (40 Marks)

- (a) Continuous Assessment 30 marks.
- (b) Internal Assessment Test 10 marks (Write Up = 1.5M, Conduction = 07M, Viva = 1.5M)

SCHEME OF EXAMINATION:

Two questions to be set. Student has to answer both full questions. 100 Marks divided in three parts 15M = Write up marks, 70M = Conduction marks & 15M = Viva marks.

12.0 Course Delivery Plan

Experiment	Lecture No.	Content	% of Portion
1	1	Verify (a) Demorgan's Theorem for 2 variables. (b) The sum -of product and product -of-sum expressions using universal gates.	7
2	2	Design and implement (a) Half Adder & Full Adder using (i) basic logic gates and (ii) NAND gates. (b) Half Subtractor & Full subtractor using (i) basic logic gates and (ii) NANAD gates	14
3	3	Design and implement of (a) 4-bit Parallel Adder/ Subtractor using IC 7483. (b) BCD to Excess-3 code conversion and vice versa.	21

4	4	Design and Implementation of (a) 1-bit Comparator. (b) 5-bit Magnitude Comparator using IC 7485.	29
5	5	Realize (a) Adder & Subtractors using IC 74153. (b) 4-variable function using IC 74151(8:1MUX).	36
6	6	Realize (a) Adder & Subtractors using IC74139. (b) Binary to Gray code conversion & vice versa (IC74139).	43
7	7	Realize the following flip/flops using NAND gates. Master-Slave JK, D & T Flip-Flop.	50
8	8	Realize the following shift registers using IC7474/IC 7495 (a)SISO (b) SIPO (c) PISO (d) PIPO (e) Ring and (f) Johnson counter.	64
9	9	Realize (i) Design Mod-N Synchronous Up counter & Down Counter using 7476 JK F/F. (ii) Mod-N Asynchronous Counter using IC7490/7476. (ii) Mod-N Synchronous counter using IC74192.	72
10	10	Design Pseudo Random Sequence generator using 7495.	86
11	11	Design serial adder with accumulator and simulate using simulation tool.	93
12	12	Design binary multiplier and simulate using simulation tool.	100

13.0 VIVA BANK

1. What is digital gate?
2. What is universal gate?
3. What is truth table?
4. What is the difference between Ex-OR and Ex-NOR gate.
5. What is demorgan's theorem?
6. What are demorgan's theorem equations?
7. What is the use of full adder?
8. In full adder , how many inputs are used.
9. In output of full adder what we gate.
10. What is the equation of SUM of full adder.
11. How many half adders are required to make full adder.
12. In full adder how many types of gates are required.
13. Draw the half adder diagram.
14. What is the difference between half & full adder.
15. Write the expression of CARRY of full adder.
16. Draw the diagram of half subtractor.
17. What is the difference between half & full subtractor.
18. What is magnitude comparator?

19. What is most significant bit ?
20. What is the operation of AND gate ?
21. What is the operation of comparator's truth table. ?
22. What is equality & inequality ?
23. What is 8 input magnitude comparator ?
24. What is IC ?
25. What is flipflop?
26. How many types of flipflop are used?
27. What is disadvantage of SR F/F?
28. What is disadvantage of JK F/F ?
29. To remove race around condition what we use?
30. What is race around condition?
31. What is the use of D F/F ?
32. What is multiplexer ?
33. What is demultiplexer ?
34. What is counter ?
35. What are the types of counter ?
36. What is ripple counter ?
37. What is Johnson counter ?
38. What is decade counter ?
39. What is synchronous counter ?
40. Write the truth table of 2 bit counter.
41. What is ring counter ?
42. How many stages are required for decade counter.
43. What causes specified condition to shift position.
44. What is shift register ?
45. What is clock enable ?
46. What is clock ?
47. What is serial adder ?
48. What is binary multiplier ?

14.0 University Result

Examination	S+	S	A	B	C	D	E	F	% Passing
Dec-2018/Jan-2019	15	12	04	02	05	03	00	00	100

Examination	S+	S	A	B	C	D	E	F	% Passing
Dec-2019/Jan-2020	13	12	04	03	05	03	00	00	100

Prepared by	Checked by		
 Prof. D.B. Madihalli Prof. B.P. Khot	 Prof. S. B. Akkole	 HOD	 Principal

LESSON PLAN

Course: Turbomachines

Code: 15MES3/17MES3/18MES4 Lect. Per week: 03

Sem: V

Module/ Unit	Details of content to be taught	Date		Signature	
		Planned	Actual	Faculty	HOD
01	Course objective, Syllabus, Course outcome, Test books & reference books.	02-09-20	01-09-20	[Signature]	
02	Definition of Turbo machines, Classification of Turbo machines	04-09-20	02-09-20	[Signature]	
03	Comparison of positive displacement and Turbo machines, Dimensionless number	07-09-20	04-09-20	[Signature]	
04	Significance of TL terms, Speed ratio, Specific speed of turbine and pump	09-09-20	07-09-20	[Signature]	
05	Importance of Specific Speed, unit quantities and Problems	11-09-20	09-09-20	[Signature]	
06	Problems and Model studies	14-09-20	11-09-20	[Signature]	
07	Problems and model studies	16-09-20	14-09-20	[Signature]	
08	Problems and model studies	18-09-20	15-09-20	[Signature]	
09	Static state, Stagnation state, Application of first law of thermodynamics and Second law of thermodynamics of turbo-machines	21-09-20	16-09-20	[Signature]	

Planned Hrs:

Actual Hrs:

10	Hydraulic and mechanical efficiencies, Various efficiencies such as f-e, e-s, s-e, t-e and problem	23-09-20	21-09-20	[Signature]	
11	Polytropic efficiency of turbine and compressor	25-09-20	23-09-20	[Signature]	
12	Finite stage efficiency of turbine and compressor.	09-10-20	25-09-20	[Signature]	

Planned Hrs: 12

Actual Hrs: 12

HOD

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Module 1: Introduction, Thermodynamics of fluid flow

LESSON PLAN

Course: Turbo machines

Code: 15MES3/17MES3/18MES4 Lect. Per week: 03

Sem.: V

Module/ Unit	Details of content to be taught	Date		Signature	
		Planned	Actual	Faculty	HOD
01	Euler's Turbine Equation	12-10-20	30-09-20	[Signature]	
02	Euler's Turbine Equation and its significance	14-10-20	12-10-20	[Signature]	
03	How to draw velocity Triangles	16-10-20	12-10-20	[Signature]	
04	How to draw velocity Triangles	19-10-20	13-10-20	[Signature]	
05	Alternate form of Euler's Turbine Eq.	21-10-20	14-10-20	[Signature]	
06	Components of energy transfer	23-10-20	19-10-20	[Signature]	
07	Axial flow turbine degree of reaction, utilization factor, impulse reaction turbine	28-10-20	21-10-20	[Signature]	
08	Relationships R & C and velocity diagrams for different R.	02-11-20	22-10-20	[Signature]	
09	Maximum utilization factor Condition for impulse and reaction turbine, Comparison of impulse and reaction turbine for maximum utilization.	04-11-20	23-10-20	[Signature]	
Planned Hrs:		Actual Hrs:			
10	Effect of discharge angle on E and R in Radial flow machines and Key points how to draw the velocity triangles, Formulae	06-11-20	28-10-20	[Signature]	
11	Power absorbing m/c's - Derivation of E & R Axial flow machines	09-11-20	02-11-20	[Signature]	
12	Power absorbing m/c's - Analysis of radial flow m/c's, H-Curve relations & imp formulae	12-11-20	04-11-20	[Signature]	
13	Problems	20-11-20	06-11-20	[Signature]	
14	problems	23-11-20	09-11-20	[Signature]	
15	Problems	25-11-20	11-11-20	[Signature]	
16	Problems	27-11-20	13-11-20	[Signature]	
Planned Hrs: 18		Actual Hrs: 16			

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LESSON PLAN

Course: Turbo machines

Code: 15MES3/17MES3/18MES4 Lect. Per week: 03

Sem.: V

Module-III
Steam turbine, Reaction turbine

Mod- ule/ Unit	Details of content to be taught	Date		Signature	
		Planned	Actual	Faculty	HOD
01	Introduction, impulse and reaction turbine, Analysis of single stage Steam turbine	30-11-20	18-11-20	L.	
02	Problems	02-12-20	20-11-20	L.	
03	Compounding, Need of Compounding and methods of compounding.	04-12-20	20-11-20	L.	
04	Analysis of multistage of Steam turbine	07-12-20	27-11-20	L.	
05	Analysis of Parsons reaction turbine	09-12-20	27-11-20	L.	
06	Problems	18-12-20	28-11-20	L.	

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28/11/20

Planned Hrs: 06

Actual Hrs: 06

Module-IV
Hydraulic Turbine

01	Hydraulic Turbines, Classification, Pelton wheel turbine, workdone, hydraulic efficiency, Condition for max. P.H., working proportion	21-12-20	30-11-20	L.	
02	Problems	22-12-20	01-12-20	L.	
03	Francis turbine, working, P.H., workdone, Call draft tube, work proportion, efficiency	22-12-20	02-12-20	L.	
04	Problems	23-12-20	03-12-20	L.	
05	Problems	24-12-20	03-12-20	L.	
06	Kaplan Turbine, working velocity triangles, work expression, problems	24-12-20	04-12-20	L.	

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04/12/20

Planned Hrs: 06

Actual Hrs: 06

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HOD
Mechanical Engg.
HIT, Nidasoshi

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HIT, Nidasoshi

LESSON PLAN

Course: Turbomachines Code: SMES3/17MES3 18MES4 Lect. Per week: 03

Sem.: IV

Module/ Unit	Details of content to be taught	Date		Signature	
		Planned	Actual	Faculty	HOD
01	Centrifugal Pump Classification, Terminology of CP, velo. Triangles, working efficiencies	28-12-20	04-12-20	<i>[Signature]</i>	
	Types of vanes, and H-Q Curve Characteristics	28-12-20	05-12-20	<i>[Signature]</i>	
	Prerotation, slip factor and coefficient				
	Minimum starting speed, Priming, Cavitation, NPSP, Cavitation factor, Precaution preventing Cavitation, effects of Cavitation, Multi-stage Pumps and casing	29-12-20	09-12-20	<i>[Signature]</i>	
04	Problems	29-12-20	11-12-20	<i>[Signature]</i>	
05	Problems	30-12-20	12-12-20	<i>[Signature]</i>	
06	Centrifugal Compressor, introduction, Elements of Compressor and principle of operation, variation of Pr. velo. different types of vanes, and its velo. dia, effect of exit blade angles on backward, radial and forward vanes	30-12-20	14-12-20	<i>[Signature]</i>	
Planned Hrs:		Actual Hrs:			
07	H-Q Curve relationship, work done on compressor, overall pressure ratio, slip factor, Power input factor, Pr. coefficient, relation bet. Slip factor, power input factor and Pressure Coefficient	31-12-20	16-12-20	<i>[Signature]</i>	
08	Surging and choking, Problems	31-12-20	23-12-20	<i>[Signature]</i>	
Planned Hrs: 08		Actual Hrs: 08			

Centrifugal Pump, Centrifugal Compressor

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 Programmes Accredited by NBA: CSE, ECE, EEE & ME

SJPNT Trust's

Examination

Academic

IA - I

2020-21 (ODD SEM)

TIME-TABLE: IA TEST - I

Class: 3rd and 5th Semesters

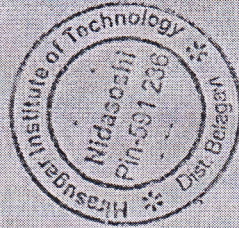
Date→ Time→ Semester↓	05-10-2020		06-10-2020		07-10-2020	
	11AM - 12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM
BE-III	18**31	18**32	18**33	18**34	18**35/18ME35B	18**36/18ME36B
BE-V	18**51/17**51	18**52/17**52	18**53/17**53	18**54/17**54	18**55/17**55X	18**56X/17**56X
18CPH29 (Constitution of India, Professional Ethics and Cyber Law): 07-10-2020:4:30pm to 5:30pm for III sem ME and EEE						
18KVK39/18KAK39(Vyavarika Kannada/Aadalitha Kannada: 07-10-2020:4:30pm to 5:30pm for III sem CSE and ECE						
18CIV59 :Environmental Studies: 07-10-2020 :4:30pm to 5:30pm for all Branches						

Instructions:

1	Subject teacher will release IA question paper at 10:50 am (Morning Session) and at 2:50pm (Afternoon Session) in the students' respective WhatsApp group.
2	Student shall write the test on available white pages only
3	Student shall write the following information on the top of the first page: IA Test-I, Name:, USN:, Roll No.:, Subject:, Subject Code:, Date:, Time:, Total No. of Pages written (after completion of the test)
4	Student shall write page number on each page and signature at the end of the each page
5	Student shall submit completed answer script in single PDF to the subject teacher's personal WhatsApp No./E-mail before 12:10pm (Morning Session) and 4:10pm (Afternoon Session)

Note: All are informed to strictly adhere to the above instructions and kindly cooperate to complete the IA in view of COVID-19 pandemic situation.

Dr. S. N. Topannavar
Examination Coordinator



Dr. S. C. Kamate
Principal

Nidasoshi-591 236, Taq: Hukkeri, Dist: Belagavi, Karnataka, India.
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 Accredited at 'A' Grade by NAAC
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Examination
Academic
2 nd IA Test
2020-21 (ODD SEM)

2nd IA Test TIME-TABLE

Class: 3rd, 5th and 7th Semesters

Date → Time → Semester ↓	23-11-2020		24-11-2020		25-11-2020	
	11AM - 12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM
BE-III	18**31	18**32	18**33	18**34	18**35/18ME35B	18**36/18ME36B
BE-V	18**51/17**51	18**52/17**52	18**53/17**53	18**54/17**54	18**55/17**55X	18**56X/17**56X
BE-VII	17**71/15**71	17**72/15**72	17**73/15**73	17**74X/15**74X	17**75X/15**75X	

18CPH39 (Constitution of India, Professional Ethics and Cyber Law): 25-11-2020:4:30pm to 5:30pm for III sem ME and EEE
 18KVK39/18KAK39 (Vyavahika Kannada/Aadaittha Kannada): 25-11-2020:4:30pm to 5:30pm for III sem CSE and ECE
 18CIV59 :Environmental Studies : 25-11-2020 :4:30pm to 5:30pm for all Branches

Instructions:

- 1 Subject teacher will release IA question paper at 10:50 am (Morning Session) and at 2:50pm (Afternoon Session) in the students' respective WhatsApp group.
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IA Test -I, Name:, USN:, Roll No:, Subject:, Subject Code:, Date:, Time:, Total No. of Pages written (after completion of the test)
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Dr. S. N. Topanavar
Examination Coordinator



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TIME-TABLE: IA TEST - I

Class: 7th Semester

Date → Time → Semester ↓	19-10-2020		20-10-2020		21-10-2020
	11AM - 12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM	11AM - 12 NOON
BE-VII	17**71/15**71	17**72/15**72	17**73/15**73	17**74X/15**74X	17**75X/15**75X

Instructions:

1	Subject teacher will release IA question paper at 10:50 am (Morning Session) and at 2:50pm (Afternoon Session) in the students' respective WhatsApp group.
2	Student shall write the test on available white pages only
3	Student shall write the following information on the top of the first page: IA Test -I, Name:, USN:, Roll No.:, Subject:, Subject Code:, Date:, Time:, Total No. of Pages written (after completion of the test)
4	Student shall write page number on each page and signature at the end of the each page
5	Student shall submit completed answer script in single PDF to the subject teacher's personal WhatsApp App No./E-mail before 12:10pm (Morning Session) and 4:10pm (Afternoon Session)

Note: All are informed to strictly adhere to the above instructions and kindly cooperate to complete the IA in view of COVID-19 pandemic situation.

[Signature]
Dr. S. N. Topannavar
Examination Coordinator



[Signature]
Dr. S. C. Kamate
Principal

Nidasoshi-591 236, Tag: Hukkeri, Dist: Belagavi, Karnataka, India.
Phone: +91-8333-278887, Fax: 278886, Web: www.hsit.ac.in, E-mail: principal@hsit.ac.in

Examination
Academic
IA - I
2020-21 (ODD SEM)



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Examination

Academic

3rd IA Test

AY:2020-21 (ODD SEM)

3rd IA Test TIME-TABLEClass: 3rd, 5th and 7th Semesters

Date → Time → Semester ↓	28-12-2020		29-12-2020		30-12-2020	
	11AM - 12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM
BE-III	18**31	18**32	18**33	18**34	18**35/18ME35B	18**36/18ME36B
BE-V	18**51/17**51/15**51	18**52/17**52/15**52	18**53/17**53/15**53	18**54/17**54/15**54	18**55/17**55X/15**55X	18**56X/17**56X/15**56X
BE-VII	17**71/15**71	17**72/15**72	17**73/15**73	17**74X/15**74X	17**75X/15**75X	

SCPH39 (Constitution of India, Professional Ethics and Cyber Law); 30-12-2020:4:30pm to 5:30pm for III sem ME and EEE

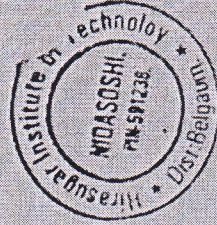
SKVK39/18KAK39 (Vyavasthika Kannada/Aadaliha Kannada: 30-12-2020:4:30pm to 5:30pm for III sem CSE and ECE

SCIVS9 - Environmental Studies : 30-12-2020 :4:30pm to 5:30pm for all Branches

Instructions:

1	Subject teacher will release IA question paper at 10:50 am (Morning Session) and at 2:50pm (Afternoon Session) in the students' respective WhatsApp group.
2	Student shall write the test on available white pages only
3	Student shall write the following information on the top of the first page: IA Test-I, Name:, USN:, Roll No.:, Subject:, Subject Code:, Date:, Time:, Total No. of Pages written (after completion of the test)
4	Student shall write page number on each page and signature at the end of the each page
5	Student shall submit completed answer script in single PDF to the subject teacher's personal WhatsApp No./E-mail before 12:10pm (Morning Session) and 4:10pm (Afternoon Session)

All are informed to strictly adhere to the above instructions and kindly cooperate to complete the IA in view of COVID-19 pandemic situation.

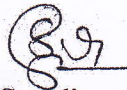
Dr. S. N. Topannavar
Examination CoordinatorDr. S. C. Kamate
PrincipalNidasoshi-591 236, Taq: Hukkeri, Dist: Belagavi, Karnataka, India.
Phone: +91-8333-278887, Fax: 278886, Web: www.hsit.ac.in, E-mail: principal@hsit.ac.in

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			Exam.
			Practical IA
			2020-21(Odd Sem)

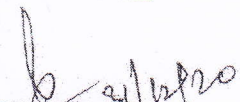
Department of Electronics & Communication Engg.

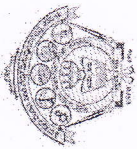
Practical I.A.Time Table-2020-21(ODD)

Sem	Date	Time	Name of The Lab	Lab Code	Batch
VII	05/01/2021	9.00 AM to 12.00 PM	Advanced Communication Lab	17ECL76	B1(01-12)
	05/01/2021	11.30 AM to 2.30 PM	Advanced Communication Lab	17ECL76	B2(13-24)
	05/01/2021	2.00 PM to 5.00 PM	Advanced Communication Lab	17ECL76/ 15ECL76	B3(25-35)
	06/01/2021	9.00 AM to 12.00 PM	VLSI Lab	17ECL77	B1(01-12)
	06/01/2021	11.30 AM to 2.30 PM	VLSI Lab	17ECL77	B2(13-24)
	06/01/2021	2.00 PM to 5.00 PM	VLSI Lab	17ECL77/ 15ECL77	B3(25-35)
V	18/01/2021	9.00 AM to 12.00 PM	HDL Lab	18ECL58	B1(01-13)
	18/01/2021	11.30 AM to 2.30 PM	HDL Lab	18ECL58	B2(14-26)
	18/01/2021	2.00 PM to 5.00 PM	HDL Lab	18ECL58/ 17ECL58	B3(27-38)
	19/01/2021	9.00 AM to 12.00 PM	DSP Lab	18ECL57	B1(01-13)
	19/01/2021	11.30 AM to 2.30 PM	DSP Lab	18ECL57	B2(14-26)
	19/01/2021	2.00 PM to 5.00 PM	DSP Lab	18ECL57/ 17ECL57	B3(27-38)
III	18/01/2021	9.00 AM to 12.00 PM	ED&I Lab	18ECL37	B1(01-10)
	18/01/2021	11.30 AM to 2.30 PM	ED&I Lab	18ECL37	B2(11-20)
	18/01/2021	2.00 PM to 5.00 PM	ED&I Lab	18ECL37	B3(21-31)
	19/01/2021	9.00 AM to 12.00 PM	DSD Lab	18ECL38	B1(01-10)
	19/01/2021	11.30 AM to 2.30 PM	DSD Lab	18ECL38	B2(11-20)
	19/01/2021	2.00 PM to 5.00 PM	DSD Lab	18ECL38	B3(21-31)


IA Coordinator


HOD
31/12/20


PRINCIPAL
Hirasugar Institute of Technology



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ECE DEPT
Academic
Feed Back-II
28-Nov-2020

NAME	DESIGNATION	SUBJECT	SEM	CLASS STRENGTH	RESPONDENTED	A	B	C	D	PERCENTAGE	AVERAGE
Prof. S I SHIVAMOGGIMATH	ASST. PROF.	TC&NT(18MAT31)	3	29	29	178	84	23	5	89.65	89.65
Prof. P V PATIL	ASST. PROF.	NT(18EC32)	3	29	29	178	92	19	1	90.75	93.44
Prof. D M KUMBHAR	ASST. PROF.	ED(18EC33)	3	29	29	70	175	38	17	80.75	80.75
Prof. D B MADIHALLI	ASST. PROF.	DSD(18EC34)	3	29	29	84	134	37	35	76.00	82.57
Prof. S S KAMATE	ASST. PROF.	COA(18EC35)	3	29	29	116	138	26	10	84.13	88.27
Prof. S S ITTANNAVAR	ASST. PROF.	PE&I(18EC36)	3	29	29	120	123	34	12	83.17	88.58
Prof. S S MALAJ	ASST. PROF.	M&ED(18ES51)	5	34+04*	38	148	189	40	03	85.36	85.19
Prof. S S ITTANNAVAR	ASST. PROF.	DSP(18EC52)	5	34+04*	38	280	87	12	01	94.00	88.58
Prof. S B AKKOLE	ASST. PROF.	PCS(18EC53)	5	34	34	245	95	00	00	94.41	94.51
Prof. D B MADIHALLI	ASST. PROF.	ITC(18EC54)	5	34+04*	38	203	154	17	06	89.15	82.57
Prof. S S KAMATE	ASST. PROF.	EW(18EC55)	5	34+04*	34	247	123	09	01	92.42	88.27
Prof. S S PATIL	ASST. PROF.	VHDL(18EC56)	5	34+04*	38	250	121	09	00	92.68	92.59
Prof. P V PATIL	ASST. PROF.	OS (17EC553)	5	04*	04	38	02	00	00	99.00	93.44
Prof. B P KHOT	ASST. PROF.	AE (17561)	5	04*	04	37	02	01	00	98.00	94.88
Prof. S S MALAJ	ASST. PROF.	M&A(17EC71)	7	35	35	149	140	61	00	85.02	85.19
Prof. B P KHOT	ASST. PROF.	DIP(17EC72)	7	35	35	216	124	10	00	91.77	94.88
Prof. P V PATIL	ASST. PROF.	PE(17EC73)	7	35	35	201	133	16	00	90.57	93.44
Prof. S S PATIL	ASST. PROF.	RTS(17EC743)	7	35	35	226	117	07	00	92.51	92.59
Prof. S B AKKOLE	ASST. PROF.	SC(17EC755)	7	35	35	261	89	00	00	94.62	94.51

Note: * 2017 Scheme (Parallel Batch)

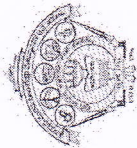
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PRINCIPAL

Hirasu Institute of Technology
NIDASOSHI-591236

Electronics & Commn. Engg. Dept.
HSIT NIDASOSHI

Nidasoshi-591 236, Taq: Hukkeri, Dist: Belagavi, Karnataka, India.
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ECE DEPT

Academic

Feed Back-I

10-Oct-2020

NAME	DESIGNATION	SUBJECT	SEM	CLASS STRENGTH	RESPONDENTED	A	B	C	D	PERCENTAGE	AVERAGE
Prof. S I SHIVAMOGGIMATH	ASST. PROF.	TC&NT(18MAT31)	3	29	29	173	106	10	01	91.10	91.10
Prof. P V PATIL	ASST. PROF.	NT(18EC32)	3	29	29	159	119	10	02	90.00	90.00
Prof. D M KUMBHAR	ASST. PROF.	ED(18EC33)	3	29	29	115	144	29	02	85.65	85.65
Prof. D B MADIHALLI	ASST. PROF.	DSD(18EC34)	3	29	29	169	108	12	01	90.68	89.75
Prof. S S KAMATE	ASST. PROF.	COA(18EC35)	3	29	29	148	135	06	01	89.65	90.43
Prof. M M GADAG	ASST. PROF.	PE&I(18EC36)	3	29	29	45	138	73	34	73.37	73.37
Prof. S S MALAJ	ASST. PROF.	M&ED(18ES51)	5	36	36	129	185	36	10	84.05	82.88
Prof. S S ITTANNAVAR	ASST. PROF.	DSP(18EC52)	5	36	36	268	85	07	00	94.50	94.50
Prof. S B AKKOLE	ASST. PROF.	PCS(18EC53)	5	36	36	265	85	09	01	94.11	95.05
Prof. D B MADIHALLI	ASST. PROF.	ITC(18EC54)	5	36	36	182	156	21	01	88.83	89.75
Prof. S S KAMATE	ASST. PROF.	EW(18EC55)	5	36	36	228	107	24	01	91.22	90.43
Prof. S S PATIL	ASST. PROF.	VHDL(18EC56)	5	36	36	256	97	05	02	93.72	93.11
Prof. M M GADAG	ASST. PROF.	M&A(17EC71)	7	35	35	00	00	00	00	00	00*
Prof. B P KHOT	ASST. PROF.	DIP(17EC72)	7	35	35	189	157	04	00	90.57	90.57
Prof. R M YADAHALLI	PROF.	PE(17EC73)	7	35	35	184	139	24	03	88.80	88.80
Prof. S S PATIL	ASST. PROF.	RTS(17EC743)	7	35	35	227	115	08	00	92.51	93.11
Prof. S B AKKOLE	ASST. PROF.	SC(17EC755)	7	35	35	280	70	00	00	96.00	95.05

Note: *: Classes are not conducted.

10/10/2020
HUD

Electronics & Commn. Engg. Dept
HSIT NIDASOSHI

PRINCIPAL

Hirasugar Institute of Technology
NIDASOSHI-591 236

Nidasoshi-591 236, Taq: Hukkeri, Dist: Belagavi, Karnataka, India.
Phone: +91-8333-278887, Fax: 278886, Web: www.hsit.ac.in, E-mail: principal@hsit.ac.in

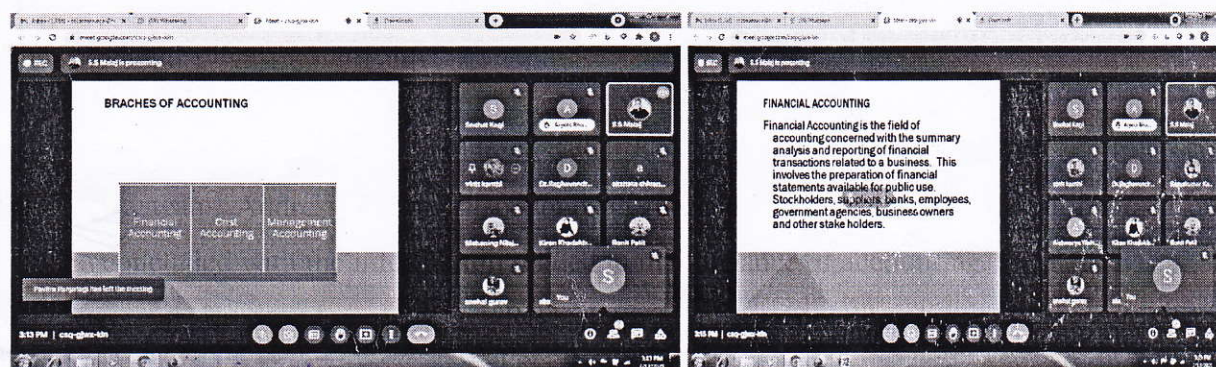


Date of Activity held and Time:	13/07/2021 3 pm to 4.15 pm
Name of Activity:	Webinar on "Project Management & Accounting"
Type of Activity: (cultural/curricular/co-curricular)	Co-curricular
Resource Person/Invitee:	Mrs. Rajeshwari.M. Adhalli
Professional Details of Resource Person:	Lecturer, Dept of Commerce, SJPN PU Independent College, Nidasoshi
Year / Class:	Open for all students and Faculties
No. of Participants:	52
Activity In charge:	Prof.S.S.Malaj

Description of Activity: Webinar on "Project Management & Accounting " was arranged on 13/07/2021. The webinar was arranged by Electronics and Communication Dept. Resource person for the webinar was Mrs. Rajeshwari. M. Adhalli , Dept of Commerce, SJPN PU Independent College, Nidasoshi.

The webinar of resource person addressed that how the management of project work has to be done in a team. Addressing was also done on the aspect of accounting .Finally the resource person concluded with the information related to maintenance of accounting.

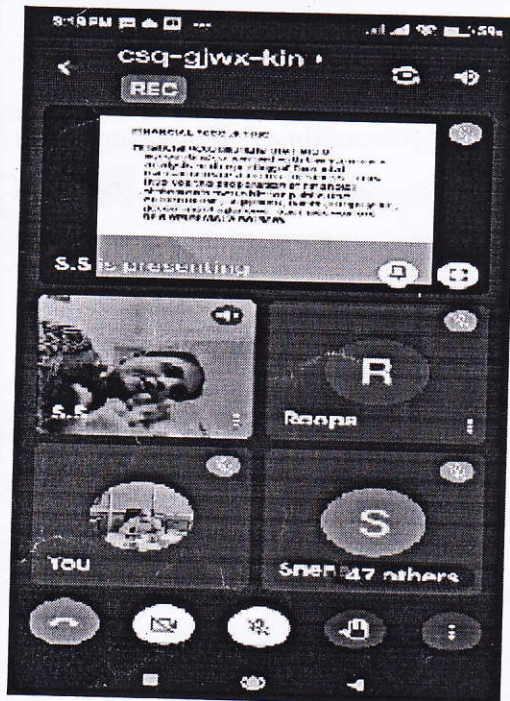
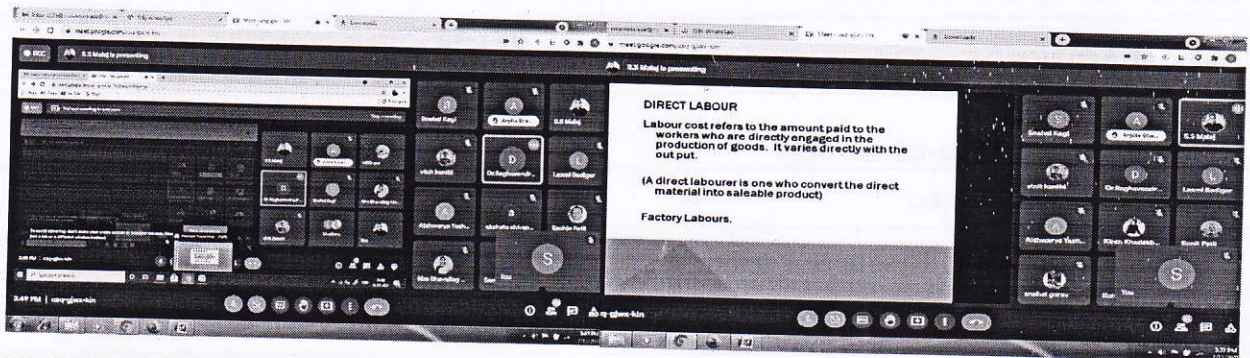
CO Defined	RBT Level	Relevance POs	Relevance PSO
To know about Project Management & Accounting	L3	PO11,PO12	--





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ECE Dept.
Webinar
Activity Report
2020-21(Even)



Mrs. Rajeshwari. M. Adhalli addressing the webinar.

Event Coordinator

HOD

HOD

Electronics & Commn. Engg. Dept.
HSIT NIDASOSHI

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		HOD Meeting
		WARC
		2020-21 Odd Semester

Ref. No.: HSIT/NDS/HOD-Meeting-17/2020-21

Date: 29.12.2020

NOTICE

A meeting of all HODs is convened on 29.12.2020 in the office of the Principal at 3.30PM. The agenda of the meeting are as below:

Agenda:

1. Review of portion coverage/attendance up to week no.17 for 3,5,7 & week No. 1 for I Sem.
2. To review the branch wise admission status for the AY 2020-21.
3. Submission of Project proposals to KSCST.
4. Any other matter with the permission of the chair.

(Dr. B.V. Madiggond)
Convener & HOD (EE)

S.N	Minutes of Meeting	Staff In charge	Due Date	Remarks
1.	The average syllabus coverage/attendance of III, V & VII Semester up to week no. 17 are 91/82%, 92/80% & 90/79% respectively and I Sem. 04/90%.	All HODs	26.12.2020	
2	The Branch wise admission status for the AY 2020-21 was discussed and is as below: First year CE-07, ME-07, EEE-17, ECE-46, CSE-53 Total: 130 & Lateral Entry -19.	All HODs	29.12.2020	
3.	Prof. M.I. Tanodi, Asst.Prof. Mechanical dept., College level coordinator for KSCST shall collect & consolidate the list of project proposals from all HODs and submit to KSCST.	MI Tanodi	04.01.2021	
4.	The meeting concluded with vote of thanks to chair			

Members Present:

Name	Design.	Sign.	Name	Dept.	Sign.
Dr. S.N.Topannavar	HOD-ME		Prof. S.V.Manjaragi	HOD-CSE	
Smt. S.S.Malaj	HOD-EC		Dr. K.M.Akkoli	I-Year Coord.	

(Dr. S.C.Kamate)
Principal

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			HOD Meeting
			WARC
			2020-21 Odd Semester

Ref. No.: HSIT/NDS/HOD-Meeting-16/2020-21

Date: 21.12.2020

NOTICE

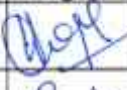
A meeting of all HODs is convened on 21.12.2020 in the office of the Principal at 4.00PM. The agenda of the meeting are as below:

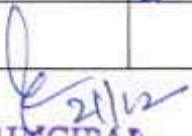
Agenda:

1. Review of portion coverage/attendance up to week no.16.
2. To review about completion I-year Induction Program.
3. Conducting of online III IA.
4. Informing students to attend offline classes.
5. Any other matter with the permission of the chair.

S.N	Minutes of Meeting	Staff In charge	Due Date	Remarks
1.	The average syllabus coverage/attendance of III, V & VII Semester up to week no. 16 are 86/83%, 86/80% & 85/81% respectively.	All HODs	19.12.2020	
2	One week Induction Program for I-Year students was completed successfully from 14 th to 19 th Dec 2020.	Dr.K.M.A.	19.12.2020	
3.	In view of Covid-19, it is decided to conduct online III IA for 3,5 & 7 Semester from 28 th to 30 th Dec 2020.	All HODs	28.12.2020	
4.	It is decided to inform I, III & V Semester students to attend offline classes from 1 st Jan 2021 and strictly adhering to SOP	All HODs	30.12.2020	
5.	The meeting concluded with vote of thanks to chair			

Members Present:

Name	Design.	Sign.	Name	Dept.	Sign.
Dr. S.N.Topannavar	HOD-ME		Prof. S.V.Manjaragi	HOD-CSE	
Prof.M.P.Yanagimath	I/c HOD-EE		Dr. K.M.Akkoli	I-Year Coord.	
Prof. S.B.Akkole	HOD-EC				


PRINCIPAL



Ref. No: HSIT/NDS/ECE/Meeting/2020-21/4

Date: 4/11/2020

DEPARTMENT MEETING

A meeting of all staff members of ECE Dept. is convened on 4/11/2020 in the office of the Head of Department of Electronics & Communication Engineering at 12.15 p.m. The agenda of the meeting is:

Agenda:

To discuss on:

1. Activities for academic year 2020-21
2. KSCST, VGST, AICTE Funding scheme
3. Additional classes for 7th semester
4. Faculty diary updation
5. FDP in VLSI domain
6. International conference
7. Project First phase demo
8. VLSI Lab. Modrobs
9. Technical quiz

B. P. Khot
 Prof. B. P. Khot
 Convener

Sl. No.	Minutes of Meeting	Staff In- charge	Due Date
1	It was discussed regarding Activities to be conduct for the academic year 2020-21 and also discussed regarding action plans for to conduct STTP and International conference	All faculty members	-
2	The faculty members were asked to prepare the proposal for KSCST, VGST, AICTE Funding scheme	All faculty members	31/11/2020
3	As per the guidance of Principal sir It was decided to put additional classes for 7 th semester students on every Saturday as portion was lagging behind and concerned faculty was asked to prepare time table	Prof. S. S. Patil	6/11/2020
4	The faculty members were asked to update the faculty diary of current semester and get signed by every Saturday	All faculty members	Every Saturday
5	It was discussed regarding organizing FDP in VLSI domain during the academic year 2020-21	Prof. S. S. Kamate Prof. S. S. Patil Prof. P. V. Patil	January 2021
6	It was discussed regarding organizing International conference during the academic year 2020-21	Dr. R. R. Maggavi Prof. S.S. Ittannavar & Prof. D. B. Madihalli	Year 2020-21
7	Conducting Project First phase demo in the second week of November	All faculty members	14/11/2020



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ECE Dept.

Dept Office

Meeting

2020-21(odd)

Sl. No.	Minutes of Meeting	Staff In- charge	Due Date
8	Head of the department asked to Prepare the proposals for VLSI Laboratory under AICTE Modrobs Scheme	Dr. R.R. Maggavi Prof. S. S. Patil Prof. S. S.Kamate	31/11/2020
9	It was discussed regarding organizing Technical quiz competition for students under ISTE & ECSA association.	Prof.D.M.Kumbhar & Prof. S.S. Ittannavar	November
The meeting concluded with vote of thanks to chair.			

Members Present:

RRM		MMG		SSK		SSM		DMK		SSP		DBM	
PVP		SSI		BPK									
PSD		VVG		MAA									

CC: The Principal, for kind information/Office of the principal

Prof. S. B. Khole
Head of ECE Department
HOD
Electronics & Commn. Engg. Dept.
HSIT NIDASOSHI



Training Activities details for AY: 2020-21

S.N.	Name of the Training	Date of Conduction	By whom	Details
1	FUEL(Friends Union for Engineering Lives, India	07/08/2020	Future Skills Program	Final Year students
2	A National Level Webinar on Job Opportunities for Mechanical & Civil Engineering Students after Covid-19 in FEM	31/08/2020	Mr. Vijayavittthal Illal Director, CADVISION R&D, Bengaluru	Final Year Students
3	Placement Assistance Training (18 Days)	29/09/2020 to 17/10/2020	Seventh Sense Talent Solution, Bengaluru	Final Year Students
4	Microsoft Ai, Machine Learning & Data Science in association with NASSCOM through VTU CPC	19/10/2020 to 24/10/2020	NASSCOM through VTU CPC	Finale Year Students
5	Global Talent Track	07/01/2021	Mr. Nawnit Kumar University Alliance Manager, Global Talent Track	Final Year (2021 Batch) Students
6	Fly Weight (Product Building with Python Course)	02/02/2021	Mr.Shrijith, Software Engineering Researcher at University of California, Irvine as a Masters Degree Pursuant	2022 & 2023 Batch Students
7	Codenest Dream Factory Project	05/04/2021 (45 Days)	Aptitude, Technical Programming, PD & Resume Building	Final year Students
8	Face Prep Lounchpad	15/06/2021	A Free Placement Preparation Course	37 Final year Students
9	Career Labs by "BYJUS"	30/04/2021	A Talk on "Placement Opportunities in Various Domens"	All Final year Students
10	Great Learning	25/06/2021	Job Eligibility Test	78 Final year Students
11	VTUSupports Destination Technologies	30/07/2021	VTU CPC Supports the 'Career and Placement Program' 120+ Hours of Free Employability Training with Placement Assistance In Association with Destination Technologies	2021 Batch & 2022 Batch All Branch Students


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