Visvesvaraya Technological University, Belagavi

B.E (CBCS) Open Electives Lists

Updated on 06-02-2018
(As per approval of Vice Chancellor's, dated:01.02.18)

Note to Students:

1) All B.E (CBCS) students (except B.Arch, B.Tech)should study one Open elective each in the 5th and 6th Semester as a part of their Programme.

2) Students should registers for the Open elective in the beginning of the 5th/6th semester in the department, where the elective is offered. An Open elective is not offered in a department if the registered student's strength is less than 10.

3) All Open electives are offered to students of all B.EProgrammes (branches) of engineering in general (except B.Arch). However, if a student of a particular Programme has already studied/going to study, in higher semester a similar Core course with majority of topics same as that of a particular Open elective, then that Open elective is not offered to that student. In which case, the student has to select an alternative Open elective.

4) Having studied/selected a particular Open Elective, a student is not eligible to take a Professional elective of his/her Programme in the Higher semesters/same semester which will have majority of topics same as that of the Open elective studied/selected. In which case, the student has to select an alternative Professional elective.

5) Students are advised to select an Open elective of their interest and if they have a pre-requisite knowledge to study that particular Open elective.

Note to Departments:

1) Above conditions are to be monitored by an Open elective coordinator of the department to which the student belongs to and the Course coordinator of the department where the student registers for the Open elective in the beginning of the 5th/6th semester.
2) The Teaching department(s) for Open Elective is not restricted to only those departments(s) indicated in the list. Any other department faculty who has requisite expertise to teach a particular Open elective can teach it.
3) Offering department indicated in the list of Open electives is the department/board which is responsible to set the Syllabus and Question paper for the particular Open elective.

Updated on 06-02-2018
(As per approval of Vice Chancellor's, dated:01.02.18)

B.E (CBCS) 5th Semester Open Electives List:

SL No	Course Code	Course Title	Teaching Department(s)	Offering Department(s)
1	15NC561	Essentials of NCC	This can be offered only in the Colleges having the NCC unit	Dept. offering the course
2	15PHY561	Laser Physics and Non-linear Optics	Physics	Basic Science (Physics)
3	15CV561	Traffic Engineering	CV	CV
4	15CV562	Sustainability Concepts In Engineering	CV	CV
5	15CV563	Remote Sensing and GIS	CV	CV
6	15CV564	Occupational Health and Safety	CV	CV
7	15ME561	Optimization Techniques	Any Branch	ME
8	15ME562	Energy and Environment	ME/Auto	ME
9	15ME563	Automation & Robotics	ME/EC/Auto	ME
10	15ME564	Project Management	ME/Auto	ME
11	15IM/IP561	Professional Communication &Report Writing	Any Branch	IP/IEM
12	15IM/IP562	Concurrent Engineering	Any Branch	IP/IEM
13	15IM/IP563	Technology Management	Any Branch	IP/IEM
14	15IM/IP564	Human Resource Management	Any Branch	IP/IEM
15	15MA561	Mechatronics	Manufacturing Sc. &Engg	Manufacturing Sc. &Engg
16	15MA562	Theory of Elasticity	Manufacturing Sc. &Engg	Manufacturing Sc. &Engg
17	15MA563	Knowledge Management	Manufacturing Sc. &Engg	Manufacturing Sc. &Engg
18	15EC561	Automotive Electronics	EC/TC/Mech	EC/TC
19	15EC562	Object Oriented Programming using C++	CS/IS/EC/TC/EE	EC/TC
20	15EC563	8051 Microcontrollers	EC/TC	EC/TC
21	15EE561	Electronic Communication Systems	EE/EC/TC	EE
22	15EE562	Programmable Logic Controllers	EE	EE
23	15EE563	Renewable Energy Sources	EE/ME	EE

24	15EE564	Business Communication	EE	EE
25	15CS561	Programming in JAVA	CS/IS	CS
26	15CS562	Artificial Intelligence	CS/IS/EC	CS
27	15CS563	Embedded Computing Systems	CS/IS/EE/EC	CS
28	15CS564	Dot Net Frame work for Application Development	CS/IS	CS
29	15CS565	Cloud Computing	Cloud Computing	CS
30	15EI/BM/ML561	Computer Organization	EI/BM/ML/CS/IS	EI/BM/ML
31	15EI562	Material Science	EI	EI
32	15BM/ML562	Virtual Bio-Instrumentation	BM/ML/EI	BM/ML/EI
33	15EI/BM563	Operating Systems	EI/BM/CS/IS/EC	EI/BM
34	15ML563	Medical Electronics Design	ML/BM	ML/BM
35	15EI564	Fundamentals of Nanotechnology	EI	EI
36	15BM564	Medical Physics	BM/ML	BM/ML
37	15ML564	Pharmacology and Drug Delivery	ML/BM	ML/BM
38	15BT561	Biology for Engineers	Bio-Tech	Bio-Tech
39	15BT562	Biomaterials	Bio-Tech	Bio-Tech
40	15BT563	BT for Sustainable Environment	Bio-Tech	Bio-Tech
41	15AE561	History of Flight & Technology Forecast	Aeronautical Engg.	Aeronautical Engg.
42	15AE562	Elements of Aeronautics	Aeronautical Engg.	Aeronautical Engg.
43	15AE563	Aircraft Transportation Systems	Aeronautical Engg.	Aeronautical Engg.
44	15AE564	Basics of Rockets & Design	Aeronautical Engg.	Aeronautical Engg.
45	15NT561	Introduction to Nano Science & Technology	Nanotechnology/ME	Nanotechnology
46	15NT562	Nanomaterials& their Applications	Nanotechnology/ME	Nanotechnology
47	15NT563	Nano Devices & Applications	Nanotechnology	Nanotechnology
48	15NT564	Nano Materials Synthesis & Characterization Techniques	Nanotechnology/ Chemistry/ME	Nanotechnology
49	15CH561	Industrial Waste Water Management	Chemical	Chemical
50	15CH562	Design of Air Pollution control Equipment	Chemical	Chemical
51	15CH563	Solid Waste Management	Chemical	Chemical
52	15CH564	Industrial Safety & Disaster Management	Chemical	Chemical
53	15PC561	Composite Materials	Petro-Chem	Petro-Chem
54	15PC562	Organic Chemistry	Petro-Chem	Petro-Chem

55	15PC563	Reservoir Rocks & Fluid Properties	Petro-Chem	Petro-Chem
56	15PC564	Natural Gas Processing	Petro-Chem	Petro-Chem
57	15AU561	Automobile Engineering	Automobile Engineering	Automobile Engineering
58	15AU562	Alternative Energy Sources for Automobiles	Automobile Engineering	Automobile Engineering
59	15AU563	Non Traditional Machining	Automobile Engineering	Automobile Engineering
60	15MN561	Industrial Safety Management	Mining Engineering	Mining Engineering
61	15MN562	Industrial Management and Entrepreneurship	Mining Engineering	Mining Engineering

B.E (CBCS) 6th Semester Open Electives List:

SL No	Course Code	Course Title	Teaching Department(s)	Offering Department(s)
1	15PHY661	Advanced Physics for Engineers	Physics	Basic Science (Physics)
2	15CV661	Water Resources Management		
3	15CV662	Environmental protection and management	CV	CV
4	15CV663	Numerical Methods and Applications	Any Branch/Maths	CV
5	15CV664	Finite element Method	CV	CV
6	15ME661	Energy Auditing	ME/Auto	ME
7	15ME662	Total Quality Management	ME/Auto/IEM	ME
8	15ME663	Maintenance Engineering	ME/Auto	ME
9	15ME664	Industrial Safety	ME/Auto	ME
10	15IM/IP661	Management Information Systems	IP/IEM	IP/IEM
11	15IM/IP662	Advance Machining Process	IP/IEM	IP/IEM
12	15IM/IP663	Value Engineering	IP/IEM	IP/IEM
13	15IM664	Development of Enterprises	IEM	IEM
14	15MA661	Microprocessor & Microcontrollers	Manufacturing Science &Engg	Manufacturing Science &Engg
15	15MA662	Theory of Plasticity	Manufacturing Science &Engg	Manufacturing Science &Engg
16	15MA663	Sensors	Manufacturing Science &Engg	Manufacturing Science &Engg
17	15MA664	Data Mining	Manufacturing	Manufacturing

			Science &Engg	Science &Engg
18	15EC661	Data Structures Using C++	CS/IS/EC/TC	EC/TC
19	15EC662	Power Electronics	EC/TC/EE	EC/TC
20	15EC663	Digital System Design using Verilog	EC/TC	EC/TC
21	15CS661	Mobile Application Development	Any Branch	CS
23	15CS662	Big Data Analytics	CS/IS	CS
24	15CS663	Wireless Networks and Mobile Computing	CS/IS	CS
25	15CS664	Python Application Programming	CS/IS	CS
26	15CS665	Service Oriented Architecture	CS/IS	CS
27	15CS666	Multi-Core Architecture and Programming	IS	CS/IS
28	15EE661	Artificial Neural Networks and Fuzzy Logic	EE/EC	EE
29	15EE662	Sensors and Transducers	EE/EC	EE
30	15EE663	Batteries and Fuel Cells for Commercial, Military and Space Applications	EE	EE
31	15EE664	Industrial Servo Control Systems	EE	EE
32	15EI/BM/ML 661	Mobile Communication	EI/BM/ML/EC/TC	EI/BM/ML
33	15EI662	MEMS and NEMS	EI/BM	EI/BM
34	15BM662	Software Engineering	BM/CS/IS	BM
35	15ML662	Embedded Real Time Systems	ML/BM	ML/BM
36	15EI/BM/ML 663	Embedded System Design and Programming	EI/BM/ML	EI/BM/ML
37	15EI/BM664	Statistics and Numerical Methods	EI/BM/Maths	EI/BM
38	15ML664	Biomaterials and Artificial Organs	ML/BM	ML/BM
39	15MAT661	Linear Algebra	Maths/EC	Basic Science (Maths)
40	15BT661	Biological Data Management	Bio-Tech	Bio-Tech
41	15BT662	Nano BT	Bio-Tech	Bio-Tech
42	15BT663	Good Manufacturing Process	Bio-Tech	Bio-Tech
43	15CH661	Food technology	Chemical	Chemical
44	15CH662	Sugar Technology	Chemical	Chemical
45	15CH663	Petro Chemical Engineering	Chemical	Chemical
46	15CH664	Polymer & Plastic Engineering	Chemical	Chemical
47	15PC661	Modern Separation Technology	Petro-Chem	Petro-Chem
48	15PC662	Process Modelling& Simulation	Petro-Chem	Petro-Chem
49	15PC663	Material Science for Petro-Chemical Engineering	Petro-Chem	Petro-Chem
50	15PC664	Catalysis Science & Technology	Petro-Chem	Petro-Chem

51	15AE661	Unmanned Aerial Vehicles Basics & Applications	Aeronautical Engg.	Aeronautical Engg.
52	15AE662	Fundamentals of Aerodynamic Theory	Aeronautical Engg.	Aeronautical Engg.
53	15AE663	Elements of Jet Propulsion Systems	Aeronautical Engg.	Aeronautical Engg.
54	15AE664	Maintenance, Overhaul & Repair of Air Craft Systems	Aeronautical Engg.	Aeronautical Engg.
55	15NT661	Nanotechnology in Electrical & electronics Engineering	Nanotechnology/EE/ EC	Nanotechnology
56	15NT662	Nanotechnology in Civil & Environmental Engineering	Nanotechnology /CV/EV	Nanotechnology
57	15NT663	Nanotechnology in Mechanical & Aerospace Engineering	Nanotechnology/ME /AE	Nanotechnology
58	15NT664	Nanotechnology in Bio-Medical Engineering	Nanotechnology/BM /BT	Nanotechnology
59	15AU661	Engineering Economics and Cost Estimation	Automobile Engineering	Automobile Engineering
60	15AU662	Hybrid and Electric Vehicle	Automobile Engineering	Automobile Engineering
61	15AU663	Non- destructive Testing	Automobile Engineering	Automobile Engineering
62	15MN661	Tunneling Engineering	Mining Engineering	Mining Engineering
63	15MN662	Underground Space Technology	Mining Engineering	Mining Engineering

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM
SCHEME OF TEACHING AND EXAMINATION

I SEMESTER B.E./B.TECH.

PHYSICS GROUP

Sl. No.	Subject Code	Subject	Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks		
						Th./Pr.	I.A.	Total
1	10 MAT-11	Engineering Maths-I	Maths	Basic Sc.	4 (T)	100	25	125
2	10 PHY-12	Engineering Physics	Physics	Basic Sc.	4 (T)	100	25	125
3	10 CIV-13	Elements of Civil Engg. & Engineering Mechanics	Civil Engg.	Civil Engg.	4 (T)	100	25	125
4	10 EME-14	Elements of Mechanical Engg.	Mech. Engg.	Mech. Engg.	4 (T)	100	25	125
5	10 ELE-15	Basic Electrical Engg.	E & E	E & E	4 (T)	100	25	125
6	10 WSL-16	Workshop Practice	Mech., Auto, IP, IEM, Mfg. Engg.	Mech. Engg.	3 (L)	50	25	75
7	10PHYL-17	Engg. Physics Lab	Physics	Basic Sc.	3 (L)	50	25	75
8	10 CIP-18	*Constitution of India & Professional Ethics	Any Department		2 (T)	50	25	75
9		Language (Kan.)	Humanities		2 (T)	---	---	---
				Total	**30**	****600**	****175**	**775**

I SEMESTER B.E./B.TECH.

CHEMISTRY GROUP

Sl. No.	Subject Code	Subject	Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks		
						Th./Pr.	I.A.	Total
1	10 MAT-11	Engineering Maths-I	Maths	Basic Sc.	4 (T)	100	25	125
2	10 CHE-12	Engineering Chemistry	Chemistry	Basic Sc.	4 (T)	100	25	125
3	10 CCP-13	Computer Concepts & C Programming	Any Engineering Department	CSE	4 (T)	100	25	125
4	10CED 14	Computer Aided Engineering Drawing	Mech./IP/Auto/ Mfg.Engg./ IEM	Mech. Engg.	6 (2T + 4L)	100	25	125
5	10 ELN-15	Basic Electronics	E & C / E & E / TC / IT	E & C	4 (T)	100	25	125
6	10 CPL-16	Computer Programming Lab	Any Engineering Department	CSE	3 (L)	50	25	75
7	10 CHEL-17	Engg. Chemistry Lab	Chemistry	Basic Sc.	3 (L)	50	25	75
8	10 CIV-18	*Environmental Studies	Civil / Environmental	Civil	2 (T)	50	25	75
9		Language (Eng.)	Humanities		2 (T)	---	---	---
				Total	**32**	****600**	****175**	**775**

II SEMESTER B.E./B.TECH.

PHYSICS GROUP

Sl. No.	Subject Code	Subject	Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks		
						Th./Pr.	I.A.	Total
1	10 MAT-21	Engineering Maths-II	Maths	Basic Sc.	4 (T)	100	25	125
2	10 PHY-22	Engineering Physics	Physics	Basic Sc.	4 (T)	100	25	125
3	10 CIV-23	Elements of Civil Engg. & Engineering Mechanics	Civil Engg.	Civil Engg.	4 (T)	100	25	125
4	10 EME-24	Elements of Mechanical Engg.	Mech. Engg.	Mech. Engg.	4 (T)	100	25	125
5	10 ELE-25	Basic Electrical Engg.	E & E	E & E	4 (T)	100	25	125
6	10 WSL-26	Workshop Practice	Mech./IP/Auto/ Mfg.Engg./ IEM	Mech. Engg.	3 (L)	50	25	75
7	10 PHYL-27	Engg. Physics Lab	Physics	Basic Sc.	3 (L)	50	25	75
8	10 CIP-28	*Constitution of India & Professional Ethics	Any Department	Civil	2 (T)	50	25	75
9		Language (Kan.)	Humanities		2 (T)	---	---	---
				Total	**30**	****600**	****175**	**775**

II SEMESTER B.E./B.TECH.

CHEMISTRY GROUP

Sl. No.	Subject Code	Subject	Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks		
						Th./Pr.	I.A.	Total
1	10 MAT-21	Engineering Maths-II	Maths	Basic Sc.	4 (T)	100	25	125
2	10 CHE-22	Engineering Chemistry	Chemistry	Basic Sc.	4 (T)	100	25	125
3	10 CCP-23	Computer Concepts & C Programming	Any Engineering Department	CSE	4 (T)	100	25	125
4	10 CED-24	Computer Aided Engineering Drawing	Mech./IP/Auto/ Mfg.Engg./ IEM	Mech. Engg.	6 (2T + 4L)	100	25	125
5	10 ELN-25	Basic Electronics	E & C/ E & E / TC / IT	E & C	4 (T)	100	25	125
6	10 CPL-26	Computer Programming Lab	Any Engineering Department	CSE	3 (L)	50	25	75
7	10 CHEL-27	Engg. Chemistry Lab	Chemistry	Basic Sc.	3 (3)	50	25	75
8	10 CIV-28	*Environmental Studies	Civil / Environmental	Civil	2 (T)	50	25	75
9		Language (Eng.)	Humanities		2 (T)	---	---	---
				Total	**32**	****600**	****175**	**775**

***CIP/Env.Engg. : Question Papers will be of Objective Type. Students have to pass the subject compulsorily, however marks will not be considered for awarding class / rank.**

****Excluding Environmental Studies/Constitution of India & Professional Ethics**

Language (Kan./Eng.) – Audit Course

SCHEME OF TEACHING AND EXAMINATION

B.E. COMPUTER SCIENCE AND ENGINEERING

(Common to CSE & ISE)

III SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10MAT31	Engineering Mathematics - III	Mathematics	04	-	03	25	100	125
2	10CS32	Electronic Circuits	CSE/ISE	04	-	03	25	100	125
3	10CS33	Logic Design	CSE/ISE	04	-	03	25	100	125
4	10CS34	Discrete Mathematical Structures	CSE/ISE	04	-	03	25	100	125
5	10CS35	Data Structures with C	CSE/ISE	04	-	03	25	100	125
6	10CS36	Object Oriented Programming with C++	CSE/ISE	04	-	03	25	100	125
7	10CSL37	Data Structures with C/C++ Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL38	Electronic Circuits & Logic Design Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

SCHEME OF TEACHING AND EXAMINATION

B.E. COMPUTER SCIENCE AND ENGINEERING

(Common to CSE & ISE)

IV SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10MAT41	Engineering Mathematics - IV	Maths	04	-	03	25	100	125
2	10CS42	Graph Theory and Combinatorics	CSE/ISE	04	-	03	25	100	125
3	10CS43	Design and Analysis of Algorithms	CSE/ISE	04	-	03	25	100	125
4	10CS44	Unix and Shell Programming	CSE/ISE	04	-	03	25	100	125
5	10CS45	Microprocessors	CSE/ISE	04	-	03	25	100	125
6	10CS46	Computer Organization	CSE/ISE	04	-	03	25	100	125
7	10CSL47	Design and Analysis of Algorithms Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL48	Microprocessors Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

SCHEME OF TEACHING AND EXAMINATION
B.E. COMPUTER SCIENCE AND ENGINEERING

V SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10IS51	Software Engineering	CSE/ISE	04	-	03	25	100	125
2	10CS52	Systems Software	CSE/ISE	04	-	03	25	100	125
3	10CS53	Operating Systems	CSE/ISE	04	-	03	25	100	125
4	10CS54	Database Management Systems	CSE/ISE	04	-	03	25	100	125
5	10CS55	Computer Networks - I	CSE/ISE	04	-	03	25	100	125
6	10CS56	Formal Languages and Automata Theory	CSE/ISE	04	-	03	25	100	125
7	10CSL57	Database Applications Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL58	Systems Software & Operating Systems Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

SCHEME OF TEACHING AND EXAMINATION
B.E. COMPUTER SCIENCE AND ENGINEERING

VI SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10AL61	Management and Entrepreneurship	CSE/ISE/ MBA	04	-	03	25	100	125
2	10CS62	Unix System Programming	CSE/ISE	04	-	03	25	100	125
3	10CS63/ 10IS662	Compiler Design	CSE/ISE	04	-	03	25	100	125
4	10CS64	Computer Networks - II	CSE/ISE	04	-	03	25	100	125
5	10CS65 / 10IS665	Computer Graphics and Visualization	CSE/ISE	04	-	03	25	100	125
6	10CS66x	Elective I (Group-A)	CSE/ISE	04	-	03	25	100	125
7	10CSL67	Computer Graphics and Visualization Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL68	Unix System Programming and Compiler Design Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

Elective I – Group A

10CS661/10IS661	Operations Research
10CS662	Signals and Systems
10CS663/10IS663	Data Compression
10CS664/10IS664	Pattern Recognition
10CS665	Stochastic Models and Applications
10CS666/10IS666	Programming Languages

SCHEME OF TEACHING AND EXAMINATION

B.E. COMPUTER SCIENCE AND ENGINEERING

VII SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10CS71	Object-Oriented Modeling and Design	CSE/ISE	04	-	03	25	100	125
2	10CS72/ 10IS752	Embedded Computing Systems	CSE/ISE	04	-	03	25	100	125
3	10CS73	Programming the Web	CSE/ISE	04	-	03	25	100	125
4	10CS74	Advanced Computer Architectures	CSE/ISE	04	-	03	25	100	125
5	10CS75x	Elective II (Group-B)	CSE/ISE	04	-	03	25	100	125
6	10CS76x	Elective III(Group-C)	CSE/ISE	04	-	03	25	100	125
7	10CSL77	Networks Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL78	Web Programming Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

	Elective II – Group B		Elective III – Group C
10CS751/10IS751	Advanced DBMS	10CS761/10IS761	C# Programming and .Net
10CS752	Digital Signal Processing	10CS762/10IS762	Digital Image Processing
10CS753/10IS753	Java and J2EE	10CS763/10IS763	Game Theory
10CS754/10IS754	Multimedia Computing	10CS764/10IS764	Artificial Intelligence
10CS755/10IS74	Data Warehousing and Data Mining	10CS765/10IS765	Storage Area Networks
10CS756/10IS756	Neural Networks	10CS766/10IS766	Fuzzy Logic

SCHEME OF TEACHING AND EXAMINATION

B.E. COMPUTER SCIENCE AND ENGINEERING

VIII SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration	Marks		
							IA	Exam	Total
1	10IS81	Software Architectures	CSE/ISE	04	-	03	25	100	125
2	10CS82	System Modeling and Simulation	CSE/ISE	04	-	03	25	100	125
3	10CS83x	Elective IV(Group-D)	CSE/ISE	04	-	03	25	100	125
4	10CS84x	Elective V(Group-E)	CSE/ISE	04	-	03	25	100	125
5	10CS85	Project Work	CSE		06	03	100	100	200
6	10CS86	Seminar	CSE	-	-	-	50	-	50
Total				16	06		250	500	750

Elective IV – Group D		**Elective V– Group E**	
10CS831/10IS831	Wireless Networks and Mobile Computing	10CS841/10IS841	Ad-hoc Networks
10CS832/10IS832	Web 2.0 and Rich Internet Applications	10CS842	Software Testing
10CS833	VLSI Design and Algorithms	10CS843	ARM Based System Design
10CS834/10IS834	Network Management Systems	10CS844/10IS844	Services Oriented Architecture
10CS835/10IS835	Information and Network Security	10CS845/10IS845	Clouds, Grids and Clusters
10CS836/10IS836	Microcontroller-Based Systems	10CS846	Multi-core Architecture and Programming

NOTE: Students have to register for one Elective from each of the five Elective Group.

SCHEME OF TEACHING & EXAMINATION
ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER (COMMON TO EC/TC/ML)

Subject Code	Title	Teaching Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
10MAT - 31	Engg. Mathematics - III	Mat	04		03	25	100	125
10ES – 32	Analog Electronic Ckts	@	04		03	25	100	125
10ES – 33	Logic Design	@	04		03	25	100	125
10ES – 34	Network Analysis	@	04		03	25	100	125
10IT– 35	Electronic Instrumentation	@	04		03	25	100	125
10ES – 36	Field Theory	@	04		03	25	100	125
10ESL – 37	Analog Electronics Lab	@		03	03	25	50	75
10ESL – 38	Logic Design Lab	@		03	03	25	50	75
		Total	24	06	24	200	700	900

SCHEME OF TEACHING & EXAMINATION
ELECTRONICS & COMMUNICATION ENGINEERING
IV SEMESTER (COMMON TO EC/TC/ML)

Subject Code	Title	Teach Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
10MAT - 41	Engg. Mathematics – IV	Mat	04		03	25	100	125
10ES– 42	Microcontrollers	@	04		03	25	100	125
10ES – 43	Control Systems	@	04		03	25	100	125
10EC – 44	Signals & Systems	@	04		03	25	100	125
10EC– 45	Fundamentals of HDL	@	04		03	25	100	125
10EC – 46	Linear ICs & Applications	@	04		03	25	100	125
10ESL – 47	Microcontrollers Lab	@		03	03	25	50	75
10ECL – 48	HDL Lab	@		03	03	25	50	75
		Total	24	06	24	200	700	900

Note : @ indicates concerned discipline. **ES (for theory) & ECL (for Lab)** in the subject code indicates that the subject is common to electrical and electronics stream consisting of **EE/EC/IT/TC/ML/BM branches** of engineering.

SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

V SEMESTER **COMMON TO EC/TE**

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
01	10AL51	Management and Entrepreneurship	AL	4	-	3	25	100	125
02	10EC52	Digital Signal Processing	EC	4	-	3	25	100	125
03	10EC53	Analog Communication	EC	4	-	3	25	100	125
04	10EC54	Microwaves and Radar	EC	4	-	3	25	100	125
05	10EC55	Information Theory & Coding	EC	4	-	3	25	100	125
06	10EC56	Fundamentals of CMOS VLSI	EC	4	-	3	25	100	125
07	10ECL57	DSP Lab	EC	-	3	3	25	50	75
08	10ECL58	Analog Communication Lab + LIC Lab	EC	-	3	3	25	50	75
TOTAL				**24**	**06**	**24**	**200**	**700**	**900**

SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

VI SEMESTER

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EC61	Digital Communication	EC	4	-	3	25	100	125
2	10EC62	Microprocessors	EC	4	-	3	25	100	125
3	10EC63	Microelectronics Circuits	EC	4	-	3	25	100	125
4	10EC64	Antennas and Propagation	EC	4	-	3	25	100	125
5	10EC65	Operating Systems	EC	4		3	25	100	125
6	10EC66x	**Elective-I (Group A)**	EC	4	-	3	25	100	125
7	10ECL67	Advanced Communication Lab	EC	-	3	3	25	50	75
8	10ECL68	Microprocessor Lab	EC	-	3	3	25	50	75
TOTAL				**24**	**06**	**24**	**200**	**700**	**900**

Elective-I (Group A)

10EC661 – Analog and Mixed Mode VLSI Design
10EC662 – Satellite Communications
10EC663 - Random Process

10EC664 – Low Power VLSI Design
10EC665 – Data Structure Using C++
10EC666 – Digital System Design Using Verilog
10EC667- Virtual Instrumentation

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SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

VII SEMESTER

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EC71	Computer Communication Networks	EC	4	-	3	25	100	125
2	10EC72	Optical Fiber Communication	EC	4	-	3	25	100	125
3	10EC73	Power Electronics	EC	4	-	3	25	100	125
4	10EC74	Embedded System Design	EC	4	-	3	25	100	125
5	10EC75x	**Elective-II (Group B)**	EC	4	-	3	25	100	125
6	10EC76x	**Elective-III (Group C)**	EC	4	-	3	25	100	125
7	10ECL77	VLSI Lab	EC	-	3	3	25	50	75
8	10ECL78	Power Electronics Lab	EC	-	3	3	25	50	75
		TOTAL		**24**	**06**	**24**	**200**	**700**	**900**

Elective-II (Group B)

Elective-III (Group C)

10EC751 – DSP Algorithms & Architecture
10EC752 - Micro and Smart Systems Technology
10EC753 – Artificial Neural Network
10EC754 – CAD for VLSI
10EC755 – Applied Embedded System Design*
10EC756 – Speech Processing

10EC761 - Programming in C++
10EC762 – Real Time Systems
10EC763 - Image Processing
10EC764 - Radio Frequency Integrated Circuits
10EC765 - Wavelet Transforms
10EC766 - Modeling and Simulation of Data Networks

NOTE: 10EC755 Applied Embedded System Design has a LAB component (syllabus is different and in the Theory Examination, questions from Lab experiments will also be there.)

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SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

VIII SEMESTER

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EC81	Wireless Communication	EC	4	-	3	25	100	125
2	10EC82	Digital Switching Systems	EC	4	-	3	25	100	125
3	10EC83x	Elective-IV (Group D)	EC	4	-	3	25	100	125
4	10EC84x	Elective-V (Group E)	EC	4	-	3	25	100	125
5	10ECP85	Project Work	EC	-	6	3	100	100	200
6	10ECS86	Seminar	EC	-	3	-	50	-	50
		TOTAL		**16**	**09**	**15**	**250**	**500**	**750**

Elective-IV (Group-D)
10EC831 – Distributed Systems
10EC832 – Network Security

Elective-V (Group-E)
10EE841 – Multimedia Communication
10EC842 – Real Time Operating Systems

10EC833 - Optical Networks
10EC834 – High Performance Computing Networks
10EC835 – Internet Engineering

10EC843 - GSM
10EC844 - Ad-hoc Wireless Networks
10EC845 – Optical Computing

SCHEME OF TEACHING & EXAMINATION

III SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Subject Code	Title	Teaching Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration in hours	Marks		
						I. A	Theory/ Practical	Total Marks
10MAT - 31	Engineering Mathematics - III	Mat	04		03	25	100	125
10ES – 32	Analog Electronic Circuits	@	04		03	25	100	125
10ES – 33	Logic Design	@	04		03	25	100	125
10ES – 34	Network Analysis	@	04		03	25	100	125
10EE– 35	Electrical and Electronic Measurements And Instrumentation	E&EE	04		03	25	100	125
10EE – 36	Electric Power Generation	E&EE	04		03	25	100	125
10ESL – 37	Analog Electronics Lab	@		03	03	25	50	75
10ESL – 38	Logic Design Lab	@		03	03	25	50	75
		Total	24	06	24	200	700	900

Note : **@** indicates concerned discipline. ES (for theory) & ESL (for Lab) in the subject code indicates that the subject is common to electrical and electronics stream consisting of EE/EC/IT/TC/ML/BM branches of engineering. EE indicates, subjects specific to E & EE branch only.

IV SEMESTER
ELECTRICAL AND ELECTRONICS ENGINEERING

Subject Code	Title	Teaching Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration in hours	Marks		
						I. A	Theory/ Practical	Total Marks
10MAT - 41	Engineering Mathematics - IV	Mat	04		03	25	100	125
10ES– 42	Microcontrollers	@	04		03	25	100	125
10ES – 43	Control Systems	@	04		03	25	100	125
10EE – 44	Field Theory	E&EE	04		03	25	100	125
10EE– 45	Power Electronics	E&EE	04		03	25	100	125
10EE – 46	Transformers and Induction Machines	E&EE	04		03	25	100	125
10ESL – 47	Microcontrollers Lab	@		03	03	25	50	75
10EEL – 48	Power Electronics Lab	E&EE		03	03	25	50	75
		Total	24	06	24	200	700	900

Note : **@** indicates concerned discipline.ES (for theory) & ESL (for Lab) in the subject code indicates that the subject is common to electrical and electronics stream consisting of EE/EC/IT/TC/ML/BM branches of engineering. EE indicates, subjects specific to E & EE branch only.

V SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
01	10AL51	Management and Entrepreneurship	@	4	-	3	25	100	125
02	10EE52	Signals and Systems	E&EE	4	-	3	25	100	125
03	10EE53	Transmission and Distribution	E&EE	4	-	3	25	100	125
04	10EE54	D.C. Machines and Synchronous Machines	E&EE	4	-	3	25	100	125
05	10EE55	Modern Control theory	E&EE	4	-	3	25	100	125
06	10EE56	Linear IC's and Applications	E&EE	4	-	3	25	100	125
07	10EEL57	Measurements and Circuit Simulation Laboratory	E&EE	-	3	3	25	50	75
08	10EEL58	Transformers and Induction Machines Laboratory	E&EE	-	3	3	25	50	75
Total				24	06	24	200	700	900

@ Any Engineering department or department of Business study.

VI SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EE61	Power System Analysis and Stability	E&EE	4	-	3	25	100	125
2	10EE62	Switchgear & Protection	E&EE	4	-	3	25	100	125
3	10EE63	Electrical Machine Design	E&EE	4	-	3	25	100	125
4	10EE64	Digital Signal Processing	E&EE	4	-	3	25	100	125
5	10EE65	E- CADD	E&EE	1	3	3	25	100	125
6	10EE66X	Elective-I (Group A)	E&EE	4	-	3	25	100	125
7	10EEL67	D.C. Machines and Synchronous Machines Laboratory	E&EE	-	3	3	25	50	75
8	10EEL68	Control Systems Laboratory	E&EE	-	3	3	25	50	75
Total				21	09	24	200	700	900

Elective-I (Group A)

10EE661-Opration Research

10EE662 - Advanced Power Electronics

10EE663 – Fuzzy Logic

10EE664 - Object Oriented Programming using C++

10EE665 - Embedded Systems

10EE666 – Electrical Engineering Materials

VII SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EE71	Computer Techniques in Power System Analysis	E&EE	4	-	3	25	100	125
2	10EE72	Electrical Power Utilization	E&EE	4	-	3	25	100	125
3	10EE73	High Voltage Engineering	E&EE	4	-	3	25	100	125
4	10EE74	Industrial Drives and Applications	E&EE	4	-	3	25	100	125
5	10EE75X	Elective-II (Group B)	E&EE	4	-	3	25	100	125
6	10EE76X	Elective-III (Group C)	E&EE	4	-	3	25	100	125
7	10EEL77	Relay and High Voltage Laboratory	E&EE	-	3	3	25	50	75
8	10EEL78	Power System Simulation Laboratory	E&EE	-	3	3	25	50	75
Total				24	06	24	200	700	900

Elective-II (Group B)
10EE751 - HVDC Transmission
10EE752 - Programmable Logic Controllers
10EE753 - Artificial Neural Network
10EE754 - Operating System
10EE755 - Digital System with VHDL
10EE756 - Testing and Commissioning of Electrical Equipment

Elective-III (Group C)
10EE761 - Power System Planning
10EE762 - Computer Control of Electrical Drives
10EE763 - Data Structure
10EE764 - VLSI Circuits and Design
10EE765 - Micro & Smart System Technology
10EE766 - Electromagnetic Compatibility

VIII SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EE81	Electrical Design, Estimation and Costing	E&EE	4	-	3	25	100	125
2	10EE82	Power System Operation and Control	E&EE	4	-	3	25	100	125
3	10EE83X	Elective-IV (Group D)	E&EE	4	-	3	25	100	125
4	10EE84X	Elective-V (Group E)	E&EE	4	-	3	25	100	125
5	10EEP85	Project Work	E&EE	-	6	3	100	100	200
6	10EES86	Seminar (on a latest topic relevant to the branch and independent of the project work)	E&EE	-	3	-	50	-	50
Total				16	09	15	250	500	750

Elective-IV (Group-D)
10EE831 - Reactive Power Management
10EE832 - Flexible A.C. Transmission Systems (FACTS)
10EE833- Advanced Instrumentation System
10EE834 - AI Applications to Power Systems
10EE835 - Data Base Management Systems (DBMS)
10EE836 - Renewable Energy Sources

Elective-V (Group-E)
10EE841 - Power Systems Dynamics and Stability
10EE842 - Energy Auditing & Demand Side Management
10EE843 - Data communications and Networking
10EE844 - Electrical Distribution Systems
10EE845 - Insulation Engineering
10EE846 - Intellectual Property Rights
10EE847 - Electrical Power Quality

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

III SEMESTER

Sl. No	Sub-Code	Title	Teaching Dept.	Teaching hours /week		Examination			
				Theory	Pract. /Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10MAT31	Engineering Mathematics - III	Mathematics	04	-	03	25	100	125
2	10ME32A/ 10ME32B	Material Sc. & Metallurgy / Mechanical Measurements & Metrology	Mechanical	04	--	03	25	100	125
3	10ME33	Basic Thermodynamics	Mechanical	04	--	03	25	100	125
4	10ME34	Mechanics of Materials	Mechanical	04	--	03	25	100	125
5	10ME35	Manufacturing Process I (Fundamentals of Foundry and Welding)	Mechanical	04	--	03	25	100	125
6	10ME36A/ 10ME36B	Computer Aided Machine Drawing / Fluid Mechanics	Mechanical	01 04	03 --	03	25	100	125
7	10MEL37A/ 10MEL37B	Metallography & Material Testing Lab / Mech. Measurements & Metrology Lab	Mechanical	--	03	03	25	50	75
8	10MEL38A/ 10MEL38B	Foundry & Forging lab / Machine Shop	Mechanical	--	03	03	25	50	75
TOTAL				**21/24**	**09**	**24**	**200**	**700**	**900**

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

IV SEMESTER

Sl. No	Sub-Code	Title	Teaching Dept.	Teaching hours /week		Examination			
				Theory	Pract/ Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10MAT41	Engineering Mathematics - IV	Mathematics	04	-	03	25	100	125
2	10ME42A/ 10ME42B	Material Sc. & Metallurgy / Mechanical Measurements & Metrology	Mechanical	04	--	03	25	100	125
3	10ME43	Applied Thermodynamics	Mechanical	04	--	03	25	100	125
4	10ME44	Kinematics of Machines	Mechanical	04	--	03	25	100	125
5	10ME45	Manufacturing Process II	Mechanical	04	--	03	25	100	125
6	10ME46A/1 0ME46B	Computer Aided Machine Drawing / Fluid Mechanics	Mechanical	01 04	03 --	03	25	100	125
7	10MEL47A/ 10MEL47B	Metallography & Material Testing Lab / Mech. Measurements & Metrology Lab	Mechanical	--	03	03	25	50	75
8	10MEL48A/ 10MEL48B	Foundry & Forging lab / Machine Shop	Mechanical	--	03	03	25	50	75
TOTAL				**21/24**	**09**	**24**	**200**	**700**	**900**

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

V SEMESTER

Sl. No	Sub-Code	Title	Teaching hours /week		Examination			
			Theory	Pract. / Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10AL51	Management and Entrepreneurship	04	--	03	25	100	125
2	10ME52	Design of Machine Elements I	04	--	03	25	100	125
3	10ME53	Energy Engineering	04	--	03	25	100	125
4	10ME54	Dynamics of Machines	04	--	03	25	100	125
5	10ME55	Manufacturing Process III	04	--	03	25	100	125
6	10ME56	Turbo Machines	04	--	03	25	100	125
7	10MEL57	Fluid Mechanics & Machines Lab	--	03	03	25	50	75
8	10MEL58	Energy Conversion Engg. Lab		03	03	25	50	75
		TOTAL	**24**	**06**	**24**	**200**	**700**	**900**

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

VI SEMESTER

Sl. No	Sub-Code	Title	Teaching hours /week		Examination			
			Theory	Pract. / Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10ME61	Computer Integrated Manufacturing	04	--	03	25	100	125
2	10ME62	Design of Machine Elements II	04	--	03	25	100	125
3	10ME63	Heat & Mass Transfer	04	--	03	25	100	125
4	10ME64	Finite Element Methods	04	--	03	25	100	125
5	10ME65	Mechatronics & Microprocessor	04	--	03	25	100	125
6	10ME66X	Elective ‘A’	04	--	03	25	100	125
7	10MEL67	Heat & Mass Transfer Lab	--	03	03	25	50	75
8	10MEL68	CAMA Lab	--	03	03	25	50	75
		TOTAL	**24**	**06**	**24**	**200**	**700**	**900**

Elective – 1 (Group A)			
10ME661	Theory of Elasticity	10ME662	Mechanics of Composite Materials
10ME663	Refrigeration & Air Conditioning	10ME664	Design of Heat Exchangers
10ME665	Non-Traditional Machining	10ME666	Knowledge Management
10ME667	Project Management	10ME668	Statistical Quality Control

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

VII SEMESTER

Sl. No	Sub-Code	Title	Teaching hours /week		Examination			
			Theory	Pract/ Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10ME71	Economics	04	--	03	25	100	125
2	10ME72	Mechanical Vibrations	04	--	03	25	100	125
3	10ME73	Hydraulics and Pneumatics	04	--	03	25	100	125
4	10ME74	Operations Research	04	--	03	25	100	125
5	10ME75X	Elective B	04	--	03	25	100	125
6	10ME76X	Elective C	04	--	03	25	100	125
7	10MEL77	Design Lab	--	03	03	25	50	75
8	10MEL78	CIM and Automation Lab	--	03	03	25	50	75
		TOTAL	**24**	**06**	**24**	**200**	**700**	**900**

Elective – 2 (Group B)		Elective – 3 (Group C)	
10ME751	Mechanism Design	10ME761	Experimental Stress Analysis
10ME752	Theory of Plasticity	10ME762	Tool Design
10ME753	Engineering Design	10ME763	Cryogenics
10ME754	Non Conventional Energy Sources	10ME764	Smart Materials
10ME755	Gas Dynamics	10ME765	Agile Manufacturing
10ME756	Management Information System	10ME766	Robotics
10ME757	Automation in Manufacturing	10ME767	Finance Management
10ME758	Total Quality Management	10ME768	Micro & Smart System Technology
		10ME769	Product Life Cycle Management

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

VIII SEMESTER

Sl. No	Sub-Code	Title	Teaching hours /week		Examination			
			Theory	Pract./ Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10ME81	Operations Management	04	--	03	25	100	125
2	10ME82	Control Engineering	04	--	03	25	100	125
3	10ME83X	Elective D	04	--	03	25	100	125
4	10ME84X	Elective E	04	--	03	25	100	125
5	10ME85L	Project Work	--	06	03	100	100	200
6	10ME86L	Seminar	--	03	--	50	--	50
TOTAL				**09**	**15**	**250**	**500**	**750**

Elective – 4 (Group D)		**Elective – 5 (Group E)**	
10ME831	Tribology	10ME841	Machine Tool Design
10ME832	Fracture Mechanics	10ME842	Industrial Engineering & Ergonomics
10ME833	Power Plant Engineering	10ME843	Bio Mass Energy Systems
10ME834	Nanotechnology	10ME844	Automotive Engineering
10ME835	Organisational Behaviour and Professional Communication	10ME845	Database Management System
10ME836	Computer Graphics	10ME846	Artificial Intelligence
10ME837	Rapid Prototyping	10ME847	Design of Experiments
10ME838	Foundry Technology	10ME848	Design for Manufacture & Assembly

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM
SCHEME OF TEACHING AND EXAMINATION

I SEMESTER B.E./B.TECH.

PHYSICS GROUP

Sl. No.	Subject Code	Subject	Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks		
						Th./Pr.	I.A.	Total
1	10 MAT-11	Engineering Maths-I	Maths	Basic Sc.	4 (T)	100	25	125
2	10 PHY-12	Engineering Physics	Physics	Basic Sc.	4 (T)	100	25	125
3	10 CIV-13	Elements of Civil Engg. & Engineering Mechanics	Civil Engg.	Civil Engg.	4 (T)	100	25	125
4	10 EME-14	Elements of Mechanical Engg.	Mech. Engg.	Mech. Engg.	4 (T)	100	25	125
5	10 ELE-15	Basic Electrical Engg.	E & E	E & E	4 (T)	100	25	125
6	10 WSL-16	Workshop Practice	Mech., Auto, IP, IEM, Mfg. Engg.	Mech. Engg.	3 (L)	50	25	75
7	10PHYL-17	Engg. Physics Lab	Physics	Basic Sc.	3 (L)	50	25	75
8	10 CIP-18	*Constitution of India & Professional Ethics	Any Department		2 (T)	50	25	75
9		Language (Kan.)	Humanities		2 (T)	---	---	---
				Total	**30**	****600**	****175**	**775**

I SEMESTER B.E./B.TECH.

CHEMISTRY GROUP

Sl. No.	Subject Code	Subject	Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks		
						Th./Pr.	I.A.	Total
1	10 MAT-11	Engineering Maths-I	Maths	Basic Sc.	4 (T)	100	25	125
2	10 CHE-12	Engineering Chemistry	Chemistry	Basic Sc.	4 (T)	100	25	125
3	10 CCP-13	Computer Concepts & C Programming	Any Engineering Department	CSE	4 (T)	100	25	125
4	10CED 14	Computer Aided Engineering Drawing	Mech./IP/Auto/ Mfg.Engg./ IEM	Mech. Engg.	6 (2T + 4L)	100	25	125
5	10 ELN-15	Basic Electronics	E & C / E & E / TC / IT	E & C	4 (T)	100	25	125
6	10 CPL-16	Computer Programming Lab	Any Engineering Department	CSE	3 (L)	50	25	75
7	10 CHEL-17	Engg. Chemistry Lab	Chemistry	Basic Sc.	3 (L)	50	25	75
8	10 CIV-18	*Environmental Studies	Civil / Environmental	Civil	2 (T)	50	25	75
9		Language (Eng.)	Humanities		2 (T)	---	---	---
				Total	**32**	****600**	****175**	**775**

II SEMESTER B.E./B.TECH.

PHYSICS GROUP

Sl. No.	Subject Code	Subject	Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks		
						Th./Pr.	I.A.	Total
1	10 MAT-21	Engineering Maths-II	Maths	Basic Sc.	4 (T)	100	25	125
2	10 PHY-22	Engineering Physics	Physics	Basic Sc.	4 (T)	100	25	125
3	10 CIV-23	Elements of Civil Engg. & Engineering Mechanics	Civil Engg.	Civil Engg.	4 (T)	100	25	125
4	10 EME-24	Elements of Mechanical Engg.	Mech. Engg.	Mech. Engg.	4 (T)	100	25	125
5	10 ELE-25	Basic Electrical Engg.	E & E	E & E	4 (T)	100	25	125
6	10 WSL-26	Workshop Practice	Mech./IP/Auto/ Mfg.Engg./ IEM	Mech. Engg.	3 (L)	50	25	75
7	10 PHYL-27	Engg. Physics Lab	Physics	Basic Sc.	3 (L)	50	25	75
8	10 CIP-28	*Constitution of India & Professional Ethics	Any Department	Civil	2 (T)	50	25	75
9		Language (Kan.)	Humanities		2 (T)	---	---	---
				Total	**30**	****600**	****175**	**775**

II SEMESTER B.E./B.TECH.

CHEMISTRY GROUP

Sl. No.	Subject Code	Subject	Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks		
						Th./Pr.	I.A.	Total
1	10 MAT-21	Engineering Maths-II	Maths	Basic Sc.	4 (T)	100	25	125
2	10 CHE-22	Engineering Chemistry	Chemistry	Basic Sc.	4 (T)	100	25	125
3	10 CCP-23	Computer Concepts & C Programming	Any Engineering Department	CSE	4 (T)	100	25	125
4	10 CED-24	Computer Aided Engineering Drawing	Mech./IP/Auto/ Mfg.Engg./ IEM	Mech. Engg.	6 (2T + 4L)	100	25	125
5	10 ELN-25	Basic Electronics	E & C/ E & E / TC / IT	E & C	4 (T)	100	25	125
6	10 CPL-26	Computer Programming Lab	Any Engineering Department	CSE	3 (L)	50	25	75
7	10 CHEL-27	Engg. Chemistry Lab	Chemistry	Basic Sc.	3 (3)	50	25	75
8	10 CIV-28	*Environmental Studies	Civil / Environmental	Civil	2 (T)	50	25	75
9		Language (Eng.)	Humanities		2 (T)	---	---	---
				Total	**32**	****600**	****175**	**775**

***CIP/Env.Engg. : Question Papers will be of Objective Type. Students have to pass the subject compulsorily, however marks will not be considered for awarding class / rank.**

****Excluding Environmental Studies/Constitution of India & Professional Ethics**

Language (Kan./Eng.) – Audit Course

SCHEME OF TEACHING AND EXAMINATION

B.E. COMPUTER SCIENCE AND ENGINEERING

(Common to CSE & ISE)

III SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10MAT31	Engineering Mathematics - III	Mathematics	04	-	03	25	100	125
2	10CS32	Electronic Circuits	CSE/ISE	04	-	03	25	100	125
3	10CS33	Logic Design	CSE/ISE	04	-	03	25	100	125
4	10CS34	Discrete Mathematical Structures	CSE/ISE	04	-	03	25	100	125
5	10CS35	Data Structures with C	CSE/ISE	04	-	03	25	100	125
6	10CS36	Object Oriented Programming with C++	CSE/ISE	04	-	03	25	100	125
7	10CSL37	Data Structures with C/C++ Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL38	Electronic Circuits & Logic Design Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

SCHEME OF TEACHING AND EXAMINATION
B.E. COMPUTER SCIENCE AND ENGINEERING
(Common to CSE & ISE)

IV SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10MAT41	Engineering Mathematics - IV	Maths	04	-	03	25	100	125
2	10CS42	Graph Theory and Combinatorics	CSE/ISE	04	-	03	25	100	125
3	10CS43	Design and Analysis of Algorithms	CSE/ISE	04	-	03	25	100	125
4	10CS44	Unix and Shell Programming	CSE/ISE	04	-	03	25	100	125
5	10CS45	Microprocessors	CSE/ISE	04	-	03	25	100	125
6	10CS46	Computer Organization	CSE/ISE	04	-	03	25	100	125
7	10CSL47	Design and Analysis of Algorithms Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL48	Microprocessors Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

SCHEME OF TEACHING AND EXAMINATION

B.E. COMPUTER SCIENCE AND ENGINEERING

V SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10IS51	Software Engineering	CSE/ISE	04	-	03	25	100	125
2	10CS52	Systems Software	CSE/ISE	04	-	03	25	100	125
3	10CS53	Operating Systems	CSE/ISE	04	-	03	25	100	125
4	10CS54	Database Management Systems	CSE/ISE	04	-	03	25	100	125
5	10CS55	Computer Networks - I	CSE/ISE	04	-	03	25	100	125
6	10CS56	Formal Languages and Automata Theory	CSE/ISE	04	-	03	25	100	125
7	10CSL57	Database Applications Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL58	Systems Software & Operating Systems Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

SCHEME OF TEACHING AND EXAMINATION

B.E. COMPUTER SCIENCE AND ENGINEERING

VI SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10AL61	Management and Entrepreneurship	CSE/ISE/ MBA	04	-	03	25	100	125
2	10CS62	Unix System Programming	CSE/ISE	04	-	03	25	100	125
3	10CS63/ 10IS662	Compiler Design	CSE/ISE	04	-	03	25	100	125
4	10CS64	Computer Networks - II	CSE/ISE	04	-	03	25	100	125
5	10CS65 / 10IS665	Computer Graphics and Visualization	CSE/ISE	04	-	03	25	100	125
6	10CS66x	Elective I (Group-A)	CSE/ISE	04	-	03	25	100	125
7	10CSL67	Computer Graphics and Visualization Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL68	Unix System Programming and Compiler Design Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

Elective I – Group A

10CS661/10IS661	Operations Research
10CS662	Signals and Systems
10CS663/10IS663	Data Compression
10CS664/10IS664	Pattern Recognition
10CS665	Stochastic Models and Applications
10CS666/10IS666	Programming Languages

SCHEME OF TEACHING AND EXAMINATION

B.E. COMPUTER SCIENCE AND ENGINEERING

VII SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Exam	Total
1	10CS71	Object-Oriented Modeling and Design	CSE/ISE	04	-	03	25	100	125
2	10CS72/ 10IS752	Embedded Computing Systems	CSE/ISE	04	-	03	25	100	125
3	10CS73	Programming the Web	CSE/ISE	04	-	03	25	100	125
4	10CS74	Advanced Computer Architectures	CSE/ISE	04	-	03	25	100	125
5	10CS75x	Elective II (Group-B)	CSE/ISE	04	-	03	25	100	125
6	10CS76x	Elective III(Group-C)	CSE/ISE	04	-	03	25	100	125
7	10CSL77	Networks Laboratory	CSE/ISE	-	03	03	25	50	75
8	10CSL78	Web Programming Laboratory	CSE/ISE	-	03	03	25	50	75
Total				24	06	-	200	700	900

	Elective II – Group B		Elective III – Group C
10CS751/10IS751	Advanced DBMS	10CS761/10IS761	C# Programming and .Net
10CS752	Digital Signal Processing	10CS762/10IS762	Digital Image Processing
10CS753/10IS753	Java and J2EE	10CS763/10IS763	Game Theory
10CS754/10IS754	Multimedia Computing	10CS764/10IS764	Artificial Intelligence
10CS755/10IS74	Data Warehousing and Data Mining	10CS765/10IS765	Storage Area Networks
10CS756/10IS756	Neural Networks	10CS766/10IS766	Fuzzy Logic

SCHEME OF TEACHING AND EXAMINATION

B.E. COMPUTER SCIENCE AND ENGINEERING

VIII SEMESTER

S. No.	Subject Code	Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration	Marks		
							IA	Exam	Total
1	10IS81	Software Architectures	CSE/ISE	04	-	03	25	100	125
2	10CS82	System Modeling and Simulation	CSE/ISE	04	-	03	25	100	125
3	10CS83x	Elective IV(Group-D)	CSE/ISE	04	-	03	25	100	125
4	10CS84x	Elective V(Group-E)	CSE/ISE	04	-	03	25	100	125
5	10CS85	Project Work	CSE		06	03	100	100	200
6	10CS86	Seminar	CSE	-	-	-	50	-	50
Total				16	06		250	500	750

Elective IV – Group D		Elective V– Group E	
10CS831/10IS831	Wireless Networks and Mobile Computing	10CS841/10IS841	Ad-hoc Networks
10CS832/10IS832	Web 2.0 and Rich Internet Applications	10CS842	Software Testing
10CS833	VLSI Design and Algorithms	10CS843	ARM Based System Design
10CS834/10IS834	Network Management Systems	10CS844/10IS844	Services Oriented Architecture
10CS835/10IS835	Information and Network Security	10CS845/10IS845	Clouds, Grids and Clusters
10CS836/10IS836	Microcontroller-Based Systems	10CS846	Multi-core Architecture and Programming

NOTE: Students have to register for one Elective from each of the five Elective Group.

SCHEME OF TEACHING & EXAMINATION
ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER (COMMON TO EC/TC/ML)

Subject Code	Title	Teaching Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
10MAT - 31	Engg. Mathematics - III	Mat	04		03	25	100	125
10ES – 32	Analog Electronic Ckts	@	04		03	25	100	125
10ES – 33	Logic Design	@	04		03	25	100	125
10ES – 34	Network Analysis	@	04		03	25	100	125
10IT– 35	Electronic Instrumentation	@	04		03	25	100	125
10ES – 36	Field Theory	@	04		03	25	100	125
10ESL – 37	Analog Electronics Lab	@		03	03	25	50	75
10ESL – 38	Logic Design Lab	@		03	03	25	50	75
		Total	24	06	24	200	700	900

SCHEME OF TEACHING & EXAMINATION
ELECTRONICS & COMMUNICATION ENGINEERING
IV SEMESTER (COMMON TO EC/TC/ML)

Subject Code	Title	Teach Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration	I. A	Theory/ Practical	Total Marks
10MAT - 41	Engg. Mathematics – IV	Mat	04		03	25	100	125
10ES– 42	Microcontrollers	@	04		03	25	100	125
10ES – 43	Control Systems	@	04		03	25	100	125
10EC – 44	Signals & Systems	@	04		03	25	100	125
10EC– 45	Fundamentals of HDL	@	04		03	25	100	125
10EC – 46	Linear ICs & Applications	@	04		03	25	100	125
10ESL – 47	Microcontrollers Lab	@		03	03	25	50	75
10ECL – 48	HDL Lab	@		03	03	25	50	75
		Total	24	06	24	200	700	900

Note : @ indicates concerned discipline. **ES (for theory) & ECL (for Lab)** in the subject code indicates that the subject is common to electrical and electronics stream consisting of **EE/EC/IT/TC/ML/BM branches** of engineering.

SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

V SEMESTER **COMMON TO EC/TE**

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
01	10AL51	Management and Entrepreneurship	AL	4	-	3	25	100	125
02	10EC52	Digital Signal Processing	EC	4	-	3	25	100	125
03	10EC53	Analog Communication	EC	4	-	3	25	100	125
04	10EC54	Microwaves and Radar	EC	4	-	3	25	100	125
05	10EC55	Information Theory & Coding	EC	4	-	3	25	100	125
06	10EC56	Fundamentals of CMOS VLSI	EC	4	-	3	25	100	125
07	10ECL57	DSP Lab	EC	-	3	3	25	50	75
08	10ECL58	Analog Communication Lab + LIC Lab	EC	-	3	3	25	50	75
TOTAL				**24**	**06**	**24**	**200**	**700**	**900**

SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

VI SEMESTER

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EC61	Digital Communication	EC	4	-	3	25	100	125
2	10EC62	Microprocessors	EC	4	-	3	25	100	125
3	10EC63	Microelectronics Circuits	EC	4	-	3	25	100	125
4	10EC64	Antennas and Propagation	EC	4	-	3	25	100	125
5	10EC65	Operating Systems	EC	4		3	25	100	125
6	10EC66x	**Elective-I (Group A)**	EC	4	-	3	25	100	125
7	10ECL67	Advanced Communication Lab	EC	-	3	3	25	50	75
8	10ECL68	Microprocessor Lab	EC	-	3	3	25	50	75
TOTAL				**24**	**06**	**24**	**200**	**700**	**900**

Elective-I (Group A)

10EC661 – Analog and Mixed Mode VLSI Design
10EC662 – Satellite Communications
10EC663 - Random Process

10EC664 – Low Power VLSI Design
10EC665 – Data Structure Using C++
10EC666 – Digital System Design Using Verilog
10EC667- Virtual Instrumentation

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SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

VII SEMESTER

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EC71	Computer Communication Networks	EC	4	-	3	25	100	125
2	10EC72	Optical Fiber Communication	EC	4	-	3	25	100	125
3	10EC73	Power Electronics	EC	4	-	3	25	100	125
4	10EC74	Embedded System Design	EC	4	-	3	25	100	125
5	10EC75x	**Elective-II (Group B)**	EC	4	-	3	25	100	125
6	10EC76x	**Elective-III (Group C)**	EC	4	-	3	25	100	125
7	10ECL77	VLSI Lab	EC	-	3	3	25	50	75
8	10ECL78	Power Electronics Lab	EC	-	3	3	25	50	75
TOTAL				**24**	**06**	**24**	**200**	**700**	**900**

Elective-II (Group B)

Elective-III (Group C)

10EC751 – DSP Algorithms & Architecture
10EC752 - Micro and Smart Systems Technology
10EC753 – Artificial Neural Network
10EC754 – CAD for VLSI
10EC755 – Applied Embedded System Design*
10EC756 – Speech Processing

10EC761 - Programming in C++
10EC762 – Real Time Systems
10EC763 - Image Processing
10EC764 - Radio Frequency Integrated Circuits
10EC765 - Wavelet Transforms
10EC766 - Modeling and Simulation of Data Networks

NOTE: 10EC755 Applied Embedded System Design has a LAB component (syllabus is different and in the Theory Examination, questions from Lab experiments will also be there.)

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SCHEME OF TEACHING AND EXAMINATION

B.E. ELECTRONICS AND COMMUNICATION

VIII SEMESTER

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EC81	Wireless Communication	EC	4	-	3	25	100	125
2	10EC82	Digital Switching Systems	EC	4	-	3	25	100	125
3	10EC83x	Elective-IV (Group D)	EC	4	-	3	25	100	125
4	10EC84x	Elective-V (Group E)	EC	4	-	3	25	100	125
5	10ECP85	Project Work	EC	-	6	3	100	100	200
6	10ECS86	Seminar	EC	-	3	-	50	-	50
		TOTAL		**16**	**09**	**15**	**250**	**500**	**750**

Elective-IV (Group-D)
10EC831 – Distributed Systems
10EC832 – Network Security

Elective-V (Group-E)
10EE841 – Multimedia Communication
10EC842 – Real Time Operating Systems

10EC833 - Optical Networks
10EC834 – High Performance Computing Networks
10EC835 – Internet Engineering

10EC843 - GSM
10EC844 - Ad-hoc Wireless Networks
10EC845 – Optical Computing

SCHEME OF TEACHING & EXAMINATION

III SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Subject Code	Title	Teaching Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration in hours	Marks		
						I. A	Theory/ Practical	Total Marks
10MAT - 31	Engineering Mathematics - III	Mat	04		03	25	100	125
10ES – 32	Analog Electronic Circuits	@	04		03	25	100	125
10ES – 33	Logic Design	@	04		03	25	100	125
10ES – 34	Network Analysis	@	04		03	25	100	125
10EE– 35	Electrical and Electronic Measurements And Instrumentation	E&EE	04		03	25	100	125
10EE – 36	Electric Power Generation	E&EE	04		03	25	100	125
10ESL – 37	Analog Electronics Lab	@		03	03	25	50	75
10ESL – 38	Logic Design Lab	@		03	03	25	50	75
		Total	24	06	24	200	700	900

Note : **@** indicates concerned discipline. ES (for theory) & ESL (for Lab) in the subject code indicates that the subject is common to electrical and electronics stream consisting of EE/EC/IT/TC/ML/BM branches of engineering. EE indicates, subjects specific to E & EE branch only.

IV SEMESTER
ELECTRICAL AND ELECTRONICS ENGINEERING

Subject Code	Title	Teaching Dept.	Teaching hours/week		Examination			
			Theory	Practical	Duration in hours	Marks		
						I. A	Theory/ Practical	Total Marks
10MAT - 41	Engineering Mathematics - IV	Mat	04		03	25	100	125
10ES– 42	Microcontrollers	@	04		03	25	100	125
10ES – 43	Control Systems	@	04		03	25	100	125
10EE – 44	Field Theory	E&EE	04		03	25	100	125
10EE– 45	Power Electronics	E&EE	04		03	25	100	125
10EE – 46	Transformers and Induction Machines	E&EE	04		03	25	100	125
10ESL – 47	Microcontrollers Lab	@		03	03	25	50	75
10EEL – 48	Power Electronics Lab	E&EE		03	03	25	50	75
		Total	24	06	24	200	700	900

Note : **@** indicates concerned discipline.ES (for theory) & ESL (for Lab) in the subject code indicates that the subject is common to electrical and electronics stream consisting of EE/EC/IT/TC/ML/BM branches of engineering. EE indicates, subjects specific to E & EE branch only.

V SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
01	10AL51	Management and Entrepreneurship	@	4	-	3	25	100	125
02	10EE52	Signals and Systems	E&EE	4	-	3	25	100	125
03	10EE53	Transmission and Distribution	E&EE	4	-	3	25	100	125
04	10EE54	D.C. Machines and Synchronous Machines	E&EE	4	-	3	25	100	125
05	10EE55	Modern Control theory	E&EE	4	-	3	25	100	125
06	10EE56	Linear IC's and Applications	E&EE	4	-	3	25	100	125
07	10EEL57	Measurements and Circuit Simulation Laboratory	E&EE	-	3	3	25	50	75
08	10EEL58	Transformers and Induction Machines Laboratory	E&EE	-	3	3	25	50	75
Total				24	06	24	200	700	900

@ Any Engineering department or department of Business study.

VI SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EE61	Power System Analysis and Stability	E&EE	4	-	3	25	100	125
2	10EE62	Switchgear & Protection	E&EE	4	-	3	25	100	125
3	10EE63	Electrical Machine Design	E&EE	4	-	3	25	100	125
4	10EE64	Digital Signal Processing	E&EE	4	-	3	25	100	125
5	10EE65	E- CADD	E&EE	1	3	3	25	100	125
6	10EE66X	Elective-I (Group A)	E&EE	4	-	3	25	100	125
7	10EEL67	D.C. Machines and Synchronous Machines Laboratory	E&EE	-	3	3	25	50	75
8	10EEL68	Control Systems Laboratory	E&EE	-	3	3	25	50	75
Total				21	09	24	200	700	900

Elective-I (Group A)

10EE661-Opration Research

10EE662 - Advanced Power Electronics

10EE663 – Fuzzy Logic

10EE664 - Object Oriented Programming using C++

10EE665 - Embedded Systems

10EE666 – Electrical Engineering Materials

VII SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EE71	Computer Techniques in Power System Analysis	E&EE	4	-	3	25	100	125
2	10EE72	Electrical Power Utilization	E&EE	4	-	3	25	100	125
3	10EE73	High Voltage Engineering	E&EE	4	-	3	25	100	125
4	10EE74	Industrial Drives and Applications	E&EE	4	-	3	25	100	125
5	10EE75X	Elective-II (Group B)	E&EE	4	-	3	25	100	125
6	10EE76X	Elective-III (Group C)	E&EE	4	-	3	25	100	125
7	10EEL77	Relay and High Voltage Laboratory	E&EE	-	3	3	25	50	75
8	10EEL78	Power System Simulation Laboratory	E&EE	-	3	3	25	50	75
Total				24	06	24	200	700	900

Elective-II (Group B)
10EE751 - HVDC Transmission
10EE752 - Programmable Logic Controllers
10EE753 - Artificial Neural Network
10EE754 - Operating System
10EE755 - Digital System with VHDL
10EE756 - Testing and Commissioning of Electrical Equipment

Elective-III (Group C)
10EE761 - Power System Planning
10EE762 - Computer Control of Electrical Drives
10EE763 - Data Structure
10EE764 - VLSI Circuits and Design
10EE765 - Micro & Smart System Technology
10EE766 - Electromagnetic Compatibility

VIII SEMESTER

ELECTRICAL AND ELECTRONICS ENGINEERING

Sl. No.	Subject Code	Title of the Subject	Teaching Dept.	Teaching Hrs / Week		Examination			
				Theory	Practical	Duration (Hrs)	Marks		
							IA	Theory / Practical	Total
1	10EE81	Electrical Design, Estimation and Costing	E&EE	4	-	3	25	100	125
2	10EE82	Power System Operation and Control	E&EE	4	-	3	25	100	125
3	10EE83X	Elective-IV (Group D)	E&EE	4	-	3	25	100	125
4	10EE84X	Elective-V (Group E)	E&EE	4	-	3	25	100	125
5	10EEP85	Project Work	E&EE	-	6	3	100	100	200
6	10EES86	Seminar (on a latest topic relevant to the branch and independent of the project work)	E&EE	-	3	-	50	-	50
Total				16	09	15	250	500	750

Elective-IV (Group-D)
10EE831 - Reactive Power Management
10EE832 - Flexible A.C. Transmission Systems (FACTS)
10EE833- Advanced Instrumentation System
10EE834 - AI Applications to Power Systems
10EE835 - Data Base Management Systems (DBMS)
10EE836 - Renewable Energy Sources

Elective-V (Group-E)
10EE841 - Power Systems Dynamics and Stability
10EE842 - Energy Auditing & Demand Side Management
10EE843 - Data communications and Networking
10EE844 - Electrical Distribution Systems
10EE845 - Insulation Engineering
10EE846 - Intellectual Property Rights
10EE847 - Electrical Power Quality

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

III SEMESTER

Sl. No	Sub-Code	Title	Teaching Dept.	Teaching hours /week		Examination			
				Theory	Pract. /Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10MAT31	Engineering Mathematics - III	Mathematics	04	-	03	25	100	125
2	10ME32A/ 10ME32B	Material Sc. & Metallurgy / Mechanical Measurements & Metrology	Mechanical	04	--	03	25	100	125
3	10ME33	Basic Thermodynamics	Mechanical	04	--	03	25	100	125
4	10ME34	Mechanics of Materials	Mechanical	04	--	03	25	100	125
5	10ME35	Manufacturing Process I (Fundamentals of Foundry and Welding)	Mechanical	04	--	03	25	100	125
6	10ME36A/ 10ME36B	Computer Aided Machine Drawing / Fluid Mechanics	Mechanical	01 04	03 --	03	25	100	125
7	10MEL37A/ 10MEL37B	Metallography & Material Testing Lab / Mech. Measurements & Metrology Lab	Mechanical	--	03	03	25	50	75
8	10MEL38A/ 10MEL38B	Foundry & Forging lab / Machine Shop	Mechanical	--	03	03	25	50	75
TOTAL				**21/24**	**09**	**24**	**200**	**700**	**900**

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

IV SEMESTER

Sl. No	Sub-Code	Title	Teaching Dept.	Teaching hours /week		Examination			
				Theory	Pract/ Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10MAT41	Engineering Mathematics - IV	Mathematics	04	-	03	25	100	125
2	10ME42A/ 10ME42B	Material Sc. & Metallurgy / Mechanical Measurements & Metrology	Mechanical	04	--	03	25	100	125
3	10ME43	Applied Thermodynamics	Mechanical	04	--	03	25	100	125
4	10ME44	Kinematics of Machines	Mechanical	04	--	03	25	100	125
5	10ME45	Manufacturing Process II	Mechanical	04	--	03	25	100	125
6	10ME46A/1 0ME46B	Computer Aided Machine Drawing / Fluid Mechanics	Mechanical	01 04	03 --	03	25	100	125
7	10MEL47A/ 10MEL47B	Metallography & Material Testing Lab / Mech. Measurements & Metrology Lab	Mechanical	--	03	03	25	50	75
8	10MEL48A/ 10MEL48B	Foundry & Forging lab / Machine Shop	Mechanical	--	03	03	25	50	75
		TOTAL		**21/24**	**09**	**24**	**200**	**700**	**900**

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

V SEMESTER

Sl. No	Sub-Code	Title	Teaching hours /week		Examination			
			Theory	Pract. / Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10AL51	Management and Entrepreneurship	04	--	03	25	100	125
2	10ME52	Design of Machine Elements I	04	--	03	25	100	125
3	10ME53	Energy Engineering	04	--	03	25	100	125
4	10ME54	Dynamics of Machines	04	--	03	25	100	125
5	10ME55	Manufacturing Process III	04	--	03	25	100	125
6	10ME56	Turbo Machines	04	--	03	25	100	125
7	10MEL57	Fluid Mechanics & Machines Lab	--	03	03	25	50	75
8	10MEL58	Energy Conversion Engg. Lab		03	03	25	50	75
		TOTAL	**24**	**06**	**24**	**200**	**700**	**900**

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

VI SEMESTER

Sl. No	Sub-Code	Title	Teaching hours /week		Examination			
			Theory	Pract. / Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10ME61	Computer Integrated Manufacturing	04	--	03	25	100	125
2	10ME62	Design of Machine Elements II	04	--	03	25	100	125
3	10ME63	Heat & Mass Transfer	04	--	03	25	100	125
4	10ME64	Finite Element Methods	04	--	03	25	100	125
5	10ME65	Mechatronics & Microprocessor	04	--	03	25	100	125
6	10ME66X	Elective ‘A’	04	--	03	25	100	125
7	10MEL67	Heat & Mass Transfer Lab	--	03	03	25	50	75
8	10MEL68	CAMA Lab	--	03	03	25	50	75
		TOTAL	**24**	**06**	**24**	**200**	**700**	**900**

Elective – 1 (Group A)			
10ME661	Theory of Elasticity	10ME662	Mechanics of Composite Materials
10ME663	Refrigeration & Air Conditioning	10ME664	Design of Heat Exchangers
10ME665	Non-Traditional Machining	10ME666	Knowledge Management
10ME667	Project Management	10ME668	Statistical Quality Control

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

VII SEMESTER

Sl. No	Sub-Code	Title	Teaching hours /week		Examination			
			Theory	Pract/ Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10ME71	Economics	04	--	03	25	100	125
2	10ME72	Mechanical Vibrations	04	--	03	25	100	125
3	10ME73	Hydraulics and Pneumatics	04	--	03	25	100	125
4	10ME74	Operations Research	04	--	03	25	100	125
5	10ME75X	Elective B	04	--	03	25	100	125
6	10ME76X	Elective C	04	--	03	25	100	125
7	10MEL77	Design Lab	--	03	03	25	50	75
8	10MEL78	CIM and Automation Lab	--	03	03	25	50	75
		TOTAL	**24**	**06**	**24**	**200**	**700**	**900**

Elective – 2 (Group B)		**Elective – 3 (Group C)**	
10ME751	Mechanism Design	10ME761	Experimental Stress Analysis
10ME752	Theory of Plasticity	10ME762	Tool Design
10ME753	Engineering Design	10ME763	Cryogenics
10ME754	Non Conventional Energy Sources	10ME764	Smart Materials
10ME755	Gas Dynamics	10ME765	Agile Manufacturing
10ME756	Management Information System	10ME766	Robotics
10ME757	Automation in Manufacturing	10ME767	Finance Management
10ME758	Total Quality Management	10ME768	Micro & Smart System Technology
		10ME769	Product Life Cycle Management

SCHEME OF TEACHING AND EXAMINATION

B.E. MECHANICAL ENGINEERING

VIII SEMESTER

Sl. No	Sub-Code	Title	Teaching hours /week		Examination			
			Theory	Pract./ Drg.	Dura-tion	I.A. Marks	Theory/ Pract.	Total Marks
1	10ME81	Operations Management	04	--	03	25	100	125
2	10ME82	Control Engineering	04	--	03	25	100	125
3	10ME83X	Elective D	04	--	03	25	100	125
4	10ME84X	Elective E	04	--	03	25	100	125
5	10ME85L	Project Work	--	06	03	100	100	200
6	10ME86L	Seminar	--	03	--	50	--	50
		TOTAL		**09**	**15**	**250**	**500**	**750**

Elective – 4 (Group D)		**Elective – 5 (Group E)**	
10ME831	Tribology	10ME841	Machine Tool Design
10ME832	Fracture Mechanics	10ME842	Industrial Engineering & Ergonomics
10ME833	Power Plant Engineering	10ME843	Bio Mass Energy Systems
10ME834	Nanotechnology	10ME844	Automotive Engineering
10ME835	Organisational Behaviour and Professional Communication	10ME845	Database Management System
10ME836	Computer Graphics	10ME846	Artificial Intelligence
10ME837	Rapid Prototyping	10ME847	Design of Experiments
10ME838	Foundry Technology	10ME848	Design for Manufacture & Assembly

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

I SEMESTER B.E./B.TECH.

PHYSICS GROUP

Sl. No.	Subject Code	Subject		Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks			Credits
							Th./Pr.	I.A.	Total	
1	15MAT11	Engineering Maths-I	BS	Maths	Basic Sc.	4 (T)	80	20	100	4
2	15PHY12	Engineering Physics	BS	Physics	Basic Sc.	4 (T)	80	20	100	4
3	15CIV13	Elements of Civil Engg. & Mechanics	ES	Civil Engg.	Civil Engg.	4 (T)	80	20	100	4
4	15EME14	Elements of Mechanical Engg.	ES	Mech. Engg.	Mech. Engg.	4 (T)	80	20	100	4
5	15ELE15	Basic Electrical Engg.	ES	E & E	E & E	4 (T)	80	20	100	4
6	15WSL16	Workshop Practice	ES	Mech., Auto, IP, IEM, Mfg. Engg.	Mech. Engg.	3(2 hrs lab+ 1 hr instruction)	80	20	100	2
7	15PHYL17	Engg. Physics Lab	BS	Physics	Basic Sc.	3(2 hrs lab+ 1 hr instruction)	80	20	100	2
8	15CPH18	Constitution of India, Professional Ethics and Human Rights (CPH)	MNC	Humanities		2 (Tutorial)	40	10	50	--
9		Language (Kan.)	Mandatory Learning	Humanities		1 (T)	-	-	-	--
						29	600	150	750	24

Note: The Subjects Kannada and English are Audit Courses

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

IISEMESTER B.E./B.TECH.

PHYSICS GROUP

Sl. No.	Subject Code	Subject		Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks			Credits
							Th./Pr.	I.A.	Total	
1	15MAT21	Engineering Maths-II	BS	Maths	Basic Sc.	4 (T)	80	20	100	4
2	15PHY22	Engineering Physics	BS	Physics	Basic Sc.	4 (T)	80	20	100	4
3	15CIV23	Elements of Civil Engg. & Mechanics	ES	Civil Engg.	Civil Engg.	4 (T)	80	20	100	4
4	15EME24	Elements of Mechanical Engg.	ES	Mech. Engg.	Mech. Engg.	4 (T)	80	20	100	4
5	15ELE25	Basic Electrical Engg.	ES	E & E	E & E	4 (T)	80	20	100	4
6	15WSL26	Workshop Practice	ES	Mech., Auto, IP, IEM, Mfg. Engg.	Mech. Engg.	3(2 hrs lab+ 1 hr instruction)	80	20	100	2
7	15PHYL27	Engg. Physics Lab	BS	Physics	Basic Sc.	3(2 hrs lab+ 1 hr instruction)	80	20	100	2
8	15CPH28	Constitution of India, Professional Ethics and Human Rights	MNC	Humanities		2 (Tutorial)	40	10	50	--
9		Language (Kan.)	Mandatory Learning	Humanities		1 (T)	-	-	-	--
						29	600	150	750	24

Note: The Subjects Kannada and English are Audit Courses

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

I SEMESTER B.E./B.TECH.

CHEMISTRY GROUP

Sl. No.	Subject Code	Subject		Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks			Credits
							Th./Pr.	I.A.	Total	
1	15MAT11	Engineering Maths-I	BS	Maths	Basic Sc.	4 (T)	80	20	100	4
2	15CHE12	Engineering Chemistry	BS	Chemistry	Basic Sc.	4 (T)	80	20	100	4
3	15PCD13	Programming in C & Data Structures	ES	Any Engineering Department	CSE	4 (T)	80	20	100	4
4	15CED14	Computer Aided Engineering Drawing	ES	Mech./IP/Auto/ Mfg.Engg./ IEM	Mech. Engg.	6 (2I+ 4P)	80	20	100	4
5	15ELN15	Basic Electronics	ES	E & C / E & E / TC / IT	E & C	4 (T)	80	20	100	4
6	15CPL16	Computer Programming Lab	ES	Any Engineering Department	CSE	3(2 hrs lab+ 1 hr Tutorial)	80	20	100	2
7	15CHEL17	Engg. Chemistry Lab	BS	Chemistry	Basic Sci.	3(2 hrs lab+ 1 hr Tutorial)	80	20	100	2
8	15CIV18	Environmental Studies	MNC	Civil / Environmental	Civil	2 (Tutorial)	40	10	50	--
9		Language (Eng.)	Mandatory Learning	Humanities		1 (T)	-	-	-	--
					Total	31	600	150	750	24

Note: The Subjects Kannada and English are Audit Courses

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

II SEMESTER B.E./B.TECH.

CHEMISTRY GROUP

Sl. No.	Subject Code	Subject		Teaching Department	Board	Theory /Lab/ Drawing (Hrs/ Week)	Examination Marks			Credits
							Th./Pr.	I.A.	Total	
1	15MAT21	Engineering Maths-II	BS	Maths	Basic Sc.	4 (T)	80	20	100	4
2	15CHE22	Engineering Chemistry	BS	Chemistry	Basic Sc.	4 (T)	80	20	100	4
3	15PCD23	Programming in C & Data Structures	ES	Any Engineering Department	CSE	4 (T)	80	20	100	4
4	15CED24	Computer Aided Engineering Drawing	ES	Mech./IP/Auto/ Mfg.Engg./ IEM	Mech. Engg.	6 (2I+ 4P)	80	20	100	4
5	15ELN25	Basic Electronics	ES	E & C / E & E / TC / IT	E & C	4 (T)	80	20	100	4
6	15CPL26	Computer Programming Lab	ES	Any Engineering Department	CSE	3(2 hrs lab+ 1 hr Tutorial)	80	20	100	2
7	15CHEL27	Engg. Chemistry Lab	BS	Chemistry	Basic Sc.	3(2 hrs lab+ 1 hr Tutorial)	80	20	100	2
8	15CIV28	Environmental Studies	MNC	Civil / Environmental	Civil	2 (Tutorial)	40	10	50	--
9		Language (Eng.)	Mandatory Learning	Humanities		1 (T)	-	-	-	--
					Total	31	600	150	750	24

Note: The Subjects Kannada and English are Audit Courses

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering/ B.E. Information Science & Engineering

III SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15MAT31	Engineering Mathematics - III	04	--	03	80	20	100	4
2	15CS32	Analog and Digital Electronics	04	--	03	80	20	100	4
3	15CS33	Data Structures and Applications	04	--	03	80	20	100	4
4	15CS34	Computer Organization	04	--	03	80	20	100	4
5	15CS35	Unix and Shell Programming	04	--	03	80	20	100	4
6	15CS36	Discrete Mathematical Structures	04	--	03	80	20	100	4
7	15CSL37	Analog and Digital Electronics Laboratory	--	1I+2P	03	80	20	100	2
8	15CSL38	Data Structures Laboratory	--	1I+2P	03	80	20	100	2
		TOTAL	**24**	**6**	**24**	**640**	**160**	**800**	**28**

Note: 'I' Stands for Instruction Hours and 'P' for practical Hours

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering/ B.E. Information Science & Engineering

IV SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15MAT41	**Engineering Mathematics - IV**	04	--	03	80	20	100	4
2	15CS 42	**Software Engineering**	04	--	03	80	20	100	4
3	15CS43	**Design and Analysis of Algorithms**	04	--	03	80	20	100	4
4	15CS 44	**Microprocessors and Microcontrollers**	04	--	03	80	20	100	4
5	15CS45	**Object Oriented Concepts**	04	--	03	80	20	100	4
6	15CS46	**Data Communication**	04	--	03	80	20	100	4
7	15CSL47	**Design and Analysis of Algorithm Laboratory**	--	1I+2P	03	80	20	100	2
8	15CSL48	**Microprocessors Laboratory**	--	1I+2P	03	80	20	100	2
TOTAL			24	06	24	640	160	800	28

Note: 'I' Stands for Instruction Hours and 'P' for practical Hours

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering

V SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	**15CS51**	Management and Entrepreneurship for IT Industry	04	--	03	80	20	100	4
2	**15CS52**	Computer Networks	04	--	03	80	20	100	4
3	**15CS53**	Database Management System	04	--	03	80	20	100	4
4	**15CS54**	Automata theory and Computability	04	--	03	80	20	100	4
5	**15CS55x**	Professional Elective 1	03	--	03	80	20	100	3
6	**15CS56x**	Open Elective 1	03	--	03	80	20	100	3
7	**15CSL57**	Computer Network Laboratory	--	1I+2P	03	80	20	100	2
8	**15CSL58**	DBMS Laboratory with mini project	--	1I+2P	03	80	20	100	2
		TOTAL	**22**	**6**	**24**	**640**	**160**	**800**	**26**

Professional Elective 1	
15CS551	Object Oriented Modeling and Design
15CS552	Introduction to Software Testing
15CS553	Advanced JAVA and J2EE
15CS554	Advanced Algorithms

1. **Professional Elective: Electives relevant to chosen specialization / branch**
2. **Open Elective: Electives from other technical and/or emerging subject areas (Announced separately)**

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering

VI SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	**15CS61**	Cryptography, Network Security and Cyber Law	04	--	03	80	20	100	4
2	**15CS62**	Computer Graphics and Visualization	04	--	03	80	20	100	4
3	**15CS63**	System Software and Compiler Design	04	--	03	80	20	100	4
4	**15CS64**	Operating Systems	04	--	03	80	20	100	4
5	**15CS65x**	Professional Elective 2	03	--	03	80	20	100	3
6	**15CS66x**	Open Elective 2	03	--	03	80	20	100	3
7	**15CSL67**	System Software and Operating System Laboratory	--	1I+2P	03	80	20	100	2
8	**15CSL68**	Computer Graphics Laboratory with mini project	--	1I+2P	03	80	20	100	2
		TOTAL	**22**	**6**	**24**	**640**	**160**	**800**	**26**

Professional Elective 2	
15CS651	Data Mining and Data Warehousing
15CS652	Software Architecture and Design Patterns
15CS653	Operations research
15CS654	Distributed Computing system

1. **Professional Elective: Electives relevant to choosen specialization / branch**
2. **Open Elective: Electives from other technical and/or emerging subject areas (Announced separately)**

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering

VII SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15CS71	Web Technology and its applications	04	--	03	20	80	100	4
2	15CS72	Advanced Computer Architectures	04	--	03	20	80	100	4
3	15CS73	Machine Learning	04	--	03	20	80	100	4
4	15CS74x	Professional Elective 3	03	--	03	20	80	100	3
5	15CS75x	Professional Elective 4	03	--	03	20	80	100	3
6	15CSL76	Machine Learning Laboratory	--	1I+2P	03	20	80	100	2
7	15CSL77	Web Technology Laboratory with mini project	--	1I+2P	03	20	80	100	2
8	15CSP78	Project Phase 1 + Seminar	--	--	--	100	--	100	2
		TOTAL	18	6	21	240	560	800	24

Professional Elective 3		Professional Elective 4	
15CS741	Natural Language Processing	15CS751	Soft and Evolutionary Computing
15CS742	Cloud Computing and its Applications	15CS752	Computer Vision and Robotics
15CS743	Information and Network Security	15CS753	Digital Image Processing
15CS744	Unix System Programming	15CS754	Storage Area Networks

1. Professional Elective: Electives relevant to choosen specialization / branch
2. Project Phase 1 + Seminar : Literature Survey, Problem Identification, Objectives and Methodology, Submission of Synopsis and Seminar

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering

VIII SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15CS81	Internet of Things and Applications	4	--	3	20	80	100	4
2	15CS82	Big Data Analytics	4	--	3	20	80	100	4
3	15CS83x	Professional Elective 5	3	--	3	20	80	100	3
4	15CS84	Internship / Professional Practice	Industry Oriented		3	50	50	100	2
5	15CSP85	Project work phase II	--	6	3	100	100	200	5
6	15CSS86	Seminar	--	4	--	100	--	100	2
		TOTAL	11	10	15	310	390	700	20

Professional Elective 5	
15CS831	High Performance Computing
15CS832	User Interface Design
15CS833	Network management
15CS834	System Modeling and Simulation

1. Professional Elective: Electives relevant to chosen specialization / branch
2. Internship / Professional Practice: To be carried out between 6^{th} and 7^{th} semester vacation or 7^{th} and 8^{th} semester vacation period

SCHEME OF TEACHING AND EXAMINATION
B.E Electronics & Communication Engineering / Telecommunication Engineering
(Common to Electronics & Communication and Telecommunication Engineering)

III SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical / Drawing	Duration	Theory / Practical Marks	I.A. Marks	Total Marks	
1	15MAT31	Engineering Mathematics –III*	04		03	80	20	100	4
2	15EC32	Analog Electronics	04		03	80	20	100	4
3	15EC33	Digital Electronics	04		03	80	20	100	4
4	15EC34	Network Analysis	04		03	80	20	100	4
5	15EC35	Electronic Instrumentation	04		03	80	20	100	4
6	15EC36	Engineering Electromagnetics	04		03	80	20	100	4
7	15ECL37	Analog Electronics Lab		1I+2P	03	80	20	100	2
8	15ECL38	Digital Electronics Lab		1I+2P	03	80	20	100	2
TOTAL			**24**	**6**	**24**	**640**	**160**	**800**	**28**

*Additional course for Lateral entry students only:

1	15MATDIP31	Additional Mathematics - I	03		03	80	--	80	--

SCHEME OF TEACHING AND EXAMINATION
B.E Electronics & Communication Engineering / Telecommunication Engineering
(Common to Electronics & Communication and Telecommunication Engineering)

IV SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical / Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15MAT41	Engineering Mathematics –IV*	04		03	80	20	100	4
2	15EC42	Microprocessor	04		03	80	20	100	4
3	15EC43	Control Systems	04		03	80	20	100	4
4	15EC44	Signals and Systems	04		03	80	20	100	4
5	15EC45	Principles of Communication Systems	04		03	80	20	100	4
6	15EC46	Linear Integrated Circuits	04		03	80	20	100	4
7	15ECL47	Microprocessor Lab		1I+2P	03	80	20	100	2
8	15ECL48	Linear ICs and Communication Lab		1I+2P	03	80	20	100	2
		TOTAL	**24**	**06**	**24**	**640**	**160**	**800**	**28**

*Additional course for Lateral entry students only:

1	15MATDIP41	Additional Mathematics - II	03		03	80	--	80	--

SCHEME OF TEACHING AND EXAMINATION
B.E.: Electronics & Communication Engineering

V SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical /Drawing	Duration	Theory / Practical Marks	I.A. Marks	Total Marks	
1	15ES51	Management and Entrepreneurship Development	04		03	80	20	100	4
2	15EC52	Digital Signal Processing	04		03	80	20	100	4
3	15EC53	Verilog HDL	04		03	80	20	100	4
4	15EC54	Information Theory & Coding	04		03	80	20	100	4
5	15EC55X	Professional Elective-1	03		03	80	20	100	3
6	15EC56X	Open Elective-1	03		03	80	20	100	3
7	15ECL57	DSP Lab		1I+2P	03	80	20	100	2
8	15ECL58	HDL Lab		1I+2P	03	80	20	100	2
TOTAL			**22**	**06**	**24**	**640**	**160**	**800**	**26**

Professional Elective-1			**Open Elective – 1* (List offered by EC/TC Board only)**	
15EC551	Nanoelectronics		15EC561	Automotive Electronics
15EC552	Switching & Finite Automata Theory		15EC562	Object Oriented Programming Using C++
15EC553	Operating System		15EC563	8051 Microcontroller
15EC554	Electrical Engineering Materials			
15EC555	MSP430 Microcontroller			

1. **Professional Elective:** Elective relevant to chosen specialization / branch.
2. *** Open Elective List**: For other Open Electives offered by other Boards, refer the Scheme of other Boards or Consolidated list in VTU Website.

SCHEME OF TEACHING AND EXAMINATION
B.E.: Electronics & Communication Engineering

VI SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical / Drawing	Duration	Theory / Practical Marks	I.A. Marks	Total Marks	
1	15EC61	Digital Communication	04		03	80	20	100	4
2	15EC62	ARM Microcontroller & Embedded Systems	04		03	80	20	100	4
3	15EC63	VLSI Design	04		03	80	20	100	4
4	15EC64	Computer Communication Networks	04		03	80	20	100	4
5	15EC65X	Professional Elective-2	03		03	80	20	100	3
6	15EC66X	Open Elective-2	03		03	80	20	100	3
7	15ECL67	Embedded Controller Lab		1I+2P	03	80	20	100	2
8	15ECL68	Computer Networks Lab		1I+2P	03	80	20	100	2
		TOTAL	**22**	**6**	**24**	**640**	**160**	**800**	**26**

Professional Elective-2			**Open Elective – 2* (List offered by EC/TC Board only)**	
15EC651	Cellular Mobile Communication		15EC661	Data Structures Using C++
15EC652	Adaptive Signal Processing		15EC662	Power Electronics
15EC653	Artificial Neural Networks		15EC663	Digital System Design using Verilog
15EC654	Digital Switching Systems			
15EC655	Microelectronics			

1. Professional Elective: Elective relevant to chosen specialization/branch.
2. * Open Elective List: For other Open Electives offered by other Boards, refer the Scheme of other Boards or Consolidated list in VTU Website.

SCHEME OF TEACHING AND EXAMINATION
B.E.: Electronics & Communication Engineering

VII SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				15EC
			Theory	Practical/Drawing	Duration	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15EC71	Microwave and Antennas	04		03	20	80	100	4
2	15EC72	Digital Image Processing	04		03	20	80	100	4
3	15EC73	Power Electronics	04		03	20	80	100	4
4	15XX74X	Professional Elective-3	03		03	20	80	100	3
5	15EC75X	Professional Elective-4	03		03	20	80	100	3
6	15ECL76	Advanced Communication Lab		1I+2P	03	20	80	100	2
7	15ECL77	VLSI Lab		1I+2P	03	20	80	100	2
8	15ECP78	Project Work Phase–I + Project work Seminar		03		100	-	100	2
TOTAL			**18**	**09**	**21**	**240**	**560**	**800**	**24**

Professional Elective-3		Professional Elective-4	
15EC741	Multimedia Communication	15EC751	DSP Algorithms and Architecture
15EC742	Biomedical Signal Processing	15EC752	IoT and Wireless Sensor Networks
15EC743	Real Time Systems	15EC753	Pattern Recognition
15EC744	Cryptography	15EC754	Advanced Computer Architecture
15EC745	CAD for VLSI	15EC755	Satellite Communication

1. Project Phase –I + Project Work Seminar: Literature Survey, Problem Identification, Objectives and Methodology. Submission of Synopsis and Seminar.

SCHEME OF TEACHING AND EXAMINATION
B.E.: Electronics & Communication Engineering

VIII SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical / Drawing	Duration	I.A. Marks	Theory / Practical Marks	Total Marks	
1	15EC81	Wireless Cellular and LTE 4G Broadband	4	-	3	20	80	100	4
2	15EC82	Fiber Optics & Networks	4	-	3	20	80	100	4
3	15EC83X	Professional Elective-5	3	-	3	20	80	100	3
4	15EC84	Internship/Professional Practice	Industry Oriented		3	50	50	100	2
5	15ECP85	Project Work	-	6	3	100	100	200	6
6	15ECS86	Seminar	-	4	-	100	-	100	1
		TOTAL	**11**	**10**	**15**	**310**	**390**	**700**	**20**

Professional Elective -5	
15EC831	Micro Electro Mechanical Systems
15EC832	Speech Processing
15EC833	Radar Engineering
15EC834	Machine learning
15EC835	Network and Cyber Security

1. Internship / Professional Practice: To be carried between the (6th and 7th Semester) or (7th and 8th) Semester Vacation period.

B.E., III Semester, Electronics & Communication Engineering /Telecommunication Engineering

ENGINEERING MATHEMATICS-III **B.E., III Semester, Common to all Branches** [As per Choice Based Credit System (CBCS) scheme]			
Subject Code	15MAT31	IA Marks	20
Number of Lecture Hours/Week	04	Exam marks	80
Total Number of Lecture Hours	50 (10 Hours per Module)		
Credits – 04			

Course Objectives: This course will enable students to:

- Introduce most commonly used analytical and numerical methods in the different engineering fields.
- Learn Fourier series, Fourier transforms and Z-transforms, statistical methods, numerical methods.
- Solve algebraic and transcendental equations, vector integration and calculus of variations.

Modules	RBT Level
Module-1	
Fourier Series: Periodic functions, Dirichlet's condition, Fourier Series of periodic functions with period 2π and with arbitrary period $2c$. Fourier series of even and odd functions. Half range Fourier Series, practical harmonic analysis-Illustrative examples from engineering field.	L1, L2, L4
Module-2	
Fourier Transforms: Infinite Fourier transforms, Fourier sine and cosine transforms. Inverse Fourier transform. **Z-transform:** Difference equations, basic definition, z-transform-definition, Standard z-transforms, Damping rule, Shifting rule, Initial value and final value theorems (without proof) and problems, Inverse z-transform. Applications of z-transforms to solve difference equations.	L2, L3, L4
Module-3	
Statistical Methods: Review of measures of central tendency and dispersion. Correlation-Karl Pearson's coefficient of correlation-problems. Regression analysis- lines of regression (without proof) –Problems **Curve Fitting:** Curve fitting by the method of least squares- fitting of the curves of the form, $y = ax + b$, $y = ax^2 + bx + c$ *and* $y = ae^{bx}$. **Numerical Methods:** Numerical solution of algebraic and transcendental equations by Regula- Falsi Method and Newton-Raphson method.	L3
Module-4	
Finite differences: Forward and backward differences, Newton's forward and backward interpolation formulae. Divided differences- Newton's divided difference formula. Lagrange's interpolation formula and inverse interpolation formula (all formulae without proof)-Problems. **Numerical integration:** Simpson's $(1/3)^{th}$ and $(3/8)^{th}$ rules, Weddle's rule (without proof)–Problems.	L3

Module-5	
Vector integration: Line integrals-definition and problems, surface and volume integrals-definition, Green's theorem in a plane, Stokes and Gauss-divergence theorem(without proof) and problems. **Calculus of Variations:** Variation of function and Functional, variational problems. Euler's equation, Geodesics, hanging chain, Problems.	L3, L4 L2, L4
Course outcomes: On completion of this course, students are able to: • Know the use of periodic signals and Fourier series to analyze circuits and system communications. • Explain the general linear system theory for continuous-time signals and digital signal processing using the Fourier Transform and z-transform. • Employ appropriate numerical methods to solve algebraic and transcendental equations. • Apply Green's Theorem, Divergence Theorem and Stokes' theorem in various applications in the field of electro-magnetic and gravitational fields and fluid flow problems. • Determine the extremals of functionals and solve the simple problems of the calculus of variations.	

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. *B.S. Grewal: Higher Engineering Mathematics, Khanna Publishers, 43rd Ed., 2015.*
2. *E. Kreyszig: Advanced Engineering Mathematics, John Wiley & Sons,10th Ed., 2015.*

Reference Books:

1. *N.P.Bali and Manish Goyal: A Text Book of Engineering Mathematics, Laxmi Publishers, 7th Ed., 2010.*
2. *B.V.Ramana: "Higher Engineering Mathematics" Tata McGraw-Hill, 2006.*
3. *H. K. Dass and Er. Rajnish Verma: "Higher Engineering Mathematics", S. Chand publishing, 1st edition, 2011.*

Web Link and Video Lectures:

1. *http://nptel.ac.in/courses.php?disciplineID=111*
2. *http://www.khanacademy.org/*
3. *http://www.class-central.com/subject/math*

ADDITIONAL MATHEMATICS - I

B.E., III Semester, Common to all Branches

(A Bridge course for Lateral Entry students of III Sem. B. E.)

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15MATDIP31	IA Marks	--
Number of Lecture Hours/Week	03	Exam marks	80
Total Number of Lecture Hours	40 (08 Hours per Module)		

Credits – 00

Course Objectives: This course will enable students to:

- Acquire basic concepts of complex trigonometry, vector algebra, differential & integral calculus and vector differentiation.
- Solve first order differential equations.

Modules	RBT Level
Module-1	
Complex Trigonometry: Complex Numbers: Definitions & properties. Modulus and amplitude of a complex number, Argand's diagram, De-Moivre's theorem (without proof). **Vector Algebra**: Scalar and vectors. Vectors addition and subtraction. Multiplication of vectors (Dot and Cross products). Scalar and vector triple products-simple problems.	**L1**
Module-2	
Differential Calculus: Review of successive differentiation. Formulae for n^{th} derivatives of standard functions- Liebnitz's theorem (without proof). Polar curves–angle between the radius vector and the tangent pedal equation- Problems. Maclaurin's series expansions- Illustrative examples. Partial Differentiation : Euler's theorem for homogeneous functions of two variables. Total derivatives-differentiation of composite and implicit function. Application to Jacobians.	**L1, L2**
Module-3	
Integral Calculus: Statement of reduction formulae for $\sin^n x$, $\cos^n x$, *and* $\sin^m x \cos^n x$ and evaluation of these with standard limits-Examples. Double and triple integrals-Simple examples.	**L1, L2**
Module-4	
Vector Differentiation: Differentiation of vector functions. Velocity and acceleration of a particle moving on a space curve. Scalar and vector point functions. Gradient, Divergence, Curl and Laplacian (Definitions only). Solenoidal and irrotational vector fields-Problems.	**L1, L2**
Module-5	
Ordinary differential equations (ODE's): Introduction-solutions of first order and first degree differential equations: homogeneous, exact, linear differential equations of order one and equations reducible to above types.	**L1, L2**

Course outcomes: On completion of the course, students are able to: • Understand the fundamental concepts of complex numbers and vector algebra to analyze the problems arising in related area. • Use derivatives and partial derivatives to calculate rates of change of multivariate functions. • Learn techniques of integration including double and triple integrals to find area, volume, mass and moment of inertia of plane and solid region. • Analyze position, velocity and acceleration in two or three dimensions using the calculus of vector valued functions. • Recognize and solve first-order ordinary differential equations occurring in different branches of engineering.	
Question paper pattern: • The question paper will have ten questions. • Each full Question consisting of 16 marks • There will be 2 full questions (with a maximum of four sub questions) from each module. • Each full question will have sub questions covering all the topics under a module. • The students will have to answer 5 full questions, selecting one full question from each module.	
Text Book: *B.S. Grewal: Higher Engineering Mathematics, Khanna Publishers, New Delhi, 43rd Ed., 2015.*	
Reference Books: 1. *E. Kreyszig: Advanced Engineering Mathematics, John Wiley & Sons, 10th Ed., 2015.* 2. *N.P.Bali and Manish Goyal: Engineering Mathematics, Laxmi Publishers, 7th Ed., 2007.*	

ANALOG ELECTRONICS
[As per Choice Based Credit System (CBCS) scheme]
SEMESTER – III (EC/TC)

Subject Code	15EC32	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours per Module)	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to:

- Explain various BJT parameters, connections and configurations.
- Explain BJT Amplifier, Hybrid Equivalent and Hybrid Models.
- Explain construction and characteristics of JFETs and MOSFETs.
- Explain various types of FET biasing, and demonstrate the use of FET amplifiers.
- Construct frequency response of BJT and FET amplifiers at various frequencies.
- Analyze Power amplifier circuits in different modes of operation.
- Construct Feedback and Oscillator circuits using FET.

Modules	RBT Level
Module -1	
BJT AC Analysis: BJT Transistor Modeling, The re transistor model, Common emitter fixed bias, Voltage divider bias, Emitter follower configuration. Darlington connection-DC bias; The Hybrid equivalent model, Approximate Hybrid Equivalent Circuit- Fixed bias, Voltage divider, Emitter follower configuration; Complete Hybrid equivalent model, Hybrid π Model.	**L1, L2,L3**
Module -2	
Field Effect Transistors: Construction and Characteristics of JFETs, Transfer Characteristics, Depletion type MOSFET, Enhancement type MOSFET. **FET Amplifiers:** JFET small signal model, Fixed bias configuration, Self bias configuration, Voltage divider configuration, Common Gate configuration. Source-Follower Configuration, Cascade configuration.	**L1, L2, L3**
Module -3	
BJT and JFET Frequency Response: Logarithms, Decibels, Low frequency response – BJT Amplifier with RL, Low frequency response-FET Amplifier, Miller effect capacitance, High frequency response – BJT Amplifier, High frequency response-FET Amplifier, Multistage Frequency Effects.	**L1, L2, L3**
Module -4	

Feedback and Oscillator Circuits: Feedback concepts, Feedback connection types, Practical feedback circuits, Oscillator operation, FET Phase shift oscillator, Wien bridge oscillator, Tuned Oscillator circuit, Crystal oscillator, UJT construction, UJT Oscillator.	**L1,L2, L3**
Module -5	
Power Amplifiers: Definition and amplifier types, Series fed class A amplifier, Transformer coupled class A amplifier, Class B amplifier operation and circuits, Amplifier distortion, Class C and Class D amplifiers. **Voltage Regulators:** Discrete transistor voltage regulation - Series and Shunt Voltage regulators.	**L1, L2, L3**

Course Outcomes: After studying this course, students will be able to:

- Describe the working principle and characteristics of BJT, FET, Single stage, cascaded and feedback amplifiers.
- Describe the Phase shift, Wien bridge, tuned and crystal oscillators using BJT/FET/UJT.
- Calculate the AC gain and impedance for BJT using re and h parameters models for CE and CC configuration.
- Determine the performance characteristics and parameters of BJT and FET amplifier using small signal model.
- Determine the parameters which affect the low frequency and high frequency responses of BJT and FET amplifiers and draw the characteristics.
- Evaluate the efficiency of Class A and Class B power amplifiers and voltage regulators.

Question paper pattern:

- The question paper will have ten questions.
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Robert L. Boylestad and Louis Nashelsky, “Electronics devices and Circuit theory”, Pearson, 10th/11th Edition, 2012, ISBN:978-81-317-6459-6.

Reference Books:

1. Adel S. Sedra and Kenneth C. Smith, “Micro Electronic Circuits Theory and Application”, 5th Edition ISBN:0198062257
2. Fundamentals of Microelectronics, Behzad Razavi, John Weily ISBN 2013 978-81-265-2307-8
3. J.Millman & C.C.Halkias—Integrated Electronics, 2nd edition, 2010, TMH. ISBN 0-07-462245-5
4. K. A. Navas, “Electronics Lab Manual”, Volume I, PHI, 5th Edition, 2015, ISBN:9788120351424.

DIGITAL ELECTRONICS [As per Choice Based Credit System (CBCS) scheme] SEMESTER – III (EC/TC)			
Subject Code	15EC33	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours per Module)	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to:

- Illustrate simplification of Algebraic equations using Karnaugh Maps and Quine-McClusky Techniques.
- Design combinational logic circuits.
- Design Decoders, Encoders, Digital Multiplexer, Adders, Subtractors and Binary Comparators.
- Describe Latches and Flip-flops, Registers and Counters.
- Analyze Mealy and Moore Models.
- Develop state diagrams Synchronous Sequential Circuits.

Modules	RBT Level
Module – 1	
Principles of combination logic: Definition of combinational logic, canonical forms, Generation of switching equations from truth tables, Karnaugh maps-3,4,5 variables, Incompletely specified functions (Don't care terms) Simplifying Max term equations, Quine-McCluskey minimization technique, Quine-McCluskey using don't care terms, Reduced prime implicants Tables.(Text 1, Chapter 3)	**L1, L2, L3**
Module -2	
Analysis and design of combinational logic: General approach to combinational logic design, Decoders, BCD decoders, Encoders, digital multiplexers, Using multiplexers as Boolean function generators, Adders and subtractors, Cascading full adders, Look ahead carry, Binary comparators.(Text 1, Chapter 4)	**L1, L2, L3**
Module -3	
Flip-Flops: Basic Bistable elements, Latches, Timing considerations, The master-slave flip-flops (pulse-triggered flip-flops): SR flip-flops,JK flip-flops, Edge triggered flip-flops, Characteristic equations. (Text 2, Chapter 6)	**L1,L2**
Module -4	
Simple Flip-Flops Applications: Registers, binary ripple counters, synchronous binary counters, Counters based on shift registers, Design of a synchronous counters, Design of a synchronous mod-n counter using clocked T , JK , D and SR flip-flops. (Text 2, Chapter 6)	**L1,L2, L3**

Module -5	
Sequential Circuit Design: Mealy and Moore models, State machine notation, Synchronous Sequential circuit analysis, Construction of state diagrams, counter design. (Text 1, Chapter 6)	**L1, L2, L3**

Course Outcomes: After studying this course, students will be able to:

- Develop simplified switching equation using Karnaugh Maps and Quine-McClusky techniques.
- Explain the operation of decoders, encoders, multiplexers, demultiplexers, adders, subtractors and comparators.
- Explain the working of Latches and Flip Flops (SR,D,T and JK).
- Design Synchronous/Asynchronous Counters and Shift registers using Flip Flops.
- Develop Mealy/Moore Models and state diagrams for the given clocked sequential circuits.
- Apply the knowledge gained in the design of Counters and Registers.

Question paper pattern:

- The question paper will have ten questions.
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Digital Logic Applications and Design, John M Yarbrough, Thomson Learning, 2001. ISBN 981-240-062-1.
2. Donald D. Givone, “Digital Principles and Design”, McGraw Hill, 2002. ISBN 978-0-07-052906-9.

Reference Books:

1. D. P. Kothari and J. S Dhillon, “Digital Circuits and Design”, Pearson, 2016, ISBN:9789332543539.
2. Morris Mano, “Digital Design”, Prentice Hall of India, Third Edition.
3. Charles H Roth, Jr., “Fundamentals of logic design”, Cengage Learning.
4. K. A. Navas, “Electronics Lab Manual”, Volume I, PHI, 5th Edition, 2015, ISBN: 9788120351424.

NETWORK ANALYSIS [As per Choice Based Credit System (CBCS) scheme] SEMESTER – III (EC/TC)			
Subject Code	15EC34	IA Marks	20
Number	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours per Module)	Exam Hours	03
CREDITS – 04			

Course objectives: This course enables students to:

- Describe basic network concepts emphasizing source transformation, source shifting, mesh and nodal techniques to solve for resistance/impedance, voltage, current and power.
- Explain network Thevenin's, Millman's, Superposition, Reciprocity, Maximum Power transfer and Norton's Theorems and apply them in solving the problems related to Electrical Circuits.
- Explain the behavior of networks subjected to transient conditions.
- Use applications of Laplace transforms to network problems.
- Describe Series and Parallel Combination of Passive Components as resonating circuits, related parameters and to analyze frequency response.
- Study two port network parameters like Z, Y, T and h and their inter-relationships and applications.

Modules	RBT Level
Module -1	
Basic Concepts: Practical sources, Source transformations, Network reduction using Star – Delta transformation, Loop and node analysis with linearly dependent and independent sources for DC and AC networks, Concepts of super node and super mesh.	**L1, L2,L3,L4**
Module -2	
Network Theorems: Superposition, Reciprocity, Millman's theorems, Thevinin's and Norton's theorems, Maximum Power transfer theorem.	**L1, L2, L3,L4**
Module -3	
Transient behavior and initial conditions: Behavior of circuit elements under switching condition and their Representation, evaluation of initial and final conditions in RL, RC and RLC circuits for AC and DC excitations. **Laplace Transformation & Applications**: Solution of networks, step, ramp and impulse responses, waveform Synthesis.	**L1, L2, L3,L4**
Module -4	
Resonant Circuits: Series and parallel resonance, frequency- response of series and Parallel circuits, Q–Factor, Bandwidth.	**L1, L2, L3,L4**
Module -5	

Two port network parameters: Definition of Z, Y, h and Transmission parameters, modeling with these parameters, relationship between parameters sets.	**L1, L2, L3,L4**

Course Outcomes: After studying this course, students will be able to:

- Determine currents and voltages using source transformation/ source shifting/ mesh/ nodal analysis and reduce given network using star-delta transformation/ source transformation/ source shifting.
- Solve network problems by applying Superposition/ Reciprocity/ Thevenin's/ Norton's/ Maximum Power Transfer/ Millman's Network Theorems and electrical laws to reduce circuit complexities and to arrive at feasible solutions.
- Calculate current and voltages for the given circuit under transient conditions.
- Apply Laplace transform to solve the given network.
- Evaluate for RLC elements/ frequency response related parameters like resonant frequency, quality factor, half power frequencies, voltage across inductor and capacitor, current through the RLC elements, in resonant circuits
- Solve the given network using specified two port network parameter like Z or Y or T or h.

Question paper pattern:

- The question paper will have ten questions.
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. M.E. Van Valkenberg (2000), "Network analysis", Prentice Hall of India, 3rd edition, 2000, ISBN: 9780136110958.

2. Roy Choudhury, "Networks and systems", 2nd edition, New Age International Publications, 2006, ISBN: 9788122427677.

Reference Books:

1. Hayt, Kemmerly and Durbin "Engineering Circuit Analysis", TMH 7th Edition, 2010.
2. J. David Irwin /R. Mark Nelms, "Basic Engineering Circuit Analysis", John Wiley, 8thed, 2006.
3. Charles K Alexander and Mathew N O Sadiku, " Fundamentals of Electric Circuits", Tata McGraw-Hill, 3rd Ed, 2009.

<u>**ELECTRONIC INSTRUMENTATION**</u>
[As per Choice Based Credit System (CBCS) scheme]
SEMESTER – III (EC/TC)

Subject Code	15EC35	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours per Module)	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to:

- Define and describe accuracy and precision, types of errors, statistical and probability analysis.
- Describe the operation of Ammeters, Voltmeters, Multimeters and develop circuits for multirange Ammeters and Voltmeters.
- Describe functional concepts and operation of various Analog and Digital measuring instruments.
- Describe basic concepts and operation of Digital Voltmeters and Microprocessor based instruments.
- Describe and discuss functioning and types of Oscilloscopes, Signal generators, AC and DC bridges.
- Recognize and describe significance and working of different types of transducers.

Modules	RBT Level
Module -1 **Measurement and Error:** Definitions, Accuracy, Precision, Resolution and Significant Figures, Types of Errors, Measurement error combinations, Basics of Statistical Analysis. **(Text 2)** **Ammeters:** DC Ammeter, Multirange Ammeter, The Ayrton Shunt or Universal Shunt, Requirements of Shunt, Extending of Ammeter Ranges, RF Ammeter (Thermocouple), Limitations of Thermocouple. **(Text 1)** **Voltmeters and Multimeters:** Introduction, Basic Meter as a DC Voltmeter, DC Voltmeter, Multirange Voltmeter, Extending Voltmeter Ranges, Loading, AC Voltmeter using Rectifiers. Transistor Voltmeter, Differential Voltmeter, True RMS Voltmeter, Considerations in Choosing an Analog Voltmeter, Multimeter. **(Text 1)**	**L1, L2, L3**
Module -2	

Digital Voltmeters: Introduction, RAMP technique, Dual Slope Integrating Type DVM, Integrating Type DVM, Most Commonly used principles of ADC, Successive Approximations, Continuous Balance DVM, $3\frac{1}{2}$-Digit, Resolution and Sensitivity of Digital Meters, General Specifications of DVM, Microprocessor based Ramp type DVM. **(Text 1)** **Digital Instruments:** Introduction, Digital Multimeters, Digital Frequency Meter, Digital Measurement of Time, Universal Counter, Digital Tachometer, Digital pH Meter, Digital Phase Meter, Digital Capacitance Meter, Microprocessor based Instruments. **(Text 1)**	**L1, L2,L3**
Module -3	
Oscilloscopes: Introduction, Basic principles, CRT features, Block diagram of Oscilloscope, Simple CRO, Vertical Amplifier, Horizontal Deflecting System, Sweep or Time Base Generator, Storage Oscilloscope, Digital Readout Oscilloscope, Measurement of Frequency by Lissajous Method, Digital Storage Oscilloscope. **(Text 1)** **Signal Generators:** Introduction, Fixed and Variable AF Oscillator, Standard Signal Generator, Laboratory Type Signal Generator, AF sine and Square Wave Generator, Function Generator, Square and Pulse Generator, Sweep Generator. **(Text 1)**	**L1, L2**
Module -4	
Measuring Instruments: Output Power Meters, Field Strength Meter, Stroboscope, Phase Meter, Vector Impedance Meter, Q Meter, Megger, Analog pH Meter. **(Text 1)** **Bridges:** Introduction, Wheatstone's bridge, Kelvin's Bridge; AC bridges, Capacitance Comparison Bridge, Inductance Comparison Bridge, Maxwell's bridge, Wien's bridge, Wagner's earth connection. **(Text 1)**	**L1, L2,L3**
Module -5	
Transducers: Introduction, Electrical transducers, Selecting a transducer, Resistive transducer, Resistive position transducer, Strain gauges, Resistance thermometer, Thermistor, Inductive transducer, Differential output transducers, LVDT, Piezoelectric transducer, Photoelectric transducer, Photovoltaic transducer, Semiconductor photo diode and transistor, Temperature transducers-RTD. **(Text 1)**	**L1, L2, L3**
Course Outcomes: After studying this course, students will be able to: • Describe instrument measurement errors and calculate them. • Describe the operation of Ammeters, Voltmeters, Multimeters and develop circuits for multirange Ammeters and Voltmeters. • Describe functional concepts and operation of Digital voltmeters and instruments to measure voltage, frequency, time period, phase difference of signals, rotation speed, capacitance and pH of solutions. • Describe functional concepts and operation of various Analog measuring instruments to measure output power, field Strength, impedance, stroboscopic speed, in/out of phase, Q of coils, insulation resistance and pH. • Describe and discuss functioning and types of Oscilloscopes, Signal generators and Transducers. • Utilize AC and DC bridges for passive component and frequency measurements.	

Question paper pattern: • The question paper will have ten questions. • Each full question consists of 16 marks. • There will be 2 full questions (with a maximum of Three sub questions) from each module. • Each full question will have sub questions covering all the topics under a module. • The students will have to answer 5 full questions, selecting one full question from each module.
Text Books: **1.** H. S. Kalsi, "Electronic Instrumentation", McGraw Hill, 3rd Edition, 2012, ISBN:9780070702066. **2.** David A. Bell, "Electronic Instrumentation & Measurements", Oxford University Press PHI 2nd Edition, 2006, ISBN 81-203-2360-2.
Reference Books: 1. A. D. Helfrick and W.D. Cooper, "Modern Electronic Instrumentation and Measuring Techniques", Pearson, 1st Edition, 2015,ISBN:9789332556065. 2. A. K. Sawhney, "Electronics and Electrical Measurements", Dhanpat Rai & Sons. ISBN -81-7700-016-0

ENGINEERING ELECTROMAGNETICS

[As per Choice Based Credit System (CBCS) scheme]

SEMESTER – III (EC/TC)

Subject Code	15EC36	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours per Module)	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to:

- Study the different coordinate systems, Physical signifiance of Divergence, Curl and Gradient.
- Understand the applications of Coulomb's law and Gauss law to different charge distributions and the applications of Laplace's and Poisson's Equations to solve real time problems on capacitance of different charge distributions.
- Understand the physical significance of Biot-Savart's, Amperes's Law and Stokes' theorem for different current distributions.
- Infer the effects of magnetic forces, materials and inductance.
- Know the physical interpretation of Maxwell' equations and applications for Plane waves for their behaviour in different media
- Acquire knowledge of Poynting theorem and its application of power flow.

Modules	**RBT Level**
Module - 1	
Coulomb's Law, Electric Field Intensity and Flux density Experimental law of Coulomb, Electric field intensity, Field due to continuous volume charge distribution, Field of a line charge, Electric flux density.	**L1, L2, L3**
Module -2	
Gauss's law and Divergence Gauss' law, Divergence. Maxwell's First equation (Electrostatics), Vector Operator ▼ and divergence theorem. **Energy, Potential and Conductors** Energy expended in moving a point charge in an electric field, The line integral, Definition of potential difference and potential, The potential field of point charge, Current and Current density, Continuity of current.	**L1, L2, L3**
Module -3	
Poisson's and Laplace's Equations Derivation of Poisson's and Laplace's Equations, Uniqueness theorem, Examples of the solution of Laplace's equation. **Steady Magnetic Field** Biot-Savart Law, Ampere's circuital law, Curl, Stokes' theorem, Magnetic flux and magnetic flux density, Scalar and Vector Magnetic Potentials.	**L1, L2, L3**
Module -4	

Magnetic Forces Force on a moving charge, differential current elements, Force between differential current elements. **Magnetic Materials** Magnetisation and permeability, Magnetic boundary conditions, Magnetic circuit, Potential Energy and forces on magnetic materials.	**L1, L2, L3**
Module -5	
Time-varying fields and Maxwell's equations Farday's law, displacement current, Maxwell's equations in point form, Maxwell's equations in integral form. **Uniform Plane Wave** Wave propagation in free space and good conductors. Poynting's theorem and wave power, Skin Effect.	**L1, L2, L3**

Course Outcomes: After studying this course, students will be able to:

- Evaluate problems on electric field due to point, linear, volume charges by applying conventional methods or by Gauss law.
- Determine potential and energy with respect to point charge and capacitance using Laplace equation.
- Calculate magnetic field, force, and potential energy with respect to magnetic materials.
- Apply Maxwell's equation for time varying fields, EM waves in free space and conductors.
- Evaluate power associated with EM waves using Poynting theorem.

Question paper pattern:

- The question paper will have ten questions.
- Each full question consisting of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

W.H. Hayt and J.A. Buck, "Engineering Electromagnetics", 7th Edition, Tata McGraw-Hill, 2009, ISBN-978-0-07-061223-5.

Reference Books:

1. John Krauss and Daniel A Fleisch, " Electromagnetics with applications", McGraw-Hill.

2. N. Narayana Rao, "Fundamentals of Electromagnetics for Engineering", Pearson.

<table>
<tr><td colspan="4">ANALOG ELECTRONICS LABORATORY
[As per Choice Based Credit System (CBCS) scheme]
SEMESTER – III (EC/TC)</td></tr>
<tr><td>Laboratory Code</td><td>15ECL37</td><td>IA Marks</td><td>20</td></tr>
<tr><td>Number of Lecture Hours/Week</td><td>01Hr Tutorial (Instructions)
+ 02 Hours Laboratory</td><td>Exam Marks</td><td>80</td></tr>
<tr><td>RBT Level</td><td>L1, L2, L3</td><td>Exam Hours</td><td>03</td></tr>
<tr><td colspan="4">CREDITS – 02</td></tr>
<tr><td colspan="4">Course objectives: This laboratory course enables students to get practical experience in design, assembly, testing and evaluation of:
• Rectifiers and Voltage Regulators.
• BJT characteristics and Amplifiers.
• JFET Characteristics and Amplifiers.
• MOSFET Characteristics and Amplifiers
• Power Amplifiers.
• RC-Phase shift, Hartley, Colpitts and Crystal Oscillators.</td></tr>
<tr><td colspan="4">NOTE: The experiments are to be carried using discrete components only.</td></tr>
<tr><td colspan="4">Laboratory Experiments:</td></tr>
<tr><td colspan="4">1. Design and set up the following rectifiers with and without filters and to determine ripple factor and rectifier efficiency:
(a)Full Wave Rectifier (b) Bridge Rectifier</td></tr>
<tr><td colspan="4">2. Conduct experiment to test diode clipping (single/double ended) and clamping circuits (positive/negative).</td></tr>
<tr><td colspan="4">3. Conduct an experiment on Series Voltage Regulator using Zener diode and power transistor to determine line and load regulation characteristics.</td></tr>
<tr><td colspan="4">4. Realize BJT Darlington Emitter follower with and without bootstrapping and determine the gain, input and output impedances.</td></tr>
<tr><td colspan="4">5. Design and set up the BJT common emitter amplifier using voltage divider bias with and without feedback and determine the gain- bandwidth product from its frequency response.</td></tr>
<tr><td colspan="4">6. Plot the transfer and drain characteristics of a JFET and calculate its drain resistance, mutual conductance and amplification factor.</td></tr>
<tr><td colspan="4">7. Design, setup and plot the frequency response of Common Source JFET/MOSFET amplifier and obtain the bandwidth.</td></tr>
</table>

8. Plot the transfer and drain characteristics of n-channel MOSFET and calculate its parameters, namely; drain resistance, mutual conductance and amplification factor.
9. Set-up and study the working of complementary symmetry class B push pull power amplifier and calculate the efficiency.
10. Design and set-up the RC-Phase shift Oscillator using FET, and calculate the frequency of output waveform.
11. Design and set-up the following tuned oscillator circuits using BJT, and determine the frequency of oscillation. (a) Hartley Oscillator (b) Colpitts Oscillator
12. Design and set-up the crystal oscillator and determine the frequency of oscillation.
Course Outcomes: On the completion of this laboratory course, the students will be able to: • Test circuits of rectifiers, clipping circuits, clamping circuits and voltage regulators. • Determine the characteristics of BJT and FET amplifiers and plot its frequency response. • Compute the performance parameters of amplifiers and voltage regulators • Design and test the basic BJT/FET amplifiers, BJT Power amplifier and oscillators.
Conduct of Practical Examination: • All laboratory experiments are to be included for practical examination. • Students are allowed to pick one experiment from the lot. • Strictly follow the instructions as printed on the cover page of answer script for breakup of marks. • Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

DIGITAL ELECTRONICS LABORATORY [As per Choice Based Credit System (CBCS) scheme] SEMESTER – III (EC/TC)			
Laboratory Code	15ECL38	IA Marks	20
Number of Lecture Hours/Week	01Hr Tutorial (Instructions) + 02 Hours Laboratory	Exam Mark	80
RBT Level	L1, L2, L3	Exam Hour	03
CREDITS – 02			

Course objectives: This laboratory course enables students to get practical experience in design, realisation and verification of

- Demorgan's Theorem, SOP, POS forms
- Full/Parallel Adders, Subtractors and Magnitude Comparator
- Multiplexer using logic gates
- Demultiplexers and Decoders
- Flip-Flops, Shift registers and Counters

NOTE:

1. Use discrete components to test and verify the logic gates. The IC umbers given are suggestive. Any equivalent IC can be used.
2. For experiment No. 11 and 12 any open source or licensed simulation tool may be used.

Laboratory Experiments:

1. Verify
   (a) Demorgan's Theorem for 2 variables.
   (b) The sum-of product and product-of-sum expressions using universal gates.
2. Design and implement
   (a) Full Adder using basic logic gates.
   (b) Full subtractor using basic logic gates.
3. Design and implement 4-bit Parallel Adder/ subtractor using IC 7483.
4. Design and Implementation of 4-bit Magnitude Comparator using IC 7485.
5. Realize
   (a) 4:1 Multiplexer using gates.
   (b) 3-variable function using IC 74151(8:1MUX).
6. Realize 1:8 Demux and 3:8 Decoder using IC74138.
7. Realize the following flip-flops using NAND Gates.
   (a) Clocked SR Flip-Flop (b) JK Flip-Flop.
8. Realize the following shift registers using IC7474
   (a) SISO (b) SIPO (c) PISO (d) PIPO.
9. Realize the Ring Counter and Johnson Counter using IC7476.
10. Realize the Mod-N Counter using IC7490.

11. Simulate Full- Adder using simulation tool.
12. Simulate Mod-8 Synchronous UP/DOWN Counter using simulation tool.
Course outcomes: On the completion of this laboratory course, the students will be able to: • Demonstrate the truth table of various expressions and combinational circuits using logic gates. • Design and test various combinational circuits such as adders, subtractors, comparators, multiplexers and demultiplexers. • Construct and test flips-flops, counters and shift registers. • Simulate full adder and up/down counters.
Conduct of Practical Examination: • All laboratory experiments are to be included for practical examination. • Students are allowed to pick one experiment from the lot. • Strictly follow the instructions as printed on the cover page of answer script for breakup of marks. • Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.

B.E E&C FOURTH SEMESTER SYLLABUS

<table>
<tr><td colspan="4" align="center"><u>ENGINEERING MATHEMATICS-IV</u>
B.E., IV Semester, Common to all Branches
[As per Choice Based Credit System (CBCS) scheme]</td></tr>
<tr><td>Subject Code</td><td>15MAT41</td><td>IA Marks</td><td>20</td></tr>
<tr><td>Number of Lecture Hours/Week</td><td>04</td><td>Exam marks</td><td>80</td></tr>
<tr><td>Total Number of Lecture Hours</td><td>50 (10 Hours per Module)</td><td></td><td></td></tr>
<tr><td colspan="4" align="center">Credits – 04</td></tr>
<tr><td colspan="4">Course Objectives: This course will enable students to:
Conversant with numerical methods to solve ordinary differential equations, complex analysis, sampling theory and joint probability distribution and stochastic processes arising in science and engineering.</td></tr>
</table>

Modules	RBT Level
Module-1	
Numerical Methods: Numerical solution of ordinary differential equations of first order and first degree, Taylor's series method, modified Euler's method, Runge - Kutta method of fourth order. Milne's and Adams-Bashforth predictor and corrector methods (No derivations of formulae).	L1, L3
Module-2	
Numerical Methods: Numerical solution of second order ordinary differential equations, Runge-Kutta method and Milne's method. **Special Functions:** Series solution-Frobenious method. Series solution of Bessel's differential equation leading to $J_n(x)$-Bessel's function of first kind. Basic properties and orthogonality. Series solution of Legendre's differential equation leading to $P_n(x)$-Legendre polynomials. Rodrigue's formula, problems.	L3
Module-3	
Complex Variables: Review of a function of a complex variable, limits, continuity, differentiability. Analytic functions-Cauchy-Riemann equations in cartesian and polar forms. Properties and construction of analytic functions. Complex line integrals-Cauchy's theorem and Cauchy's integral formula, Residue, poles, Cauchy's Residue theorem (without proof) and problems. **Transformations:** Conformal transformations, discussion of transformations: $w=z^2$, $w=e^z$, $w=z+(1/z)(z\neq 0)$ and bilinear transformations-problems.	L1, L3, L3
Module-4	
Probability Distributions: Random variables (discrete and continuous), probability mass/density functions. Binomial distribution, Poisson distribution. Exponential and normal distributions, problems.	L3

Joint probability distribution: Joint Probability distribution for two discrete random variables, expectation, covariance, correlation coefficient.	
Module-5	
Sampling Theory: Sampling, Sampling distributions, standard error, test of hypothesis for means and proportions, confidence limits for means, student's t-distribution, Chi-square distribution as a test of goodness of fit. **Stochastic process:** Stochastic processes, probability vector, stochastic matrices, fixed points, regular stochastic matrices, Markov chains, higher transition probability-simple problems.	L3 L1
Course Outcomes: On completion of this course, students are able to: • Solve first and second order ordinary differential equations arising in flow problems using single step and multistep numerical methods. • Understand the analyticity, potential fields, residues and poles of complex potentials in field theory and electromagnetic theory. • Describe conformal and bilinear transformation arising in aerofoil theory, fluid flow visualization and image processing. • Solve problems of quantum mechanics, hydrodynamics and heat conduction by employing Bessel's function relating to cylindrical polar coordinate systems and Legendre's polynomials relating to spherical polar coordinate systems. • Solve problems on probability distributions relating to digital signal processing, information theory and optimization concepts of stability of design and structural engineering. • Draw the validity of the hypothesis proposed for the given sampling distribution in accepting or rejecting the hypothesis. • Determine joint probability distributions and stochastic matrix connected with the multivariable correlation problems for feasible random events. • Define transition probability matrix of a Markov chain and solve problems related to discrete parameter random process.	
Question paper pattern: • The question paper will have ten questions. • Each full Question consisting of 16 marks • There will be 2 full questions (with a maximum of four sub questions) from each module. • Each full question will have sub questions covering all the topics under a module. • The students will have to answer 5 full questions, selecting one full question from each module.	
Text Books: *1. B.S. Grewal: Higher Engineering Mathematics, Khanna Publishers, 43rd Ed., 2015.*	

2. E. Kreyszig: Advanced Engineering Mathematics, John Wiley & Sons,10th Ed., 2015.	
Reference Books: *1. N.P.Bali and Manish Goyal: A Text Book of Engineering Mathematics, Laxmi Publishers,7th Ed., 2010.* *2. B.V.Ramana: "Higher Engineering Mathematics" Tata McGraw-Hill, 2006.* *3. H. K. Dass and Er. Rajnish Verma: "Higher Engineering Mathematics", S. Chand publishing, 1st edition, 2011.*	
Web Link and Video Lectures: *1. http://nptel.ac.in/courses.php?disciplineID=111* **2.** *http://www.khanacademy.org/* **3.** *http://www.class-central.com/subject/math*	

ADDITIONAL MATHEMATICS - II

B.E., IV Semester, Common to all Branches

(A Bridge course for Lateral Entry students of IV Sem. B. E.)

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15MATDIP41	IA Marks	--
Number of Lecture Hours/Week	03	Exam marks	80
Total Number of Lecture Hours	40 (08 Hours per Module)		

Credits – 00

Course Objectives: This course will enable students to:

- Understand essential concepts of linear algebra.
- Solve second and higher order differential equations.
- Understand Laplace and inverse Laplace transforms and elementary probability theory.

Modules	RBT Level
Module-1	
Linear Algebra: Introduction - rank of matrix by elementary row operations - Echelon form. Consistency of system of linear equations - Gauss elimination method. Eigen values and Eigen vectors of a square matrix. Application of Cayley-Hamilton theorem (without proof) to compute the inverse of a matrix-Examples.	**L1,L3**
Module-2	
Higher order ODE's: Linear differential equations of second and higher order equations with constant coefficients. Homogeneous /non-homogeneous equations. Inverse differential operators. Solutions of initial value problems. Method of undetermined coefficients and variation of parameters.	**L1,L3**
Module-3	
Laplace transforms: Laplace transforms of elementary functions. Transforms of derivatives and integrals, transforms of periodic function and unit step function-Problems only.	**L1,L2**
Module-4	
Inverse Laplace transforms: Definition of inverse Laplace transforms. Evaluation of Inverse transforms by standard methods. Application to solutions of Linear differential equations and simultaneous differential equations.	**L1,L2**
Module-5	
Probability: Introduction. Sample space and events. Axioms of probability. Addition and multiplication theorems. Conditional probability – illustrative examples. Bayes's theorem-examples.	**L1,L2**

Course Outcomes: On completion of this course, students are able to:

- Solve systems of linear equations in the different areas of linear algebra.
- Solve second and higher order differential equations occurring in of electrical circuits, damped/un-damped vibrations.

• Describe Laplace transforms of standard and periodic functions. • Determine the general/complete solutions to linear ODE using inverse Laplace transforms. • Recall basic concepts of elementary probability theory and, solve problems related to the decision theory, synthesis and optimization of digital circuits.	
Question paper pattern: • The question paper will have ten questions. • Each full Question consisting of 16 marks • There will be 2 full questions (with a maximum of four sub questions) from each module. • Each full question will have sub questions covering all the topics under a module. • The students will have to answer 5 full questions, selecting one full question from each module.	
Text Book: *B.S. Grewal: Higher Engineering Mathematics, Khanna Publishers, 43rd Ed., 2015.*	
Reference Books: *1. E. Kreyszig: Advanced Engineering Mathematics, John Wiley & Sons, 10th Ed., 2015.* *2. N.P.Bali and Manish Goyal: A Text Book of Engineering Mathematics, Laxmi Publishers,7th Ed., 2007.*	

MICROPROCESSORS

[As per Choice Based Credit System (CBCS) scheme]

SEMESTER – IV (EC/TC)

Subject Code	15EC42	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours per Module)	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to:

- Familiarize basic architecture of 8086 microprocessor
- Program 8086 Microprocessor using Assembly Level Language
- Use Macros and Procedures in 8086 Programs
- Understand interfacing of 16 bit microprocessor with memory and peripheral chips involving system design
- Understand the architecture of 8088, 8087 Coprocessor and other CPU architectures

Modules	**RBT Level**
Module -1	
8086 PROCESSOR: Historical background (refer Reference Book 1), 8086 CPU Architecture (1.1 – 1.3 of Text). Addressing modes, Machine language instruction formats, Machine coding the program (2.2, 2.1, 3.2 of Text). **INSTRUCTION SET OF 8086:** Data transfer and arithmetic instructions. Control/Branch Instructions, Illustration of these instructions with example programs (2.3 of Text).	**L1, L2, L3**
Module -2	
Logical Instructions, String manipulation instructions, Flag manipulation and Processor control instructions, Illustration of these instructions with example programs. Assembler Directives and Operators, Assembly Language Programming and example programs (2.3, 2.4, 3.4 of Text).	**L1, L2, L3**
Module -3	
Stack and Interrupts: Introduction to stack, Stack structure of 8086, Programming for Stack. Interrupts and Interrupt Service routines, Interrupt cycle of 8086, NMI, INTR, Interrupt programming, Passing parameters to procedures, Macros, Timing and Delays. (Chap. 4 of Text).	**L1, L2, L3**
Module -4	

8086 Bus Configuration and Timings: Physical memory Organization, General Bus operation cycle, I/O addressing capability, Special processor activities, Minimum mode 8086 system and Timing diagrams, Maximum Mode 8086 system and Timing diagrams. (1.4 to 1.9 of Text). **Basic Peripherals and their Interfacing with 8086 (Part 1)**: Static RAM Interfacing with 8086 (5.1.1), Interfacing I/O ports, PIO 8255, Modes of operation – Mode-0 and BSR Mode, Interfacing Keyboard and 7-Segment digits using 8255 (Refer 5.3, 5.4, 5.5 of Text).	**L1, L2, L3**
Module 5	
Basic Peripherals and their Interfacing with 8086 (Part 2): Interfacing ADC-0808/0809, DAC-0800, Stepper Motor using 8255 (5.6.1, 5.7.2, 5.8). Timer 8254 – Mode 0, 1, 2 & 3 and Interfacing programmes for these modes (refer 6.1 of Text). **INT 21H DOS Function calls** - for handling Keyboard and Display (refer Appendix-B of Text). **Other Architectures:** Architecture of 8088 (refer 1.10 upto 1.10.1 of Text) and Architecture of NDP 8087 (refer 8.3.1, 8.3.5 of Text). Von-Neumann & Harvard CPU architecture and CISC & RISC CPU architecture (refer Reference Book 1).	**L1, L2, L3**

Course Outcomes: At the end of the course students will be able to:

- Explain the History of evaluation of Microprocessors, Architecture and instruction set of 8086, 8088, 8087, CISC & RISC, Von-Neumann & Harvard CPU Architecture, Configuration & Timing diagrams of 8086 and Instruction set of 8086.
- Write8086 Assembly level programs using the 8086 instruction set
- Write modular programs using procedures and macros.
- Write 8086 Stack and Interrupts programming
- Interface 8086 to Static memory chips and 8255, 8254, 0808 ADC, 0800 DAC, Keyboard, Display and Stepper motors.
- Use INT 21 DOS interrupt function calls to handle Keyboard and Display.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book: **Advanced Microprocessors and Peripherals** - A.K. Ray and K.M. Bhurchandi, TMH, 3rd Edition, 2012, ISBN 978-1-25-900613-5.
Reference Books: 1. **Microprocessor and Interfacing**- Douglas V Hall, SSSP Rao, 3rd edition TMH, 2012. 2. **Microcomputer systems-The 8086 / 8088 Family** – Y.C. Liu and A. Gibson, 2nd edition, PHI -2003. 3. **The 8086 Microprocessor: Programming & Interfacing the PC** – Kenneth J Ayala, CENGAGE Learning, 2011. 4. **The Intel Microprocessor, Architecture, Programming and Interfacing** - Barry B. Brey, 6e, Pearson Education / PHI, 2003.

CONTROL SYSTEMS [As per Choice Based Credit System (CBCS) scheme] SEMESTER – IV (EC/TC)			
Subject Code	15EC43	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50(10 Hours per Module)	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to:

- Understand the basic features, configurations and application of control systems.
- Understand various terminologies and definitions for the control systems.
- Learn how to find a mathematical model of electrical, mechanical and electro-mechanical systems.
- Know how to find time response from the transfer function.
- Find the transfer function via Masons' rule.
- Analyze the stability of a system from the transfer function.

Modules	**RBT Level**
Module -1	
Introduction to Control Systems: Types of Control Systems, Effect of Feedback Systems, Differential equation of Physical Systems – Mechanical Systems, Electrical Systems, Analogous Systems. Block diagrams and signal flow graphs: Transfer functions, Block diagram algebra and Signal Flow graphs.	**L1, L2, L3**
Module -2	
Time Response of feedback control systems: Standard test signals, Unit step response of First and Second order Systems. Time response specifications, Time response specifications of second order systems, steady state errors and error constants. Introduction to PI, PD and PID Controllers (excluding design).	**L1, L2, L3**
Module -3	
Stability analysis: Concepts of stability, Necessary conditions for Stability, Routh stability criterion, Relative stability analysis: more on the Routh stability criterion, Introduction to Root-Locus Techniques, The root locus concepts, Construction of root loci.	**L1, L2, L3**
Module -4	

Frequency domain analysis and stability: Correlation between time and frequency response, Bode Plots, Experimental determination of transfer function. Introduction to Polar Plots, (Inverse Polar Plots excluded) Mathematical preliminaries, Nyquist Stability criterion, (Systems with transportation lag excluded) Introduction to lead, lag and lead-lag compensating networks (excluding design).	**L1, L2, L3**
Module -5	
Introduction to Digital Control System: Introduction, Spectrum Analysis of Sampling process, Signal reconstruction, Difference equations. Introduction to State variable analysis: Introduction, Concept of State, State variables & State model, State model for Linear Continuous & Discrete time systems, Diaganolisation.	**L1, L2, L3**

Course Outcomes: At the end of the course, the students will be able to

- Develop the mathematical model of mechanical and electrical systems
- Develop transfer function for a given control system using block diagram reduction techniques and signal flow graph method
- Determine the time domain specifications for first and second order systems
- Determine the stability of a system in the time domain using Routh-Hurwitz criterion and Root-locus technique.
- Determine the stability of a system in the frequency domain using Nyquist and bode plots
- Develop a control system model in continuous and discrete time using state variable techniques

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

J.Nagarath and M.Gopal, " Control Systems Engineering", New Age International (P) Limited, Publishers, Fifth edition-2005, ISBN: 81-224-2008-7.

Reference Books:

1. "Modern Control Engineering," K.Ogata, Pearson Education Asia/PHI, 4th Edition, 2002. ISBN 978-81-203-4010-7.
2. "Automatic Control Systems", Benjamin C. Kuo, John Wiley India Pvt. Ltd., 8th Edition, 2008.
3. "Feedback and Control System," Joseph J Distefano III et al., Schaum's Outlines, TMH, 2nd Edition 2007.

SIGNALS AND SYSTEMS [As per Choice Based Credit System (CBCS) scheme] **SEMESTER – IV (EC/TC)**			
Subject Code	15EC44	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50(10 Hours per Module)	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to:

- Understand the mathematical description of continuous and discrete time signals and systems.
- Analyze the signals in time domain using convolution difference/differential equations
- Classify signals into different categories based on their properties.
- Analyze Linear Time Invariant (LTI) systems in time and transform domains.
- Build basics for understanding of courses such as signal processing, control system and communication.

Modules	RBT Level
Module -1	
Introduction and Classification of signals: Definition of signal and systems, communication and control systems as examples. Sampling of analog signals, Continuous time and discrete time signal, Classification of signals as even, odd, periodic and non-periodic, deterministic and non-deterministic, energy and power. **Elementary signals/Functions:** Exponential, sine, impulse, step and its properties, ramp, rectangular, triangular, signum, sync functions. **Operations on signals:** Amplitude scaling, addition, multiplication, differentiation, integration (Accumulator for DT), time scaling, time shifting and time folding. **Systems:** Definition, Classification: linear and non-linear, time variant and invariant, causal and non- causal, static and dynamic, stable and unstable, invertible.	**L1, L2, L3**
Module -2	
Time domain representation of LTI System: System modeling: Input-output relation, definition of impulse response, convolution sum, convolution integral, computation of convolution integral and convolution sum using graphical method for unit step to unit step, unit step to exponential, exponential to exponential, unit step to rectangular and rectangular to rectangular only. Properties of convolution.	**L1, L2, L3**
Module -3	

System interconnection, system properties in terms of impulse response, step response in terms of impulse response (4 Hours). **Fourier Representation of Periodic Signals**: Introduction to CTFS and DTFS, definition, properties (No derivation) and basic problems (inverse Fourier series is excluded) (06 Hours).	**L1, L2, L3**
Module -4	
Fourier Representation of aperiodic Signals: **FT representation of aperiodic CT signals - FT,** definition, FT of standard CT signals, Properties and their significance (4 Hours). **FT representation of aperiodic discrete signals-DTFT**, definition, DTFT of standard discrete signals, Properties and their significance (4 Hours). **Impulse sampling and reconstruction:** Sampling theorem (only statement) and reconstruction of signals (2 Hours).	**L1, L2, L3**
Module -5	
Z-Transforms: Introduction, the Z-transform, properties of the Region of convergence, Properties of the Z-Transform, Inversion of the Z-Transform, Transform analysis of LTI systems.	**L1, L2, L3**

Course Outcomes: At the end of the course, students will be able to:

- Classify the signals as continuous/discrete, periodic/aperiodic, even/odd, energy/power and deterministic/random signals.
- Determine the linearity, causality, time-invariance and stability properties of continuous and discrete time systems.
- Compute the response of a Continuous and Discrete LTI system using convolution integral and convolution sum.
- Determine the spectral characteristics of continuous and discrete time signal using Fourier analysis.
- Compute Z-transforms, inverse Z- transforms and transfer functions of complex LTI systems.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Simon Haykins and Barry Van Veen, “Signals and Systems”, 2nd Edition, 2008, WileyIndia. ISBN 9971-51-239-4.

Reference Books:

1. **Michael Roberts,** "Fundamentals of Signals & Systems", 2nd edition, Tata McGraw-Hill, 2010, ISBN 978-0-07-070221-9.
2. **Alan V Oppenheim, Alan S, Willsky and A Hamid Nawab,** "Signals and Systems" Pearson Education Asia / PHI, 2nd edition, 1997. Indian Reprint 2002.
3. **H. P Hsu, R. Ranjan,** "Signals and Systems", Scham's outlines, TMH, 2006.
4. **B. P. Lathi,** "Linear Systems and Signals", Oxford University Press, 2005.
5. **Ganesh Rao and Satish Tunga,** "Signals and Systems", Pearson/Sanguine Technical Publishers, 2004.

PRINCIPLES OF COMMUNICATION SYSTEMS
[As per Choice Based Credit System (CBCS) scheme]
SEMESTER – IV (EC/TC)

Subject Code	15EC45	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50(10 Hours per Module)	Exam Hours	03

CREDITS – 04

Course objectives: This course will enable students to:

- Design simple systems for generating and demodulating AM, DSB, SSB and VSB signals.
- Understand the concepts in Angle modulation for the design of communication systems.
- Design simple systems for generating and demodulating frequency modulated signals.
- Learn the concepts of random process and various types of noise.
- Evaluate the performance of the communication system in presence of noise.
- Analyze pulse modulation and sampling techniques.

Modules	RBT Level
Module – 1	
AMPLITUDE MODULATION: Introduction, Amplitude Modulation: Time & Frequency – Domain description, Switching modulator, Envelop detector. **DOUBLE SIDE BAND-SUPPRESSED CARRIER MODULATION:** Time and Frequency – Domain description, Ring modulator, Coherent detection, Costas Receiver, Quadrature Carrier Multiplexing. **SINGLE SIDE–BAND AND VESTIGIAL SIDEBAND METHODS OF MODULATION:** SSB Modulation, VSB Modulation, Frequency Translation, Frequency- Division Multiplexing, Theme Example: VSB Transmission of Analog and Digital Television. (Chapter 3 of Text).	**L1, L2, L3**
Module – 2	
ANGLE MODULATION: Basic definitions, Frequency Modulation: Narrow Band FM, Wide Band FM, Transmission bandwidth of FM Signals, Generation of FM Signals, Demodulation of FM Signals, FM Stereo Multiplexing, Phase–Locked Loop: Nonlinear model of PLL, Linear model of PLL, Nonlinear Effects in FM Systems. The Superheterodyne Receiver (refer Chapter 4 of Text).	**L1, L2, L3**
Module – 3	

RANDOM VARIABLES & PROCESS: Introduction, Probability, Conditional Probability, Random variables, Several Random Variables. Statistical Averages: Function of a random variable, Moments, Random Processes, Mean, Correlation and Covariance function: Properties of autocorrelation function, Cross–correlation functions (refer Chapter 5 of Text). **NOISE**: Shot Noise, Thermal noise, White Noise, Noise Equivalent Bandwidth (refer Chapter 5 of Text), Noise Figure (refer Section 6.7 of Text).	**L1, L2, L3**
Module – 4	
NOISE IN ANALOG MODULATION: Introduction, Receiver Model, Noise in DSB-SC receivers, Noise in AM receivers, Threshold effect, Noise in FM receivers, Capture effect, FM threshold effect, FM threshold reduction, Pre-emphasis and De-emphasis in FM (refer Chapter 6 of Text).	**L1, L2, L3**
Module – 5	
DIGITAL REPRESENTATION OF ANALOG SIGNALS: Introduction, Why Digitize Analog Sources?, The Sampling process, Pulse Amplitude Modulation, Time Division Multiplexing, Pulse-Position Modulation, Generation of PPM Waves, Detection of PPM Waves, The Quantization Process, Quantization Noise, Pulse–Code Modulation: Sampling, Quantization, Encoding, Regeneration, Decoding, Filtering, Multiplexing (refer Chapter 7 of Text), Application to Vocoder (refer Section 6.8 of Reference Book 1).	**L1, L2, L3**

Course Outcomes: At the end of the course, students will be able to:

- Determine the performance of analog modulation schemes in time and frequency domains.
- Determine the performance of systems for generation and detection of modulated analog signals.
- Characterize analog signals in time domain as random processes and in frequency domain using Fourier transforms.
- Characterize the influence of channel on analog modulated signals
- Determine the performance of analog communication systems.
- Understand the characteristics of pulse amplitude modulation, pulse position modulation and pulse code modulation systems.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Communication Systems, Simon Haykins & Moher, 5th Edition, John Willey, India Pvt. Ltd, 2010, ISBN 978 – 81 – 265 – 2151 – 7.

Reference Books:

1. **Modern Digital and Analog Communication Systems,** B. P. Lathi, Oxford University Press., 4th edition.
2. **An Introduction to Analog and Digital Communication**, Simon Haykins, John Wiley India Pvt. Ltd., 2008, ISBN 978–81–265–3653–5.
3. **Principles of Communication Systems**, H.Taub & D.L.Schilling, TMH, 2011.
4. **Communication Systems**, Harold P.E, Stern Samy and A.Mahmond, Pearson Edition, 2004.
5. **Communication Systems**: **Analog and Digital,** R.P.Singh and S.Sapre: TMH 2nd edition, 2007.

LINEAR INTEGRATED CIRCUITS [As per Choice Based Credit System (CBCS) scheme] SEMESTER – IV (EC/TC)			
Subject Code	15EC46	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50(10 Hours per Module)	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to:

- Define and describe various parameters of Op-Amp, its characteristics and specifications.
- Discuss the effects of Input and Output voltage ranges upon Op-Amp circuits.
- Sketch and Analyze Op-Amp circuits to determine Input Impedances, output Impedances and other performance parameters.
- Sketch and Explain typical Frequency Response graphs for each of the Filter circuits showing Butterworth and Chebyshev responses where ever appropriate.
- Describe and Sketch the various switching circuits of Op-Amps and analyze its operations.
- Differentiate between various types of DACs and ADCs and evaluate the performance of each with neat circuit diagrams and assuming suitable inputs.

Modules	RBT Level
Module -1	
Operational Amplifier Fundamentals: Basic Op-amp circuit, Op-Amp parameters – Input and output voltage, CMRR and PSRR, offset voltages and currents, Input and output impedances, Slew rate and Frequency limitations. **OP-Amps as DC Amplifiers** – Biasing OP-amps, Direct coupled voltage followers, Non-inverting amplifiers, inverting amplifiers, Summing amplifiers, and Difference amplifiers. Interpretation of OP-amp LM741 & TL081 datasheet.**(Text1)**	**L1, L2,L3**
Module -2	
Op-Amps as AC Amplifiers: Capacitor coupled voltage follower, High input impedance – Capacitor coupled voltage follower, Capacitor coupled non inverting amplifiers, High input impedance – Capacitor coupled Non inverting amplifiers, Capacitor coupled inverting amplifiers, setting the upper cut-off frequency, Capacitor coupled difference amplifier. **OP-Amp Applications:** Voltage sources, current sources and current sinks, current amplifiers, instrumentation amplifier, precision rectifiers.**(Text1)**	**L1, L2,L3**
Module-3	
More Applications : Limiting circuits, Clamping circuits, Peak detectors, Sample and hold circuits, V to I and I to V converters, Differentiating Circuit, Integrator Circuit, Phase shift oscillator, Wien bridge oscillator, Crossing detectors**,** inverting Schmitt trigger. **(Text 1)** Log and antilog amplifiers, Multiplier and divider. **(Text2)**	**L1, L2,L3**

Module -4	
Active Filters: First order and second order active Low-pass and high pass filters, Bandpass Filter, Bandstop Filter. **(Text 1)** **Voltage Regulators:** Introduction, Series Op-amp regulator, IC voltage regulators. 723 general purpose regulators. **(Text 2)**	**L1, L2,L3**
Module -5	
Phase locked loop: Basic Principles, Phase detector/comparator, VCO. **DAC and ADC convertor**: DAC using R-2R, ADC using Successive approximation. **Other IC Application:** 555 timer, Basic timer circuit, 555 timer used as astable and monostable multivibrator. **(Text 2)**	**L1, L2,L3**

Course Outcomes: After studying this course, students will be able to:

- Explain Op-Amp circuit and parameters including CMRR, PSRR, Input & Output Impedances and Slew Rate.
- Design Op-Amp based Inverting, Non-inverting, Summing & Difference Amplifier, and AC Amplifiers including Voltage Follower.
- Test circuits of Op-Amp based Voltage/ Current Sources & Sinks, Current, Instrumentation and Precision Amplifiers.
- Test circuits of Op-Amp based linear and non-linear circuits comprising of limiting, clamping, Sample & Hold, Differentiator/ Integrator Circuits, Peak Detectors, Oscillators and Multiplier & Divider.
- Design first & second order Low Pass, High Pass, Band Pass, Band Stop Filters and Voltage Regulators using Op-Amps.
- Explain applications of linear ICs in phase detector, VCO, DAC, ADC and Timer.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. "Operational Amplifiers and Linear IC's", David A. Bell, 2nd edition, PHI/Pearson, 2004. ISBN 978-81-203-2359-9.

2. "Linear Integrated Circuits", D. Roy Choudhury and Shail B. Jain, 4th edition, Reprint 2006, New Age International ISBN 978-81-224-3098-1.

Reference Books:

1. Ramakant A Gayakwad, “Op-Amps and Linear Integrated Circuits”, Pearson, 4th Ed, 2015. ISBN 81-7808-501-1.
2. B Somanathan Nair, “Linear Integrated Circuits: Analysis, Design & Applications,” Wiley India, 1st Edition, 2015.
3. James Cox, “Linear Electronics Circuits and Devices”, Cengage Learning, Indian Edition, 2008, ISBN-13: 978-07-668-3018-7.
4. Data Sheet: http://www.ti.com/lit/ds/symlink/tl081.pdf.

MICROPROCESSOR LABORATORY

[As per Choice Based Credit System (CBCS) scheme]

SEMESTER – IV (EC/TC)

Laboratory Code	15ECL47	IA Marks	20
Number of Lecture Hours/Week	01Hr Tutorial (Instructions) + 02 Hours Laboratory	Exam Marks	80
RBT Level	L1, L2, L3	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to:

- Get familiarize with 8086 instructions and DOS 21H interrupts and function calls.
- Develop and test assembly language programs to use instructions of 8086.
- Get familiarize with interfacing of various peripheral devices with 8086 microprocessor for simple applications.

Laboratory Experiments:

1. Programs involving:

Data transfer instructions like:

i) Byte and word data transfer in different addressing Modes
ii) Block move (with and without overlap)
iii) Block interchange

2. Programs involving:

Arithmetic & logical operations like:

i) Addition and Subtraction of multi precision nos.
ii) Multiplication and Division of signed and unsigned Hexadecimal nos.
iii) ASCII adjustment instructions.
iv) Code conversions.

3. Programs involving:

Bit manipulation instructions like checking:

i) Whether given data is positive or negative
ii) Whether given data is odd or even
iii) Logical 1's and 0's in a given data
iv) 2 out 5 code
v) Bit wise and nibble wise palindrome

4. Programs involving:

Branch/ Loop instructions like

i) Arrays: addition/subtraction of N nos., Finding largest and smallest nos., Ascending and descending order.
ii) Two application programs using Procedures and Macros (Subroutines).

5. Programs involving

String manipulation like string transfer, string reversing, searching for a string.

6. Programs involving

Programs to use DOS interrupt INT 21h Function calls for Reading a Character from keyboard, Buffered Keyboard input, Display of character/ String on console.

7. Interfacing Experiments:

Experiments on interfacing 8086 with the following interfacing modules through DIO (Digital Input/Output - PCI bus compatible card / 8086 Trainer)

1. Matrix keyboard interfacing
2. Seven segment display interface
3. Logical controller interface
4. Stepper motor interface
5. ADC and DAC Interface (8 bit)
6. Light dependent resistor (LDR), Relay and Buzzer Interface to make light operated switches

Course Outcomes: On the completion of this laboratory course, the students will be able to:

- Write and execute 8086 assembly level programs to perform data transfer, arithmetic and logical operations.
- Understand assembler directives, branch, loop operations and DOS 21H Interrupts.
- Write and execute 8086 assembly level programs to sort and search elements in a given array.
- Perform string transfer, string reversing, searching a character in a string with string manipulation instructions of 8086.
- Utilize procedures and macros in programming 8086.
- Demonstrate the interfacing of 8086 with 7 segment display, matrix keyboard, logical controller, stepper motor, ADC, DAC, and LDR for simple applications.

Conduct of Practical Examination:

- All laboratory experiments are to be included for practical examination.
- For examination, one question from software and one question from hardware interfacing to be set.
- Students are allowed to pick one experiment from the lot.
- Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

LINEAR ICS AND COMMUNICATION LAB

As per Choice Based Credit System (CBCS) scheme]

SEMESTER – IV (EC/TC)

Laboratory Code	15ECL48	IA Marks	20
Number of Lecture Hours/Week	01Hr Tutorial (Instructions) + 02 Hours Laboratory	Exam Marks	80
RBT Level	L1, L2, L3	Exam Hours	03
CREDITS – 02			

Course objectives: This laboratory course enables students to:

- Design, Demonstrate and Analyze instrumentation amplifier, filters, DAC, adder, differentiator and integrator circuits, using op-amp.
- Design, Demonstrate and Analyze multivibrators and oscillator circuits using Op-amp
- Design, Demonstrate and Analyze analog systems for AM, FM and Mixer operations.
- Design, Demonstrate and Analyze balance modulation and frequency synthesis.
- Demonstrate and Analyze pulse sampling and flat top sampling.

Laboratory Experiments:

1. Design an instrumentation amplifier of a differential mode gain of 'A' using three amplifiers.
2. Design of RC Phase shift and Wien's bridge oscillators using Op-amp.
3. Design active second order Butterworth low pass and high pass filters.
4. Design 4 bit R – 2R Op-Amp Digital to Analog Converter (i) using 4 bit binary input from toggle switches and (ii) by generating digital inputs using mod-16 counter.
5. Design Adder, Integrator and Differentiator using Op-Amp.
6. Design of Monostable and Astable Multivibrator using 555 Timer.
7. Demonstrate Pulse sampling, flat top sampling and reconstruction.
8. Amplitude modulation using transistor/FET (Generation and detection).
9. Frequency modulation using IC 8038/2206 and demodulation.
10. Design BJT/FET Mixer.
11. DSBSC generation using Balance Modulator IC 1496/1596.
12. Frequency synthesis using PLL.

Course Outcomes: This laboratory course enables students to: • Illustrate the pulse and flat top sampling techniques using basic circuits. • Demonstrate addition and integration using linear ICs, and 555 timer operations to generate signals/pulses. • Demonstrate AM and FM operations and frequency synthesis. • Design and illustrate the operation of instrumentation amplifier, LPF, HPF, DAC and oscillators using linear IC.
Conduct of Practical Examination: • All laboratory experiments are to be included for practical examination. • Students are allowed to pick one experiment from the lot. • Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

B.E E&C FIFTH SEMESTER SYLLABUS

MANAGEMENT AND ENTREPRENEURSHIP DEVELOPMENT

B.E., V Semester, EC/TC/EI/BM/ML

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15ES51	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours / Module)	Exam Hours	03
CREDITS – 04			

Course Objectives: This course will enable students to:

- Understand basic skills of Management
- Understand the need for Entrepreneurs and their skills
- Understand Project identification and Selection
- Identify the Management functions and Social responsibilities
- Distinguish between management and administration

Module-1	RBT Level
Management: Nature and Functions of Management – Importance, Definition, Management Functions, Levels of Management, Roles of Manager, Managerial Skills, Management & Administration, Management as a Science, Art & Profession (Selected topics of Chapter 1, Text 1). **Planning:** Planning**-**Nature, Importance, Types, Steps and Limitations of Planning; Decision Making – Meaning, Types and Steps in Decision Making(Selected topics from Chapters 4 & 5, Text 1).	L1, L2
Module-2	
Organizing and Staffing: Organization-Meaning, Characteristics, Process of Organizing, Principles of Organizing, Span of Management (meaning and importance only), Departmentalisation, Committees–Meaning, Types of Committees; Centralization Vs Decentralization of Authority and Responsibility; **Staffing**-Need and Importance**,** Recruitment and Selection Process (Selected topics from Chapters 7, 8 & 11,Text 1). **Directing and Controlling:** Meaning and Requirements of Effective Direction, Giving Orders; Motivation-Nature of Motivation, Motivation Theories (Maslow's Need-Hierarchy Theory and Herzberg's Two Factor Theory); Communication – Meaning, Importance and Purposes of Communication; Leadership-Meaning, Characteristics, Behavioural Approach of Leadership; Coordination-Meaning, Types, Techniques of Coordination; Controlling – Meaning, Need for Control System, Benefits of Control, Essentials of Effective Control System, Steps in Control Process (Selected topics from Chapters 15 to 18 and 9, Text 1).	L1, L2
Module-3	
Social Responsibilities of Business: Meaning of Social Responsibility, Social Responsibilities of Business towards Different Groups, Social Audit, Business Ethics and Corporate Governance (Selected topics from Chapter 3, Text 1).	L1, L2

Entrepreneurship: Definition of Entrepreneur, Importance of Entrepreneurship, concepts of Entrepreneurship, Characteristics of successful Entrepreneur, Classification of Entrepreneurs, Myths of Entrepreneurship, Entrepreneurial Development models, Entrepreneurial development cycle, Problems faced by Entrepreneurs and capacity building for Entrepreneurship (Selected topics from Chapter 2, Text 2).	
Module-4	
Modern Small Business Enterprises: Role of Small Scale Industries, Impact of Globalization and WTO on SSIs, Concepts and definitions of SSI Enterprises, Government policy and development of the Small Scale sector in India, Growth and Performance of Small Scale Industries in India, Sickness in SSI sector, Problems for Small Scale Industries, Ancillary Industry and Tiny Industry (Definition only)(Selected topics from Chapter1, Text 2). **Institutional Support for Business Enterprises:** Introduction, Policies & Schemes of Central Level Institutions, State Level Institutions (Selected topics from Chapter 4, Text 2).	L1, L2
Module-5	
Projects Management: AProject. Search for a Business idea**:** Introduction, Choosing an Idea, Selection of product, The Adoption process, Product Innovation, Product Planning and Development Strategy, Product Planning and Development Process. Concepts of Projects and Classification**:** Introduction, Meaning of Projects, Characteristics of a Project, Project Levels, Project Classification, Aspects of a Project, The project Cycle, Features and Phases of Project management, Project Management Processes. Project Identification**:** Feasibility Report, Project Feasibility Analysis. Project Formulation**:** Meaning, Steps in Project formulation, Sequential Stages of Project Formulation, Project Evaluation. **Project Design and Network Analysis:** Introduction, Importance of Network Analysis, Origin of PERT and CPM, Network, Network Techniques, Need for Network Techniques, Steps in PERT, CPM, Advantages, Limitations and Differences. (Selected topics from Chapters 16 to 20 of Unit 3, Text 3).	L1, L2, L3

Course Outcomes: After studying this course, students will be able to:

- Understand the fundamental concepts of Management and Entrepreneurship
- Select a best Entrepreneurship model for the required domain of establishment
- Describe the functions of Managers, Entrepreneurs and their social responsibilities
- Compare various types of Entrepreneurs
- Analyze the Institutional support by various state and central government agencies

Question paper pattern

- The question paper will have TEN questions.
- Each full question carries 16 marks.
- There will be two full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books: 1. Principles of Management – P.C Tripathi, P.N Reddy, McGraw Hill Education, 6th Edition, 2017. ISBN-13:978-93-5260-535-4. **2.** Entrepreneurship Development Small Business Enterprises- Poornima M Charantimath, Pearson Education 2008, ISBN 978-81-7758-260-4. **3.** Dynamics of Entrepreneurial Development and Management by Vasant Desai. HPH 2007, ISBN: 978-81-8488-801-2.
Reference Book: Essentials of Management: An International, Innovation and Leadership perspective by Harold Koontz, Heinz Weihrich McGraw Hill Education, 10th Edition 2016. ISBN- 978-93-392-2286-4.

DIGITAL SIGNAL PROCESSING

B.E., V Semester, Electronics & Communication Engineering / Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC52	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours / Module)	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to

- Understand the frequency domain sampling and reconstruction of discrete time signals.
- Study the properties and the development of efficient algorithms for the computation of DFT.
- Realization of FIR and IIR filters in different structural forms.
- Learn the procedures to design of IIR filters from the analog filters using impulse invariance and bilinear transformation.
- Study the different windows used in the design of FIR filters and design appropriate filters based on the specifications.

Modules

Module-1	**RBT Level**
Discrete Fourier Transforms (DFT): Frequency domain sampling and reconstruction of discrete time signals. DFT as a linear transformation, its relationship with other transforms. Properties of DFT, multiplication of two DFTs- the circular convolution.	L1, L2
Module-2	
Additional DFT properties, use of DFT in linear filtering, overlap-save and overlap-add method. Fast-Fourier-Transform (FFT) algorithms: Direct computation of DFT, need for efficient computation of the DFT (FFT algorithms).	L1, L2, L3
Module-3	
Radix-2 FFT algorithm for the computation of DFT and IDFT–decimation-in-time and decimation-in-frequency algorithms. Goertzel algorithm, and chirp-z transform.	L1, L2, L3
Module-4	
Structure for IIR Systems: Direct form, Cascade form, Parallel form structures. IIR filter design: Characteristics of commonly used analog filter – Butterworth and Chebyshev filters, analog to analog frequency transformations. Design of IIR Filters from analog filter using Butterworth filter: Impulse invariance, Bilinear transformation.	L1, L2, L3
Module-5	
Structure for FIR Systems: Direct form, Linear Phase, Frequency sampling	L1, L2,

structure, Lattice structure. FIR filter design: Introduction to FIR filters, design of FIR filters using - Rectangular, Hamming, Hanning and Bartlett windows.	L3

Course Outcomes: After studying this course, students will be able to:

- Determine response of LTI systems using time domain and DFT techniques.
- Compute DFT of real and complex discrete time signals.
- Computation of DFT using FFT algorithms and linear filtering approach.
- Solve problems on digital filter design and realize using digital computations.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks**.**
- There will be 2 full questions (with a maximum of three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Digital signal processing – Principles Algorithms & Applications, Proakis & Monalakis, Pearson education, 4th Edition, New Delhi, 2007.

Reference Books:

1. Discrete Time Signal Processing, Oppenheim & Schaffer, PHI, 2003.
2. Digital Signal Processing, S. K. Mitra, Tata Mc-Graw Hill, 3rd Edition, 2010.
3. Digital Signal Processing, Lee Tan: Elsevier publications, 2007.

Verilog HDL

B.E., V Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC53	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours / Module)	Exam Hours	03
CREDITS – 04			

Course objectives: This course will enable students to:

- Differentiate between Verilog and VHDL descriptions.
- Learn different Verilog HDL and VHDL constructs.
- Familiarize the different levels of abstraction in Verilog.
- Understand Verilog Tasks and Directives.
- Understand timing and delay Simulation.
- Learn VHDL at design levels of data flow, behavioral and structural for effective modeling of digital circuits.

Module-1	RBT Level
Overview of Digital Design with Verilog HDL Evolution of CAD, emergence of HDLs, typical HDL-flow, why Verilog HDL?, trends in HDLs. (Text1) **Hierarchical Modeling Concepts** Top-down and bottom-up design methodology, differences between modules and module instances, parts of a simulation, design block, stimulus block. (Text1)	L1, L2, L3
Module-2	
Basic Concepts Lexical conventions, data types, system tasks, compiler directives. (Text1) **Modules and Ports** Module definition, port declaration, connecting ports, hierarchical name referencing. (Text1)	L1, L2, L3
Module-3	
Gate-Level Modeling Modeling using basic Verilog gate primitives, description of and/or and buf/not type gates, rise, fall and turn-off delays, min, max, and typical delays. (Text1) **Dataflow Modeling** Continuous assignments, delay specification, expressions, operators, operands, operator types. (Text1)	L1, L2, L3
Module-4	
Behavioral Modeling Structured procedures, initial and always, blocking and non-blocking	L1, L2, L3

statements, delay control, generate statement, event control, conditional statements, Multiway branching, loops, sequential and parallel blocks. (Text1)	
Module-5	
Introduction to VHDL **Introduction:** Why use VHDL?, Shortcomings, Using VHDL for Design Synthesis, Design tool flow, Font conventions. **Entities and Architectures:** Introduction, A simple design, Design entities, Identifiers, Data objects, Data types, and Attributes. (Text 2)	L1, L2, L3

Course Outcomes: At the end of this course, students should be able to

- Write Verilog programs in gate, dataflow (RTL), behavioral and switch modeling levels of Abstraction.
- Write simple programs in VHDL in different styles.
- Design and verify the functionality of digital circuit/system using test benches.
- Identify the suitable Abstraction level for a particular digital design.
- Write the programs more effectively using Verilog tasks and directives.
- Perform timing and delay Simulation.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks**.**
- There will be 2 full questions (with a maximum of three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Books:

1. Samir Palnitkar, "**Verilog HDL: A Guide to Digital Design and Synthesis"**, Pearson Education, Second Edition.

2. Kevin Skahill, "**VHDL for Programmable Logic**", PHI/Pearson education, 2006.

Reference Books:

1. Donald E. Thomas, Philip R. Moorby, "The Verilog Hardware Description Language", Springer Science+Business Media, LLC, Fifth edition.
2. Michael D. Ciletti, "Advanced Digital Design with the Verilog HDL" Pearson (Prentice Hall), Second edition.
3. Padmanabhan, Tripura Sundari, "Design through Verilog HDL", Wiley, 2016 or earlier.

INFORMATION THEORY AND CODING

B.E., V Semester, Electronics & Communication Engineering / Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC54	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours / Module)	Exam Hours	03
CREDITS – 04			

Course Objectives: This course will enable students to:

- Understand the concept of Entropy, Rate of information and order of the source with reference to dependent and independent source.
- Study various source encoding algorithms.
- Model discrete & continuous communication channels.
- Study various error control coding algorithms.

Modules

Module-1	RBT Level
Information Theory: Introduction, Measure of information, Information content of message, Average Information content of symbols in Long Independent sequences, Average Information content of symbols in Long dependent sequences, Markov Statistical Model of Information Sources, Entropy and Information rate of Markoff Sources (Section 4.1, 4.2 of Text 1).	L1, L2, L3
Module-2	
Source Coding: Source coding theorem, Prefix Codes, Kraft McMillan Inequality property – KMI (Section 2.2 of Text 2). Encoding of the Source Output, Shannon's Encoding Algorithm (Sections 4.3, 4.3.1 of Text 1). Shannon Fano Encoding Algorithm, Huffman codes, Extended Huffman coding, Arithmetic Coding, Lempel – Ziv Algorithm (Sections 3.6, 3.7, 3.8, 3.10 of Text 3).	L1, L2, L3
Module-3	
Information Channels: Communication Channels (Section 4.4 of Text 1). Channel Models, Channel Matrix, Joint probabilty Matrix, Binary Symmetric Channel, System Entropies, Mutual Information, Channel Capacity, Channel Capacity of : Binary Symmetric Channel, Binary Erasure Channel, Muroga,s Theorem, Contineuos Channels (Sections 4.2, 4.3, 4.4, 4.6, 4.7 of Text 3).	L1, L2, L3
Module-4	

Error Control Coding: Introduction, Examples of Error control coding, methods of Controlling Errors, Types of Errors, types of Codes, Linear Block Codes: matrix description of Linear Block Codes, Error Detection and Error Correction Capabilities of Linear Block Codes, Single Error Correcting hamming Codes, Table lookup Decoding using Standard Array. **Binary Cyclic Codes:** Algebraic Structure of Cyclic Codes, Encoding using an (n-k) Bit Shift register, Syndrome Calculation, Error Detection and Correction (Sections 9.1, 9.2, 9.3, 9.3.1, 9.3.2, 9.3.3 of Text 1).	L1, L2, L3
Module-5	
Some Important Cyclic Codes: Golay Codes, BCH Codes(Section 8.4 – Article 5 of Text 2). **Convolution Codes**: Convolution Encoder, Time domain approach, Transform domain approach, Code Tree, Trellis and State Diagram, The Viterbi Algorithm) (Section 8.5 – Articles 1,2 and 3, 8.6- Article 1 of Text 2).	L1, L2, L3

Course Outcomes: At the end of the course the students will be able to:

- Explain concept of Dependent & Independent Source, measure of information, Entropy, Rate of Information and Order of a source
- Represent the information using Shannon Encoding, Shannon Fano, Prefix and Huffman Encoding Algorithms
- Model the continuous and discrete communication channels using input, output and joint probabilities
- Determine a codeword comprising of the check bits computed using Linear Block codes, cyclic codes & convolutional codes
- Design the encoding and decoding circuits for Linear Block codes, cyclic codes, convolutional codes, BCH and Golay codes.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks**.**
- There will be 2 full questions (with a maximum of three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Books:

1. Digital and analog communication systems, K. Sam Shanmugam, John Wiley India Pvt. Ltd, 1996.
2. Digital communication, Simon Haykin, John Wiley India Pvt. Ltd, 2008.
3. Information Theory and Coding, Muralidhar Kulkarni, K.S. Shivaprakasha, Wiley India Pvt. Ltd, 2015, ISBN:978-81-265-5305-1.

Reference Books:

1. ITC and Cryptography, Ranjan Bose, TMH, II edition, 2007
2. Principles of digital communication, J. Das, S. K. Mullick, P. K. Chatterjee, Wiley, 1986 - Technology & Engineering

3. Digital Communications – Fundamentals and Applications, Bernard Sklar, Second Edition, Pearson Education, 2016, ISBN: 9780134724058.
4. Information Theory and Coding, K.N.Haribhat, D.Ganesh Rao, Cengage Learning, 2017.

NANOELECTRONICS

B.E., V Semester, Electronics & Communication Engineering / Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC551	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to:

- Enhance basic engineering science and technical knowledge of nanoelectronics.
- Explain basics of top-down and bottom-up fabrication process, devices and systems.
- Describe technologies involved in modern day electronic devices.
- Know various nanostructures of carbon and the nature of the carbon bond itself.
- Learn the photo physical properties of sensor used in generating a signal.

Module-1	RBT Level
Introduction: Overview of nanoscience and engineering. Development milestones in microfabrication and electronic industry. Moore's law and continued miniaturization, Classification of Nanostructures, Electronic properties of atoms and solids: Isolated atom, Bonding between atoms, Giant molecular solids, Free electron models and energy bands, crystalline solids, Periodicity of crystal lattices, Electronic conduction, effects of nanometerlength scale, Fabrication methods: Top down processes, Bottom up processes methods for templating the growth of nanomaterials, ordering of nanosystems (Text 1).	L1, L2
Module-2	
Characterization: Classification, Microscopic techniques, Field ion microscopy, scanning probe techniques, diffraction techniques: bulk and surface diffraction techniques (Text 1). **Inorganic semiconductor nanostructures:** overview of semiconductor physics. Quantum confinement in semiconductor nanostructures: quantum wells, quantum wires, quantum dots, super-lattices, band offsets, electronic density of states (Text 1).	L1, L2
Module-3	
Fabrication techniques: requirements of ideal semiconductor, epitaxial growth of quantum wells, lithography and etching, cleaved-edge over growth, growth of vicinal substrates, strain induced dots and wires, electrostatically induced dots and wires, Quantum well width fluctuations, thermally annealed quantum wells, semiconductor nanocrystals, collidal quantum dots, self-assembly techniques.(Text 1). **Physical processes:** modulation doping, quantum hall effect, resonant tunneling, charging effects, ballistic carrier transport, Inter band absorption, intraband absorption, Light emission processes, phonon bottleneck, quantum confined stark effect, nonlinear effects, coherence and dephasing, characterization of semiconductor nanostructures: optical	L1, L2

electrical and structural (Text 1).	
Module-4	
Carbon Nanostructures: Carbon molecules, Carbon Clusters, Carbon Nanotubes, application of Carbon Nanotubes. (Text 2)	L1, L2
Module-5	
Nanosensors: Introduction, What is Sensor and Nanosensors?, What makes them Possible?, Order From Chaos, Characterization, Perception, Nanosensors Based On Quantum Size Effects, Electrochemical Sensors, Sensors Based On Physical Properties, Nanobiosensors, Smart dust Sensor for the future. (Text 3) **Applications:** Injection lasers, quantum cascade lasers, single-photon sources, biological tagging, optical memories, coulomb blockade devices, photonic structures, QWIP's, NEMS, MEMS (Text 1).	L1, L2
Course outcomes: After studying this course, students will be able to: • Know the principles behind Nanoscience engineering and Nanoelectronics. • Know the effect of particles size on mechanical, thermal, optical and electrical properties of nanomaterials. • Know the properties of carbon and carbon nanotubes and its applications. • Know the properties used for sensing and the use of smart dust sensors. • Apply the knowledge to prepare and characterize nanomaterials. • Analyse the process flow required to fabricate state-of-the-art transistor technology.	

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks**.**
- There will be 2 full questions (with a maximum of three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Books:

1. Ed Robert Kelsall, Ian Hamley, Mark Geoghegan, "Nanoscale Science and Technology", John Wiley, 2007.
2. Charles P Poole, Jr, Frank J Owens, "Introduction to Nanotechnology", John Wiley, Copyright 2006, Reprint 2011.
3. T Pradeep, "Nano: The essentials-Understanding Nanoscience and Nanotechnology", TMH.

Reference Book:

Ed William A Goddard III, Donald W Brenner, Sergey E. Lyshevski, Gerald J Iafrate, "Hand Book of Nanoscience Engineering and Technology", CRC press, 2003.

SWITCHING & FINITE AUTOMATA THEORY

B.E., V Semester, Electronics & Communication Engineering / Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC552	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to:

1. Understand the basics of threshold logic, effect of hazards on digital circuits and techniques of fault detection
2. Explain finite state model and minimization techniques
3. Know structure of sequential machines, and state identification
4. Understand the concept of fault detection experiments

Modules

Module-1	RBT Level
Threshold Logic: Introductory Concepts: Threshold element, capabilities and limitations of threshold logic, Elementary Properties, Synthesis of Threshold networks: Unate functions, Identification and realization of threshold functions, The map as a tool in synthesizing threshold networks. (Sections 7.1, 7.2 of Text)	L1, L2, L3
Module-2	
Reliable Design and Fault Diagnosis: Hazards, static hazards, Design of Hazard-free Switching Circuits, Fault detection in combinational circuits, Fault detection in combinational circuits: The faults, The Fault Table, Covering the fault table, Fault location experiments: Preset experiments, Adaptive experiments, Boolean differences, Fault detection by path sensitizing. (Sections 8.1, 8.2, 8.3, 8.4, 8.5 of Text)	L1, L2, L3
Module-3	
Sequential Machines: Capabilities, Minimization and Transformation The Finite state model and definitions, capabilities and limitations of finite state machines, State equivalence and machine minimization: k-equivalence, The minimization Procedure, Machine equivalence, Simplification of incompletely specified machines. (Section 10.1, 10.2, 10.3, 10.4 of Text)	L1, L2, L3
Module-4	
Structure of Sequential Machines: Introductory example, State assignment using partitions: closed partitions, The lattice of closed partitions, Reduction of output dependency, Input dependence and autonomous clocks, Covers and generation of closed partitions by state splitting: Covers, The implication graph, An application of state splitting to parallel decomposition. (Section 12.1, 12.2, 12.3, 12.4, 12.5, 12.6 of Text)	L1, L2, L3
Module-5	
State–Identification and Fault Detection Experiments: Experiments, Homing experiments, Distinguishing experiments, Machine identification,	L1, L2, L3

Fault detection experiments, Design of diagnosable machines, Second algorithm for the design of fault detection experiments. (Sections 13.1, 13.2, 13.3, 13.4, 13.5, 13.6, 13.7 of Text)	

Course outcomes: At the end of the course, students should be able to:

- Explain the concept of threshold logic
- Understand the effect of hazards on digital circuits and fault detection and analysis
- Define the concepts of finite state model
- Analyze the structure of sequential machine
- Explain methods of state identification and fault detection experiments

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Book:

Switching and Finite Automata Theory – Zvi Kohavi, McGraw Hill, 2nd edition, 2010 ISBN: 0070993874.

Reference Books:

1. **Fault Tolerant And Fault Testable Hardware Design**-Parag K Lala, Prentice Hall Inc. 1985.
2. **Digital Circuits and Logic Design**.-Charles Roth Jr, Larry L. Kinney, Cengage Learning, 2014, ISBN: 978-1-133-62847-7.

OPERATING SYSTEM

B.E., V Semester, Electronics & Communication Engineering / Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC553	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course objectives: This course will enable students to:

- Understand the services provided by an operating system.
- Understand how processes are synchronized and scheduled.
- Understand different approaches of memory management and virtual memory management.
- Understand the structure and organization of the file system
- Understand interprocess communication and deadlock situations.

Module-1	RBT Level
Introduction to Operating Systems OS, Goals of an OS, Operation of an OS, Computational Structures, Resource allocation techniques, Efficiency, System Performance and User Convenience, Classes operating System, Batch processing, Multi programming, Time Sharing Systems, Real Time and distributed Operating Systems (Topics from Sections 1.2, 1.3, 2.2 to 2.8 of Text).	L1, L2
Module-2	
Process Management: OS View of Processes, PCB, Fundamental State Transitions, Threads, Kernel and User level Threads, Non-preemptive scheduling- FCFS and SRN, Preemptive Scheduling- RR and LCN, Long term, medium term and short term scheduling in a time sharing system (Topics from Sections 3.3, 3.3.1 to 3.3.4, 3.4, 3.4.1, 3.4.2 , 4.2, 4.3, 4.4.1 of Text).	L1, L2
Module-3	
Memory Management: Contiguous Memory allocation, Non-Contiguos Memory Allocation, Paging, Segmentation, Segmentation with paging, Virtual Memory Management, Demand Paging, Paging Hardware, VM handler, FIFO, LRU page replacement policies (Topics from Sections 5.5 to 5.9, 6.1 to 6.3, except Optimal policy and 6.3.1of Text).	L1, L2
Module-4	
File Systems: File systems and IOCS, File Operations, File Organizations, Directory structures, File Protection, Interface between File system and IOCS, Allocation of disk space, Implementing file access (Topics from Sections 7.1 to 7.8 of Text).	L1, L2, L3
Module-5	
Message Passing and Deadlocks: Overview of Message Passing, Implementing message passing, Mailboxes, Deadlocks, Deadlocks in resource allocation, Resource state modelling, Deadlock detection algorithm, Deadlock Prevention (Topics from Sections 10.1 to 10.3, 11.1 to	L1, L2, L3

11.5 of Text).	

Course outcomes: After studying this course, students will be able to:

- Explain the goals, structure, operation and types of operating systems.
- Apply scheduling techniques to find performance factors.
- Explain organization of file systems and IOCS.
- Apply suitable techniques for contiguous and non-contiguous memory allocation.
- Describe message passing, deadlock detection and prevention methods.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks**.**
- There will be 2 full questions (with a maximum of three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Book:

Operating Systems – A concept based approach, by Dhamdare, TMH, 2nd edition.

Reference Books:

1. Operating systems concepts, Silberschatz and Galvin, John Wiley India Pvt. Ltd, 5th edition,2001.
2. Operating system–internals and design system, William Stalling, Pearson Education, 4th ed, 2006.
3. Design of operating systems, Tannanbhaum, TMH, 2001.

ELECTRICAL ENGINEERING MATERIALS

B.E., V Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC554	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours/Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to:

- Understand the formation of bands in materials and the classification of materials on the basis of band theory
- Understand the classification of magnetic materials on the basis of their behavior in an external magnetizing field.
- Understand the characteristics and properties of conducting and superconducting materials
- Understand the electrical characteristics of the material to be considered on the basis of their uses.
- Classify electrical engineering materials into low and high resistance materials

Modules

Module-1	RBT Level
Band Theory of Solids: Introduction to free electron theory, Kroning-Penney Model, Explanation for Discontinuities in E vs. K curve, Formation of Solid Material, Formation of Band in Metals, Formation of Bands in Semiconductors and Insulating Materials, Classification of Materials on the Basis of Band Structure, Explanation for differences in the Electrical properties of different Materials. Important Characteristics of a Band Electron, Number of energy states per band, Explanation for Insulating and Metallic Behavior of Materials, Concept of Hole.	L1, L2
Module-2	
Magnetic Properties of Materials: Introduction, Origin of Magnetism, Basic Terms in Magnetism, Relation between Magnetic Permeability and Susceptibility, Classification of magnetic Materials, Characteristics of Diamagnetic Materials, Paramagnetic Materials, Ferromagnetic Materials, Ferrimagnetic Materials, Langevin's Theory of Diamagnetism, Explanation of Dia, Para and Ferromagnetism, Ampere's Lam in Dia, Para and Ferromagnetism, Hystersis and Hystersis loss, Langevin's Theory of paramagnetism, Modification in the Langevin's Theory, Anti-Ferromagnetism and Neel Temperature, Ferrimagnetic Materials, Properties of some important Magnetic Materials, Magentostriction and Magnetostrictive Materials, Hard and Soft Ferromagnetic Materials and their Applications.	L1, L2
Module-3	
Behavior of Dielectric Materials in AC and DC Fields: Introduction, Classification of Dielectric Materials at Microscopic level, Polar Dielectric Materials, Non-polar Dielectric Materials, Kinds of Polarizations, behavior of	L1, L2

dielectric materials, Three electric Vectors, Gauss's Law in a Dielectric, Electric Susceptibility and Static Dielectric constant, Effect of Dielectric medium upon capacitance, macroscopic electric field, Microscopic Electric field, temperature dependence of dielectric constant, polar dielectric in ac and dc fields, behavior of polar dielectric at high frequencies, Dielectric loss, Dielectric strength and Dielectric Breakdown, Various kinds of Dielectric Materials, Hysteresis in Ferroelectric Materials, Applications of Ferroelectric Materials in Devices.	
Module-4	
Conductivity of Metals and Superconductivity: Introduction, Ohm's law, Explanation for the dependence of electrical resistivity upon temperature, Free-electron theory of metals, Application of Lorentz-Drude free-electron theory, Effect of various parameters on Electrical Conductivity, Resistivity Ratio, Variation of resistivity of alloys with temperature, Thermal Conductivity of Materials, Heat produced in Current Carrying Conductor, Thermoelectric Effect, Thermoelectric Series, Seebeck's Experiment. Discovery of superconductivity, superconductivity and transition temperature, superconducting materials, explanation of superconductivity phenomenon, characteristics of superconductors, change in thermodynamic parameters in superconducting state, frequency dependence of superconductivity, current status of high temperature superconductors, practical applications of superconductors.	L1, L2
Module-5	
Electrical Conducting and Insulating materials: Introduction, Classification of conducting materials, difference in properties of Hard-Drawn and Annealed copper, standard conductors, comparison between some popular Low-Resistivity Materials, Low-Resistivity Copper Alloys, Electrical contact materials and their selection, classification of contact materials, Materials for Lamp Filaments, Preparation of Tungsten Filaments. Insulating gases, Liquids and solids and their characteristics, Selection of the insulating material, other important properties of Insulating materials, Thermal characteristics, chemical properties of Insulating materials, classification of Insulating materials on the basis of structure.	L1, L2

Course Outcomes: At the end of the course, students will be able to

- Understand the various kinds of materials and their applications in ac and dc fields.
- Understand the conductivity of superconductivity of materials.
- Explain the electrical properties of different materials and metallic behavior of materials on the basis of band theory.
- Explain the properties and applications of all kind of magnetic materials.
- Explain the properties of electrical conducting and insulating materials.
- Assess a variety of approaches in developing new materials with enhanced performance to replace existing materials.

Question paper pattern:

- The question paper will have ten questions

- Each full question consists of 16 marks**.**
- There will be 2 full questions (with a maximum of three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Book:

R K Shukla and Archana Singh, “Electrical Engineering Materials” McGraw Hill, 2012, ISBN: 978-1-25-90062-03.

Reference Books:

1. S.O. KASAP, “Electronic Materials and Devices” 3rd edition, McGraw Hill, 2014, ISBN-978-0-07-064820-3.
2. C.S.Indulkar and S. Thiruvengadam, S., “An Introduction to Electrical Engineering Materials”, ISBN-9788121906661.

MSP430 MICROCONTROLLER

B.E., V Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC555	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course objectives: This course will enable students to:

- Understand the architectural features and instruction set of 16 bit microcontroller MSP430.
- Program MSP430 using the various instructions for different applications.
- Understand the functions of the various peripherals which are interfaced with MSP430.
- Describe the power saving modes in MSP430.
- Explain the low power applications using MSP430.

Module-1	RBT Level
MSP430 Architecture: Introduction –Where does the MSP430 fit, The outside view, The inside view-Functional block diagram, Memory, Central Processing Unit, Memory Mapped Input and Output, Clock Generator, Exceptions: Interrupts and Resets, MSP430 family. (Text: Ch1- 1.3 to 1.7, Ch2- 2.1 to 2.7, Ch5- 5.1, 5.7 up to 5.7.1)	L1, L2
Module-2	
Addressing Modes & Instruction Set-Addressing Modes, Instruction set**,** Constant Generator and Emulated Instructions, Program Examples. (Text: Ch5- 5.2 to 5.5)	L1, L2, L3
Module-3	
Clock System, Interrupts and Operating Modes-Clock System, Interrupts, What happens when an interrupted is requested, Interrupt Service Routines, Low Power Modes of Operation, Watchdog Timer, Basic Timer1, Real Time Clock, Timer-A: Timer Block, Capture/Compare Channels, Interrupts from Timer-A. (Text: Ch5 - 5.8 upto 5.8.4, Ch 6-6.6 to 6.8, 6.10, Ch8 -8.1, 8.2, 8.3)	L1, L2
Module-4	
Analog Input-Output and PWM - Comparator-A, ADC10, ADC12, Sigma-Delta ADC, Internal Operational Amplifiers, DAC, Edge Aligned PWM, Simple PWM, Design of PWM. LCD interfacing. (Text: Ch9 – 9.1 up to 9.1.2, 9.4, 9.5 up to 9.5.1, 9.7, 9.8 up to 9.8.1, 9.11.5, 9.12 (without 9.12.1), 8.6.2 to 8.6.4)	L1, L2
Module-5	

<table>
<tr><td>Digital Input-Output and Serial Communication:
Parallel Ports, Lighting LEDs, Flashing LEDs, Read Input from a Switch, Toggle the LED state by pressing the push button, LCD interfacing. Asynchronous Serial Communication, Asynchronous Communication with USCI_A, Communications, Peripherals in MSP430, Serial Peripheral Interface.
(Text: Selected topics from Ch4 & Ch7 and Ch7- 7.1, Ch10 – 10.1, 10.2, and 10.12)</td><td>L1, L2, L3</td></tr>
<tr><td>Course outcomes: After studying this course, students will be able to:
Understand the architectural features and instruction set of 16 bit microcontroller MSP430.
Develop programs using the various instructions of MSP430 for different applications.
Understand the functions of the various peripherals which are interfaced with MSP430 microcontroller.
Describe the power saving modes in MSP430.
Explain the low power applications using MSP430 microcontroller.</td><td></td></tr>
<tr><td colspan="2">Evaluation of Internal Assessment Marks:
It is suggested that at least a few simple programs to be executed by students using any evaluation board of MSP430 for better understanding of the course. This activity can be considered for the evaluation of 5 marks out of 20 Internal assessment marks, reserved for the other activities.</td></tr>
<tr><td colspan="2">Question paper pattern:
The question paper will have ten questions
Each full question consists of 16 marks.
There will be 2 full questions (with a maximum of three sub questions) from each module.
Each full question will have sub questions covering all the topics under a module
The students will have to answer 5 full questions, selecting one full question from each module</td></tr>
<tr><td colspan="2">Text Book:
John H Davies, MSP430 Microcontroller Basics, Newnes Publications, Elsevier, 2008.</td></tr>
<tr><td colspan="2">References:
Chris Nagy, Embedded Systems Design using TI MSP430 Series, Newnes Publications, Elsevier, 2003.
User Guide from Texas Instruments.</td></tr>
</table>

DSP Lab

B.E., V Semester, EC/TC

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15ECL57	IA Marks	20
Number of Lecture Hours/Week	01Hr Tutorial (Instructions) + 02 Hours Laboratory=03	Exam Marks	80
RBT Levels	L1, L2, L3	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to

- Simulate discrete time signals and verification of sampling theorem.
- Compute the DFT for a discrete signal and verification of its properties using MATLAB.
- Find solution to the difference equations and computation of convolution and correlation along with the verification of properties.
- Compute and display the filtering operations and compare with the theoretical values.
- Implement the DSP computations on DSP hardware and verify the result.

Laboratory Experiments

Following Experiments to be done using MATLAB / SCILAB / OCTAVE or equivalent:

1. Verification of sampling theorem.
2. Linear and circular convolution of two given sequences, Commutative, distributive and associative property of convolution.
3. Auto and cross correlation of two sequences and verification of their properties
4. Solving a given difference equation.
5. Computation of N point DFT of a given sequence and to plot magnitude and phase spectrum (using DFT equation and verify it by built-in routine).
6. (i) Verification of DFT properties (like Linearity and Parseval's theorem, etc.)
   (ii) DFT computation of square pulse and Sinc function etc.
7. Design and implementation of FIR filter to meet given specifications (using different window techniques).
8. Design and implementation of IIR filter to meet given specifications.

Following Experiments to be done using DSP kit

9. Linear convolution of two sequences
10. Circular convolution of two sequences
11. N-point DFT of a given sequence
12. Impulse response of first order and second order system
13. Implementation of FIR filter

Course outcomes: On the completion of this laboratory course, the students will be able to:

- Understand the concepts of analog to digital conversion of signals and frequency domain sampling of signals.

• Modelling of discrete time signals and systems and verification of its properties and results. • Implementation of discrete computations using DSP processor and verify the results. • Realize the digital filters using a simulation tool and a DSP processor and verify the frequency and phase response.
Conduct of Practical Examination: 1. All laboratory experiments are to be included for practical examination. 2. Strictly follow the instructions as printed on the cover page of answer script for breakup of marks. **3.** Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

HDL Lab
B.E., V Semester, EC/TC
[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15ECL58	IA Marks	20
Number of Lecture Hours/Week	01 Hr Tutorial (Instructions) + 02 Hours Laboratory = 03	Exam Marks	80
RBT Levels	L1, L2, L3	Exam Hours	03

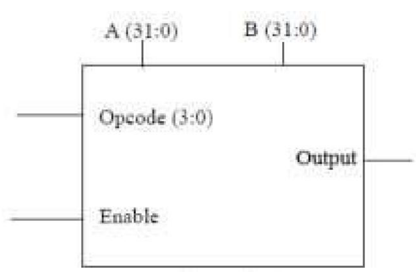
CREDITS – 02

Course objectives: This course will enable students to:
- Familiarize with the CAD tool to write HDL programs.
- Understand simulation and synthesis of digital design.
- Program FPGAs/CPLDs to synthesise the digital designs.
- Interface hardware to programmable ICs through I/O ports.
- Choose either Verilog or VHDL for a given Abstraction level.

Note: Programming can be done using any compiler. Download the programs on a FPGA/CPLD boards such as Apex/Acex/Max/Spartan/Sinfi or equivalent and performance testing may be done using 32 channel pattern generator and logic analyzer apart from verification by simulation with tools such as Altera/Modelsim or equivalent.

Laboratory Experiments

Part–A: PROGRAMMING
1. Write Verilog code to realize all the logic gates
2. Write a Verilog program for the following combinational designs
   a. 2 to 4 decoder
   b. 8 to 3 (encoder without priority & with priority)
   c. 8 to 1 multiplexer.
   d. 4 bit binary to gray converter
   e. Multiplexer, de-multiplexer, comparator.
3. Write a VHDL and Verilog code to describe the functions of a Full Adder using three modeling styles.
4. Write a Verilog code to model 32 bit ALU using the schematic diagram shown below



- ALU should use combinational logic to calculate an output based on the four bit op-code input.
- ALU should pass the result to the out bus when enable line in high, and tri-state the out bus when the enable line is low.

- ALU should decode the 4 bit op-code according to the example given below.

OPCODE	ALU Operation
1.	A+B
2.	A-B
3.	A Complement
4.	A*B
5.	A AND B
6.	A OR B
7.	A NAND B
8.	A XOR B

5. Develop the Verilog code for the following flip-flops, SR, D, JK and T.
6. Design a 4 bit binary, BCD counters (Synchronous reset and Asynchronous reset) and "any sequence" counters, using Verilog code.

Part–B: INTERFACING (at least four of the following must be covered using VHDL/Verilog)

1. Write HDL code to display messages on an alpha numeric LCD display.
2. Write HDL code to interface Hex key pad and display the key code on seven segment display.
3. Write HDL code to control speed, direction of DC and Stepper motor.
4. Write HDL code to accept Analog signal, Temperature sensor and display the data on LCD or Seven segment display.
5. Write HDL code to generate different waveforms (Sine, Square, Triangle, Ramp etc.,) using DAC - change the frequency.
6. Write HDL code to simulate Elevator operation.

Course Outcomes: At the end of this course, students should be able to:

- Write the Verilog/VHDL programs to simulate Combinational circuits in Dataflow, Behavioral and Gate level Abstractions.
- Describe sequential circuits like flip flops and counters in Behavioral description and obtain simulation waveforms.
- Synthesize Combinational and Sequential circuits on programmable ICs and test the hardware.
- Interface the hardware to the programmable chips and obtain the required output.

Conduct of Practical Examination:

1. All laboratory experiments are to be included for practical examination.
2. Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.
3. Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

5th Semester Open Electives Syllabus for the Courses offered by EC/TC Board

Automotive Electronics
B.E V Semester (Open Elective)
[As per Choice Based Credit System (CBCS) scheme

Subject Code	15EC561	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40(08 Hrs per Module)	Exam Hours	03
CREDITS – 03			

Course objectives: This course will enable students to:

- Understand the basics of automobile dynamics and design electronics to complement those features.
- Design and implement the electronics that attribute the reliability, safety, and smartness to the automobiles, providing add-on comforts.

Module-1	RBT Level
Automotive Fundamentals Overview – Evolution of Automotive Electronics, Automobile Physical Configuration, Survey of Major Automotive Systems, The Engine – Engine Block, Cylinder Head, Four Stroke Cycle, Engine Control, Ignition System - Spark plug, High voltage circuit and distribution, Spark pulse generation, Ignition Timing, Diesel Engine, Drive Train - Transmission, Drive Shaft, Differential, Suspension, Brakes, Steering System (Text 1: Chapter1), Starter Battery –Operating principle: (Text 2: Pg. 407-410) (4 hours) **The Basics of Electronic Engine Control –** Motivation for Electronic Engine Control – Exhaust Emissions, Fuel Economy, Concept of an Electronic Engine control system, Definition of General terms, Definition of Engine performance terms, Engine mapping, Effect of Air/Fuel ratio, spark timing and EGR on performance, Control Strategy, Electronic Fuel control system, Analysis of intake manifold pressure, Electronic Ignition. (Text 1: Chapter 5) (4 hours)	L1, L2
Module-2	

Automotive Control System applications of Sensors and Actuators – Typical Electronic Engine Control System, Variables to be measured (Text 1: Chapter 6) (1 hour) **Automotive Sensors** – Airflow rate sensor, Strain Gauge MAP sensor, Engine Crankshaft Angular Position Sensor, Magnetic Reluctance Position Sensor, Hall effect Position Sensor, Shielded Field Sensor, Optical Crankshaft Position Sensor, Throttle Angle Sensor (TAS), Engine Coolant Temperature (ECT) Sensor, Exhaust Gas Oxygen (O2/EGO) Lambda Sensors, Piezoelectric Knock Sensor. (Text 1: Chapter 6) (5 hours) **Automotive Actuators –** Solenoid, Fuel Injector, EGR Actuator, Ignition System (Text 1: Chapter 6) (2 hours)	L1, L2
Module-3	
Digital Engine Control Systems – Digital Engine control features, Control modes for fuel Control (Seven Modes), EGR Control, Electronic Ignition Control - Closed loop Ignition timing, Spark Advance Correction Scheme, Integrated Engine Control System - Secondary Air Management, Evaporative Emissions Canister Purge, Automatic System Adjustment, System Diagnostics. (Text 1: Chapter 7) (6 hours) **Control Units –** Operating conditions, Design, Data processing, Programming, Digital modules in the Control unit, Control unit software. (Text 2: Pg. 196-207) (2 hours)	L1, L2
Module-4	
Automotive Networking –Bus Systems – Classification, Applications in the vehicle, Coupling of networks, Examples of networked vehicles (Text 2: Pg. 85-91), Buses - CAN Bus, LIN Bus, MOST Bus, Bluetooth, Flex Ray, Diagnostic Interfaces. (Text 2: Pg. 92-151) (6 hours) **Vehicle Motion Control** – Typical Cruise Control System, Digital Cruise Control System, Digital Speed Sensor, Throttle Actuator, Digital Cruise Control configuration, Cruise Control Electronics (Digital only), Antilock Brake System (ABS) (Text 1: Chapter 8) (2 hours)	L1, L2
Module-5	
Automotive Diagnostics–Timing Light, Engine Analyzer, On-board diagnostics, Off-board diagnostics, Expert Systems, Occupant Protection Systems – Accelerometer based Air Bag systems. (Text 1: Chapter 10) (2 hours) **Future Automotive Electronic Systems** – Alternative Fuel Engines, Electric and Hybrid vehicles, Fuel cell powered cars, Collision Avoidance Radar warning Systems, Low tire pressure warning system, Heads Up display, Speech Synthesis, Navigation – Navigation Sensors - Radio Navigation, Signpost navigation, dead reckoning navigation, Voice Recognition Cell Phone dialing, Advanced Cruise Control, Stability Augmentation, Automatic driving Control (Text 1: Chapter 11) (6 hours)	L1, L2, L3

Course Outcomes: At the end of the course, students will be able to:

- Acquire an overview of automotive components, subsystems, and basics of Electronic Engine Control in today's automotive industry.
- Use available automotive sensors and actuators while interfacing with microcontrollers / microprocessors during automotive system design.
- Understand the networking of various modules in automotive systems, communication protocols and diagnostics of the sub systems.
- Design and implement the electronics that attribute the reliability, safety, and smartness to the automobiles, providing add-on comforts and get fair idea on future Automotive Electronic Systems.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions (with a maximum of three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. William B. Ribbens, "Understanding Automotive Electronics", 6th Edition, Elsevier Publishing.
2. Robert Bosch Gmbh (Ed.) Bosch Automotive Electrics and Automotive Electronics Systems and Components, Networking and Hybrid Drive, 5th edition, John Wiley& Sons Inc., 2007.

Object Oriented Programming Using C++

B.E. V Semester (Open Elective)

[As per Choice Based Credit System (CBCS)scheme]

Subject Code	15EC562	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (08 Hrs/ Module	Exam Hours	03
CREDITS – 03			

Course objectives: This course will enable students to:

- Define Encapsulation, Inheritance and Polymorphism.
- Solve the problem with object oriented approach.
- Analyze the problem statement and build object oriented system model.
- Describe the characters and behavior of the objects that comprise a system.
- Explain function overloading, operator overloading and virtual functions.
- Discuss the advantages of object oriented programming over procedure oriented programming.

Module -1	RBT Level
Beginning with C++ and its features: What is C++?, Applications and structure of C++ program, Different Data types, Variables, Different Operators, expressions, operator overloading and control structures in C++ (Topics from Ch -2,3 of Text).	L1, L2
Module -2	
Functions, classes and Objects: Functions, Inline function, function overloading, friend and virtual functions, Specifying a class, C++ program with a class, arrays within a class, memory allocation to objects, array of objects, members, pointers to members and member functions (Selected Topics from Chap-4,5 of Text).	L1, L2, L3
Module -3	
Constructors, Destructors and Operator overloading: Constructors, Multiple constructors in a class, Copy constructor, Dynamic constructor, Destructors, Defining operator overloading, Overloading Unary and binary operators, Manipulation of strings using operators (Selected topics from Chap-6, 7 of Text).	L1, L2, L3
Module -4	
Inheritance, Pointers, Virtual Functions, Polymorphism: Derived Classes, Single, multilevel, multiple inheritance, Pointers to objects and derived classes, this pointer, Virtual and pure virtual functions (Selected topics from Chap-8,9 of Text).	L1, L2, L3

Module -5	
Streams and Working with files: C++ streams and stream classes, formatted and unformatted I/O operations, Output with manipulators, Classes for file stream operations, opening and closing a file, EOF (Selected topics from Chap-10, 11 of Text).	L1, L2, L3

Course Outcomes: At the end of the course, students will be able to:

- Explain the basics of Object Oriented Programming concepts.
- Apply the object initialization and destroy concept using constructors and destructors.
- Apply the concept of polymorphism to implement compile time polymorphism in programs by using overloading methods and operators.
- Use the concept of inheritance to reduce the length of code and evaluate the usefulness.
- Apply the concept of run time polymorphism by using virtual functions, overriding functions and abstract class in programs.
- Use I/O operations and file streams in programs.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Object Oriented Programming with C++, E.Balaguruswamy, TMH, 6th Edition, 2013.

Reference Book:

Object Oriented Programming using C++, Robert Lafore, Galgotia publication 2010.

8051 MICROCONTROLLER

B.E., V Semester (Open Elective)

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC563	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (08 Hrs/ Module)	Exam Hours	03
CREDITS – 03			

Course objectives: This course will enable students to:

- Understand the difference between a Microprocessor and a Microcontroller and embedded microcontrollers.
- Familiarize the basic architecture of 8051 microcontroller.
- Program 8051microprocessor using Assembly Level Language and C.
- Understand the interrupt system of 8051 and the use of interrupts.
- Understand the operation and use of inbuilt Timers/Counters and Serial port of 8051.
- Interface 8051 to external memory and I/O devices using its I/O ports.

Module -1	RBT Level
8051 Microcontroller: Microprocessor Vs Microcontroller, Embedded Systems, Embedded Microcontrollers, 8051 Architecture- Registers, Pin diagram, I/O ports functions, Internal Memory organization. External Memory (ROM & RAM) interfacing.	L1, L2
Module -2	
8051 Instruction Set: Addressing Modes, Data Transfer instructions, Arithmetic instructions, Logical instructions, Branch instructions, Bit manipulation instructions. Simple Assembly language program examples (without loops) to use these instructions.	L1, L2
Module -3	
8051 Stack, I/O Port Interfacing and Programming: 8051 Stack, Stack and Subroutine instructions. Assembly language program examples on subroutine and involving loops - Delay subroutine, Factorial of an 8 bit number (result maximum 8 bit), Block move without overlap, Addition of N 8 bit numbers, Picking smallest/largest of N 8 bit numbers. Interfacing simple switch and LED to I/O ports to switch on/off LED with respect to switch status.	L1, L2, L3
Module -4	
8051 Timers and Serial Port: 8051 Timers and Counters – Operation and Assembly language programming to generate a pulse	L1, L2, L3

using Mode-1 and a square wave using Mode-2 on a port pin. 8051 Serial Communication- Basics of Serial Data Communication, RS-232 standard, 9 pin RS232 signals, Simple Serial Port programming in Assembly and C to transmit a message and to receive data serially.	
Module -5	
8051 Interrupts and Interfacing Applications: 8051 Interrupts. 8051 Assembly language programming to generate an external interrupt using a switch, 8051 C programming to generate a square waveform on a port pin using a Timer interrupt. Interfacing 8051 to ADC-0804, LCD and Stepper motor and their 8051 Assembly language interfacing programming.	L1, L2, L3

Evaluation of Internal Assessment Marks:

It is suggested that at least a few simple programs to be executed by students using a simulation software or an 8051 microcontroller kit for better understanding of the course. This activity can be considered for the evaluation of 5 marks out of 20 Internal assessment marks, reserved for the other activities.

Course outcomes: At the end of the course, students will be able to:

- Explain the difference between Microprocessors & Microcontrollers, Architecture of 8051 Microcontroller, Interfacing of 8051 to external memory and Instruction set of 8051.
- Write 8051 Assembly level programs using 8051 instruction set.
- Explain the Interrupt system, operation of Timers/Counters and Serial port of 8051.
- Write 8051 Assembly language program to generate timings and waveforms using 8051 timers, to send & receive serial data using 8051 serial port and to generate an external interrupt using a switch.
- Write 8051 C programs to generate square wave on 8051 I/O port pin using interrupt and to send & receive serial data using 8051 serial port.
- Interface simple switches, simple LEDs, ADC 0804, LCD and Stepper Motor to 8051 using 8051 I/O ports.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

TEXT BOOKS: 1. **"The 8051 Microcontroller and Embedded Systems – using assembly and C ",** Muhammad Ali Mazidi and Janice Gillespie Mazidi and Rollin D. McKinlay; PHI, 2006 / Pearson, 2006. 2. **"The 8051 Microcontroller",** Kenneth J. Ayala, 3rd Edition, Thomson/Cengage Learning.
REFERENCE BOOKS: 1. **"The 8051 Microcontroller Based Embedded Systems",** Manish K Patel, McGraw Hill, 2014, ISBN: 978-93-329-0125-4. 2. **"Microcontrollers: Architecture, Programming, Interfacing and System Design",** Raj Kamal, Pearson Education, 2005.

B.E E&C SIXTH SEMESTER SYLLABUS

DIGITAL COMMUNICATION

B.E., VI Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC61	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours/Module)	Exam Hours	03
CREDITS – 04			

Course Objectives: The objectives of the course is to enable students to:

- Understand the mathematical representation of signal, symbol, noise and channels.
- Apply the concept of signal conversion to symbols and signal processing to symbols in transmitter and receiver functional blocks.
- Compute performance issues and parameters for symbol processing and recovery in ideal and corrupted channel conditions.
- Compute performance parameters and mitigate for these parameters in corrupted and distorted channel conditions.

Module-1	**RBT Level**
Bandpass Signal to Equivalent Lowpass: Hilbert Transform, Pre-envelopes, Complex envelopes, Canonical representation of bandpass signals, Complex low pass representation of bandpass systems, Complex representation of band pass signals and systems (Text 1: 2.8, 2.9, 2.10, 2.11, 2.12, 2.13). **Line codes:** Unipolar, Polar, Bipolar (AMI) and Manchester code and their power spectral densities (Text 1: Ch 6.10). Overview of HDB3, B3ZS, B6ZS (Ref. 1: 7.2)	L1, L2, L3
Module-2	
Signaling over AWGN Channels- Introduction, Geometric representation of signals, Gram-Schmidt Orthogonalization procedure, Conversion of the continuous AWGN channel into a vector channel, Optimum receivers using coherent detection: ML Decoding, Correlation receiver, matched filter receiver (Text 1: 7.1, 7.2, 7.3, 7.4).	L1, L2, L3
Module-3	
Digital Modulation Techniques: Phase shift Keying techniques using coherent detection: generation, detection and error probabilities of BPSK and QPSK, M–ary PSK, M–ary QAM (Relevant topics in Text 1 of 7.6, 7.7). Frequency shift keying techniques using Coherent detection: BFSK	

generation, detection and error probability (Relevant topics in Text 1 of 7.8). Non coherent orthogonal modulation techniques: BFSK, DPSK Symbol representation, Block diagrams treatment of Transmitter and Receiver, Probability of error (without derivation of probability of error equation) (Text 1: 7.11, 7.12. 7.13).	
Module-4	
Communication through Band Limited Channels: Digital Transmission through Band limited channels: Digital PAM Transmission through Band limited Channels, Signal design for Band limited Channels: Design of band limited signals for zero ISI–The Nyquist Criterion (statement only), Design of band limited signals with controlled ISI-Partial Response signals, Probability of error for detection of Digital PAM: Probability of error for detection of Digital PAM with Zero ISI, Symbol–by–Symbol detection of data with controlled ISI (Text 2: 9.1, 9.2, 9.3.1, 9.3.2). Channel Equalization: Linear Equalizers (ZFE, MMSE), Adaptive Equalizers (Text 2: 9.4.2).	L1, L2, L3
Module-5	
Principles of Spread Spectrum: Spread Spectrum Communication Systems: Model of a Spread Spectrum Digital Communication System, Direct Sequence Spread Spectrum Systems, Effect of De-spreading on a narrowband Interference, Probability of error (statement only), Some applications of DS Spread Spectrum Signals, Generation of PN Sequences, Frequency Hopped Spread Spectrum, CDMA based on IS-95 (Text 2: 11.3.1, 11.3.2, 11.3.3, 11.3.4, 11.3.5, 11.4.2).	L1, L2, L3

Course Outcomes: At the end of the course, the students will be able to:

- Associate and apply the concepts of Bandpass sampling to well specified signals and channels.
- Analyze and compute performance parameters and transfer rates for low pas and bandpass symbol under ideal and corrupted non band limited channels.
- Test and validate symbol processing and performance parameters at the receiver under ideal and corrupted bandlimited channels.
- Demonstrate by simulation and emulation that bandpass signals subjected to corrupted and distorted symbols in a bandlimited channel, can be demodulated and estimated at receiver to meet specified performance criteria.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks**.**
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Books:

1. Simon Haykin, “Digital Communication Systems”, John Wiley & sons, First Edition, 2014, ISBN 978-0-471-64735-5.
2. John G Proakis and Masoud Salehi, “Fundamentals of Communication Systems”, 2014 Edition, Pearson Education, ISBN 978-8-131-70573-5.

Reference Books:

1. B.P.Lathi and Zhi Ding, “Modern Digital and Analog communication Systems”, Oxford University Press, 4th Edition, 2010, ISBN: 978-0-198-07380-2.
2. Ian A Glover and Peter M Grant, “Digital Communications”, Pearson Education, Third Edition, 2010, ISBN 978-0-273-71830-7.
3. John G Proakis and Masoud Salehi, “Communication Systems Engineering”, 2nd Edition, Pearson Education, ISBN 978-93-325-5513-6.

ARM MICROCONTROLLER & EMBEDDED SYSTEMS

B.E., VI Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

ARM MICROCONTROLLER & EMBEDDED SYSTEMS

B.E., VI Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) Scheme]

Course Code	**15EC62**	**IA Marks**	**20**
Number of Lecture Hours/Week	**04**	**Exam Marks**	**80**
Total Number of Lecture Hours	**50 (10 Hours / Module)**	**Exam Hours**	**03**

CREDITS – 04

Course objectives: This course will enable students to:

- Understand the architectural features and instruction set of 32 bit microcontroller ARM Cortex M3.
- Program ARM Cortex M3 using the various instructions and C language for different applications.
- Understand the basic hardware components and their selection method based on the characteristics and attributes of an embedded system.
- Develop the hardware software co-design and firmware design approaches.
- Explain the need of real time operating system for embedded system applications.

Module-1

ARM-32 bit Microcontroller: Thumb-2 technology and applications of ARM, Architecture of ARM Cortex M3, Various Units in the architecture, Debugging support, General Purpose Registers, Special Registers, exceptions, interrupts, stack operation, reset sequence (Text 1: Ch 1, 2, 3) **L1, L2**

Module-2

ARM Cortex M3 Instruction Sets and Programming: Assembly basics, Instruction list and description, Useful instructions, Memory mapping, Bit-band operations and CMSIS, Assembly and C language Programming (Text 1: Ch-4, Ch-5, Ch-10 (10.1, 10.2, 10.3, 10.5 only) **L1, L2, L3**

Module-3

Embedded System Components: Embedded Vs General computing system, Classification of Embedded systems, Major applications and purpose of ES. Core of an Embedded System including all types of processor/controller, Memory, Sensors, Actuators, LED, 7 segment LED display, Optocoupler, Relay, Piezo buzzer, Push button switch, Communication Interface (onboard and external types), Embedded firmware, Other system components.

(Text 2: All the Topics from Ch-1 and Ch-2, excluding 2.3.3.4 (stepper motor), 2.3.3.8 (keyboard) and 2.3.3.9 (PPI) sections). **L1, L2, L3**

Module-4

Embedded System Design Concepts: Characteristics and Quality Attributes of Embedded Systems, Operational and non-operational quality attributes, Embedded

Systems-Application and Domain specific, Hardware Software Co-Design and Program Modelling (excluding UML), Embedded firmware design and development (excluding C language). (Text 2: Ch-3, Ch-4, Ch-7 (Sections 7.1, 7.2 only), Ch-9 (Sections 9.1, 9.2, 9.3.1, 9.3.2 only) **L1, L2, L3**
Module-5
RTOS and IDE for Embedded System Design: Operating System basics, Types of operating systems, Task, process and threads (Only POSIX Threads with an example program), Thread preemption, Preemptive Task scheduling techniques, Task Communication, Task synchronization issues – Racing and Deadlock, Concept of Binary and counting semaphores (Mutex example without any program), How to choose an RTOS, Integration and testing of Embedded hardware and firmware, Embedded system Development Environment – Block diagram (excluding Keil), Disassembler/decompiler, simulator, emulator and debugging techniques (Text 2: Ch-10 (Sections 10.1, 10.2, 10.3, 10.5.2 , 10.7, 10.8.1.1, 10.8.1.2, 10.8.2.2, 10.10 only), Ch 12, Ch-13 (a block diagram before 13.1, 13.3, 13.4, 13.5, 13.6 only) **L1, L2, L3**
Course outcomes: After studying this course, students will be able to: • Describe the architectural features and instructions of 32 bit microcontroller ARM Cortex M3. • Apply the knowledge gained for Programming ARM Cortex M3 for different applications. • Understand the basic hardware components and their selection method based on the characteristics and attributes of an embedded system. • Develop the hardware /software co-design and firmware design approaches. • Explain the need of real time operating system for embedded system applications.
Text Books: 1. Joseph Yiu, "The Definitive Guide to the ARM Cortex-M3", 2nd Edition, Newnes, (Elsevier), 2010. 2. Shibu K V, "Introduction to Embedded Systems", Tata McGraw Hill Education Private Limited, 2nd Edition.

VLSI Design

B.E., VI Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC63	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours / Module)	Exam Hours	03
CREDITS – 04			

Course Objectives: The objectives of the course is to enable students to:

- Impart knowledge of MOS transistor theory and CMOS technologies
- Impart knowledge on architectural choices and performance tradeoffs involved in designing and realizing the circuits in CMOS technology
- Cultivate the concepts of subsystem design processes
- Demonstrate the concepts of CMOS testing

Module-1	RBT Level
Introduction: A Brief History, MOS Transistors, MOS Transistor Theory, Ideal I-V Characteristics, Non-ideal I-V Effects, DC Transfer Characteristics (1.1, 1.3, 2.1, 2.2, 2.4, 2.5 of TEXT2). **Fabrication:** nMOS Fabrication, CMOS Fabrication [P-well process, N-well process, Twin tub process], BiCMOS Technology (1.7, 1.8,1.10 of TEXT1).	L1, L2
Module-2	
MOS and BiCMOS Circuit Design Processes: MOS Layers, Stick Diagrams, Design Rules and Layout. **Basic Circuit Concepts:** Sheet Resistance, Area Capacitances of Layers, Standard Unit of Capacitance, Some Area Capacitance Calculations, Delay Unit, Inverter Delays, Driving Large Capacitive Loads (3.1 to 3.3, 4.1, 4.3 to 4.8 of TEXT1).	L1, L2, L3
Module-3	
Scaling of MOS Circuits: Scaling Models & Scaling Factors for Device Parameters **Subsystem Design Processes:** Some General considerations, An illustration of Design Processes, **Illustration of the Design Processes**- Regularity, Design of an ALU Subsystem, The Manchester Carry-chain and Adder Enhancement Techniques(5.1, 5.2, 7.1, 7.2, 8.2, 8.3, 8.4.1, 8.4.2 of TEXT1).	L1, L2, L3
Module-4	
Subsystem Design: Some Architectural Issues, Switch Logic, Gate(restoring) Logic, Parity Generators, Multiplexers, The Programmable Logic Array (PLA) (6.1to 6.3, 6.4.1, 6.4.3, 6.4.6 of TEXT1). **FPGA Based Systems:** Introduction, Basic concepts, Digital design and FPGA's, FPGA based System design, FPGA architecture, Physical design for FPGA's (1.1 to 1.4, 3.2, 4.8 of TEXT3).	L1, L2, L3
Module-5	
Memory, Registers and Aspects of system Timing- System Timing Considerations, Some commonly used Storage/Memory elements (9.1, 9.2 of TEXT1).	L1, L2, L3

Testing and Verification: Introduction, Logic Verification, Logic Verification Principles, Manufacturing Test Principles, Design for testability (12.1, 12.1.1, 12.3, 12.5, 12.6 of TEXT 2).	

Course outcomes: At the end of the course, the students will be able to:

- Demonstrate understanding of MOS transistor theory, CMOS fabrication flow and technology scaling.
- Draw the basic gates using the stick and layout diagrams with the knowledge of physical design aspects.
- Interpret Memory elements along with timing considerations
- Demonstrate knowledge of FPGA based system design
- Interpret testing and testability issues in VLSI Design
- Analyze CMOS subsystems and architectural issues with the design constraints.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Books:

1. **"Basic VLSI Design"**- Douglas A. Pucknell& Kamran Eshraghian, PHI 3rd Edition (original Edition – 1994).
2. **"CMOS VLSI Design- A Circuits and Systems Perspective"-** Neil H.E. Weste, David Harris, Ayan Banerjee, 3rd Edition, Pearson Education.
3. **"FPGA Based System Design"-** Wayne Wolf, Pearson Education, 2004, Technology and Engineering.

COMPUTER COMMUNICATION NETWORKS

B.E., VI Semester, Electronics & Communication Engineering / Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

<table>
<tr><td colspan="4"><u>COMPUTER COMMUNICATION NETWORKS</u>
B.E., VI Semester, Electronics & Communication Engineering / Telecommunication Engineering
[As per Choice Based Credit System (CBCS) Scheme]</td></tr>
<tr><td>Course Code</td><td>15EC64</td><td>IA Marks</td><td>20</td></tr>
<tr><td>Number of Lecture Hours/Week</td><td>04</td><td>Exam Marks</td><td>80</td></tr>
<tr><td>Total Number of Lecture Hours</td><td>50 (10 Hours / Module)</td><td>Exam Hours</td><td>03</td></tr>
<tr><td colspan="4">CREDITS – 04</td></tr>
<tr><td colspan="4">Course Objectives: This course will enable students to:
• Understand the layering architecture of OSI reference model and TCP/IP protocol suite.
• Understand the protocols associated with each layer.
• Learn the different networking architectures and their representations.
• Learn the various routing techniques and the transport layer services.</td></tr>
<tr><td colspan="4">Module-1</td></tr>
<tr><td colspan="4">Introduction: Data Communications: Components, Representations, Data Flow, Networks: Physical Structures, Network Types: LAN, WAN, Switching, Internet.
Network Models: Protocol Layering: Scenarios, Principles, Logical Connections, TCP/IP Protocol Suite: Layered Architecture, Layers in TCP/IP suite, Description of layers, Encapsulation and Decapsulation, Addressing, Multiplexing and Demultiplexing, The OSI Model: OSI Versus TCP/IP.
Data-Link Layer: Introduction: Nodes and Links, Services, Categories' of link, Sublayers, Link Layer addressing: Types of addresses, ARP. Data Link Control (DLC) services: Framing, Flow and Error Control, Data Link Layer Protocols: Simple Protocol, Stop and Wait protocol, Piggybacking. L1, L2</td></tr>
<tr><td colspan="4">Module-2</td></tr>
<tr><td colspan="4">Media Access Control: Random Access: ALOHA, CSMA, CSMA/CD, CSMA/CA. Controlled Access: Reservation, Polling, Token Passing.
Wired LANs: Ethernet: Ethernet Protocol: IEEE802, Ethernet Evolution, Standard Ethernet: Characteristics, Addressing, Access Method, Efficiency, Implementation, Fast Ethernet: Access Method, Physical Layer, Gigabit Ethernet: MAC Sublayer, Physical Layer, 10 Gigabit Ethernet. L1, L2</td></tr>
<tr><td colspan="4">Module-3</td></tr>
<tr><td colspan="4">Wireless LANs: Introduction: Architectural Comparison, Characteristics, IEEE 802.11: Architecture, MAC Sublayer, Addressing Mechanism, Physical Layer, Bluetooth: Architecture, Layers.
Connecting Devices: Hubs, Switches, Virtual LANs: Membership, Configuration, Communication between Switches and Routers, Advantages.
Network Layer: Introduction, Network Layer services: Packetizing, Routing and Forwarding, Other services, Packet Switching: Datagram Approach, Virtual Circuit Approach, IPV4 Addresses: Address Space, Classful Addressing, Classless Addressing,</td></tr>
</table>

DHCP, Network Address Resolution, Forwarding of IP Packets: Based on destination Address and Label. **L1, L2**

Module-4

Network Layer Protocols: Internet Protocol (IP): Datagram Format, Fragmentation, Options, Security of IPv4 Datagrams, ICMPv4: Messages, Debugging Tools, Mobile IP: Addressing, Agents, Three Phases, Inefficiency in Mobile IP.

Unicast Routing: Introduction, Routing Algorithms: Distance Vector Routing, Link State Routing, Path vector routing, Unicast Routing Protocol: Internet Structure, Routing Information Protocol, Open Shortest Path First, Border Gateway Protocol Version 4. **L1, L2, L3**

Module-5

Transport Layer: Introduction: Transport Layer Services, Connectionless and Connection oriented Protocols, Transport Layer Protocols: Simple protocol, Stop and wait protocol, Go-Back-N Protocol, Selective repeat protocol, User Datagram Protocol: User Datagram, UDP Services, UDP Applications, Transmission Control Protocol: TCP Services, TCP Features, Segment, Connection, State Transition diagram, Windows in TCP, Flow control, Error control, TCP congestion control. **L1, L2**

Course Outcomes: At the end of the course, the students will be able to:

- Identify the protocols and services of Data link layer.
- Identify the protocols and functions associated with the transport layer services.
- Describe the layering architecture of computer networks and distinguish between the OSI reference model and TCP/IP protocol suite.
- Distinguish the basic network configurations and standards associated with each network.
- Construct a network model and determine the routing of packets using different routing algorithms.

Text Book:

Data Communications and Networking , Forouzan, 5th Edition, McGraw Hill, 2016
ISBN: 1-25-906475-3

Reference Books:

1. Computer Networks, James J Kurose, Keith W Ross, Pearson Education, 2013, ISBN: 0-273-76896-4
2. Introduction to Data Communication and Networking, Wayarles Tomasi, Pearson Education, 2007, ISBN:0130138282

CELLULAR MOBILE COMMUNICATIONS

B.E., VI Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC651	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course enables students to:

- Understand the application of multi user access in a cellular communication scenario.
- Understand the propagation mechanisms in an urban mobile communications using statistical and empirical models.
- Understand system architecture, call processing protocols and services of GSM, GPRS and EDGE.
- Understand system architecture, call processing protocols and services of CDMA based systems IS95 and CDMA2000.

Module-1	RBT Level
Cellular Concept: Frequency Reuse, Channel Assignment Strategies, Interference and System Capacity, Power Control for Reducing Interference, Trunking and Grade of Service, Improving Capacity in Cellular Systems. **Mobile Radio Propagation:** Large Scale path Loss- Free Space Model, Three basic propagation mechanisms, Practical Link Budget Design using Path Loss Models, Outdoor Propagation Models – Okumura, Hata, PCS Extension to Hata Model (explanations only) (Text 1).	L1, L2
Module-2	
Mobile Radio Propagation: Small-Scale Fading and Multipath: Small scale Multipath Propagation, Impulse Response Model of a Multipath Channel, Small-Scale Multipath Measurements, Parameters of Mobile Multipath Channels, Types of Small-Scale Fading, Rayleigh and Ricean Distributions, Statistical Model for Multipath Fading Channels (Clarke's Model for Flat Fading only).(Text 1)	L1, L2
Module-3	
System Architecture and Addressing: System architecture, The SIM concept, Addressing, Registers and subscriber data, Location registers (HLR and VLR) Security-related registers (AUC and EIR), Subscriber data, Network interfaces and configurations. **Air Interface – GSM Physical Layer:** Logical channels, Physical channels, Synchronization- Frequency and clock synchronization, Adaptive frame synchronization, Mapping of logical onto physical channels, Radio subsystem link control, Channel coding, source coding and speech processing, Source coding and speech processing, Channel coding, Power-up scenario. **GSM Protocols:** Protocol architecture planes, Protocol architecture of the user plane, Protocol architecture of the signaling plane, Signaling at the air interface (Um), Signaling at the A and Abis interfaces, Security-related network functions,	L1, L2

Signaling at the user interface.(Text 2)	
Module-4	
GSM Roaming Scenarios and Handover: Mobile application part interfaces, Location registration and location update, Connection establishment and termination, Handover. (up to 6.4.1 only in Text2) **Services:** Classical GSM services, Popular GSM services: SMS and MMS. **Improved data services in GSM**: **GPRS, HSCSD and EDGE** GPRS System architecture of GPRS , Services , Session management, mobility management and routing, Protocol architecture, Signaling plane, Interworking with IP networks, Air interface, Authentication and ciphering, Summary of GPRS . HSCSD: Architecture, Air interface, HSCSD resource allocation and capacity issues. EDGE: The EDGE concept, EDGE physical layer, modulation and coding, EDGE: effects on the GSM system architecture, ECSD and EGPRS. (Text 2)	L1, L2
Module-5	
CDMA Technology – Introduction to CDMA,CDMA frequency bands, CDMA Network and System Architecture, CDMA Channel concept, Forward Logical Channels, Reverse logical Channels, CDMA frame format, CDMA System Operations(Initialization/Registration), Call Establishment, CDMA Call handoff,IS-95B,CDMA2000,W-CDMA,UMTS,CDMA data networks, Evolution of CDMA to 3G, CDMA 2000 RAN Components, CDMA 2000 Packet Data Service. (Text 3)	L1, L2

Course outcomes: At the end of the course, the students will be able to:

- Apply the understanding of statistical characterization of urban mobile channels to compute the performance for simple modulation schemes.
- Demonstrate the limitations of GSM, GPRS and CDMA to meet high data rate requirements and limited improvements that are needed.
- Analyze the call process procedure between a calling number and called number for all scenarios in GSM or CDMA based systems.
- Test and validate voice and data call handling for various scenarios in GSM and CDMA systems for national and international interworking situations.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks**.**
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Books:

1. Theodore Rapport, "Wireless Communications – Principles and Practice", Prentice Hall of India , 2nd Edition, 2007, ISBN 978-8-120-32381-0.
2. Jorg Eberspacher, Hans-Jorg Vogel, Christian Bettstetter, Christian Hartmann,

"GSM– Architecture, Protocols and Services”, Wiley,3rd Edition, 2009,ISBN-978-0-470-03070-7.

3. Gary J Mullet, “Introduction To Wireless Telecommunications Systems and Networks", Cengage Learning.

ADAPTIVE SIGNAL PROCESSING

B.E., VI Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC652	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03

CREDITS – 03

Course Objectives: The objectives of this course are to:

- Introduce to the concept and need of adaptive filters and popular adaptive signal processing algorithms
- Understand the concepts of training and convergence and the trade-off between performance and complexity.
- Introduce to common linear estimation techniques
- Demonstrate applications of adaptive systems to sample problems.
- Introduce inverse adaptive modelling.

Module-1	RBT Level
Adaptive systems: Definitions and characteristics - applications – properties-examples - adaptive linear combiner input signal and weight vectors - performance function-gradient and minimum mean square error - introduction to filtering-smoothing and prediction - linear optimum filtering-orthogonality - Wiener – Hopf equation-performance surface(Chapters 1& 2 of Text).	L1, L2
Module-2	
Searching performance surface-stability and rate of convergence: Learning curve-gradient search - Newton's method - method of steepest descent - comparison - Gradient estimation - performance penalty - variance - excess MSE and time constants – mis-adjustments (Chapters 4& 5 of Text).	L1, L2
Module-3	
LMS algorithm convergence of weight vector: LMS/Newton algorithm - properties - sequential regression algorithm - adaptive recursive filters - random-search algorithms - lattice structure - adaptive filters with orthogonal signals (Chapters 6& 8 of Text).	L1, L2, L3
Module-4	
Applications-adaptive modeling and system identification: Multipath communication channel, geophysical exploration, FIR digital filter synthesis. (Chapter 9 of Text).	L1, L2, L3
Module-5	
Inverse adaptive modeling: Equalization, and deconvolution adaptive equalization of telephone channels-adapting poles and zeros for IIR digital filter synthesis(Chapter 10 of Text).	L1, L2, L3

Course Outcomes: At the end of the course, students should be able to:

- Devise filtering solutions for optimising the cost function indicating error in estimation of parameters and appreciate the need for adaptation in design.
- Evaluate the performance of various methods for designing adaptive filters

through estimation of different parameters of stationary random process clearly considering practical application specifications.

- Analyse convergence and stability issues associated with adaptive filter design and come up with optimum solutions for real life applications taking care of requirements in terms of complexity and accuracy.
- Design and implement filtering solutions for applications such as channel equalisation, interference cancelling and prediction considering present day challenges.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Bernard Widrow and Samuel D. Stearns, "Adaptive Signal Processing", Person Education, 1985.

Reference Books:

1. Simon Haykin, "Adaptive Filter Theory", Pearson Education, 2003.
2. John R. Treichler, C. Richard Johnson, Michael G. Larimore, "Theory and Design of Adaptive Filters", Prentice-Hall of India, 2002.

ARITIFICAL NEURAL NETWORKS

B.E., VI Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC653	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03

CREDITS – 03

Course Objectives: The objectives of this course are:

- Understand the basics of ANN and comparison with Human brain
- Provide knowledge on Generalization and function approximation and various architectures of building an ANN
- Provide knowledge of reinforcement learning using neural networks
- Provide knowledge of unsupervised learning using neural networks.

Module-1	RBT Level
Introduction: Biological Neuron – Artificial Neural Model - Types of activation functions – **Architecture**: Feedforward and Feedback, Convex Sets, Convex Hull and Linear Separability, Non-Linear Separable Problem. XOR Problem, Multilayer Networks. **Learning:** Learning Algorithms, Error correction and Gradient Descent Rules, Learning objective of TLNs, Perceptron Learning Algorithm, Perceptron Convergence Theorem.	L1, L2
Module-2	
Supervised Learning: Perceptron learning and Non Separable sets, α-Least Mean Square Learning, MSE Error surface, Steepest Descent Search, μ-LMS approximate to gradient descent, Application of LMS to Noise Cancelling, Multi-layered Network Architecture, Backpropagation Learning Algorithm, Practical consideration of BP algorithm.	L1, L2, L3
Module-3	
Support Vector Machines and Radial Basis Function: Learning from Examples, Statistical Learning Theory, Support Vector Machines, SVM application to Image Classification, Radial Basis Function Regularization theory, Generalized RBF Networks, Learning in RBFNs, RBF application to face recognition.	L1, L2, L3
Module-4	
Attractor Neural Networks: Associative Learning Attractor Associative Memory, Linear Associative memory, Hopfield Network, application of Hopfield Network, Brain State in a Box neural Network, Simulated Annealing, Boltzmann Machine, Bidirectional Associative Memory.	L1, L2, L3
Module-5	
Self-organization Feature Map: Maximal Eigenvector Filtering, Extracting Principal Components, Generalized Learning Laws, Vector Quantization, Self-organization Feature Maps, Application of SOM, Growing Neural Gas.	L1, L2, L3

Course outcomes: At the end of the course, students should be able to:

- Understand the role of neural networks in engineering, artificial intelligence, and cognitive modelling.
- Understand the concepts and techniques of neural networks through the study of the most important neural network models.
- Evaluate whether neural networks are appropriate to a particular application.
- Apply neural networks to particular applications, and to know what steps to take to improve performance.

Question paper pattern:

The question paper will have ten questions.

- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Neural Networks A Classroom Approach– Satish Kumar, McGraw Hill Education (India) Pvt. Ltd, Second Edition.

Reference Books:

1. **Introduction to Artificial Neural Systems-**J.M. Zurada, Jaico Publications 1994.
2. **Artificial Neural Networks-**B. Yegnanarayana, PHI, New Delhi 1998.

DIGITAL SWITCHING SYSTEMS

B.E., VI Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC654	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to

- Understand the basics of telecommunication networks and digital transmission of data.
- Study about the evolution of switching systems and the digital switching.
- Study about the telecommunication traffic and its measurements.
- Learn the technologies associated with the data switching operations.
- Understand the use of software for the switching and its maintenance

Module-1	RBT Level
DEVELOPMENT OF TELECOMMUNICATIONS: Network structure, Network services, terminology, Regulation, Standards. Introduction to telecommunications transmission, Power levels, Four wire circuits, Digital transmission, FDM,TDM, PDH and SDH [Text-1]	L1, L2
Module-2	
EVOLUTION OF SWITCHING SYSTEMS: Introduction, Message switching, Circuit switching, Functions of switching systems, Distribution systems, Basics of crossbar systems, Electronic switching. **DIGITAL SWITCHING SYSTEMS:** Switching system hierarchy, Evolution of digital switching systems, Stored program control switching systems, Building blocks of a digital switching system, Basic call processing. [Text-1 and 2]	L1, L2
Module-3	
TELECOMMUNICATIONS TRAFFIC: Introduction, Unit of traffic, Congestion, Traffic measurement, Mathematical model, lost call systems, Queuing systems. **SWITCHING SYSTEMS:** Introduction, Single stage networks, Gradings, Link Systems, GOS of Linked systems. [Text-1]	L1, L2
Module-4	
TIME DIVISION SWITCHING: Introduction, space and time switching, Time switching networks, Synchronisation. **SWITCHING SYSTEM SOFTWARE:** Introduction, Basic software architecture, Software architecture for level 1to 3 control, Digital switching system software classification, Call models, Software linkages during call, Feature flow diagram, Feature interaction. [Text-1 and 2]	L1, L2
Module-5	
MAINTENANCE OF DIGITAL SWITCHING SYSTEM: Introduction , Software maintenance, Interface of a typical digital switching system central office, System outage and its impact on digital switching system reliability, Impact	L1, L2

of software patches on digital switching system maintainability, A methodology for proper maintenance of digital switching system **A GENERIC DIGITAL SWITCHING SYSTEM MODEL:** Introduction, Hardware architecture, Software architecture, Recovery strategy, Simple call through a digital system, Common characteristics of digital switching systems. Reliability analysis. [Text-2]	

Course Outcomes: At the end of the course, students should be able to:

- Describe the electromechanical switching systems and its comparison with the digital switching.
- Determine the telecommunication traffic and its measurements.
- Define the technologies associated with the data switching operations.
- Describe the software aspects of switching systems and its maintenance.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Books:

1. Telecommunication and Switching, Traffic and Networks - J E Flood: Pearson Education, 2002.

2. Digital Switching Systems, Syed R. Ali, TMH Ed 2002.

Reference Book:

Digital Telephony - John C Bellamy: Wiley India Pvt. Ltd, 3rd Ed, 2008.

MICROELECTRONICS

B.E., VI Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC655	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to:

- Be familiar with the MOSFET physical structure and operation, terminal characteristics, circuit models and basic circuit applications.
- Confront integrated device and/or circuit design problems, identify the design issues, and develop solutions.
- Analyze and design microelectronic circuits for linear amplifier and digital applications.
- Contrast the input/output and gain characteristics of single-transistor, differential and common two-transistor linear amplifier building block stages.

Module-1	RBT Level
MOSFETS: Device Structure and Physical Operation, V-I Characteristics, MOSFET Circuits at DC, MOSFET as an amplifier and as a switch.	L1, L2
Module-2	
MOSFETS (continued): Biasing in MOS amplifier Circuits, Small Signal Operation and Models, Basic MOSFET amplifier, MOSFET internal capacitances, frequency response of CS amplifier.	L1, L2
Module-3	
MOSFETS (continued): Discrete circuit MOS amplifiers. **Single Stage IC Amplifier:** Comparison of MOSFET and BJT, Current sources, Current mirrors and Current steering circuits, high frequency response- general considerations.	L1, L2, L3
Module-4	
Single Stage IC Amplifier (continued): CS with active loads, high frequency response of CS, CG amplifiers with active loads, high frequency response of CG, Cascode amplifiers. CS with source degeneration (only MOS amplifiers to be dealt).	L1, L2
Module-5	
Differential and Multistage Amplifiers: The MOS differential pair, small signal operation of MOS differential pair, Differential amplifier with active loads, and frequency response of the differential amplifiers. Multistage amplifiers (only MOS amplifiers to be dealt).	L1, L2
Course outcomes: After studying this course, students will be able to: • Explain the underlying physics and principles of operation of Metaloxide-semiconductor (MOS) capacitors and MOS field effect transistors (MOSFETs). • Describe and apply simple large signal circuit models for MOSFETs. • Analyze and design microelectronic circuits for linear amplifier for digital applications.	

- Use of discrete MOS circuits to design Single stage and Multistage amplifiers to meet stated operating specifications.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks**.**
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

"Microelectronic Circuits", Adel Sedra and K.C. Smith, 6th Edition, Oxford University Press, International Version, 2009.

Reference Books:

1. **"Microelectronics An integrated approach",** Roger T Howe, Charles G Sodini, Pearson education.
2. **"Fundamentals of Microelectronics",** Behzad Razavi, John Wiley India Pvt. Ltd, 2008.
3. **"Microelectronics – Analysis and Design",** Sundaram Natarajan, Tata McGraw-Hill, 2007.

EMBEDDED CONTROLLER LAB

B.E., VI Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15ECL67	IA Marks	20
Number of Lecture Hours/Week	01Hr Tutorial (Instructions) + 02 Hours Laboratory = 03	Exam Marks	80
RBT Levels	L1, L2, L3	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to:

- Understand the instruction set of ARM Cortex M3, a 32 bit microcontroller and the software tool required for programming in Assembly and C language.
- Program ARM Cortex M3 using the various instructions in assembly level language for different applications.
- Interface external devices and I/O with ARM Cortex M3.
- Develop C language programs and library functions for embedded system applications.

Laboratory Experiments

PART-A: Conduct the following Study experiments to learn ALP using ARM Cortex M3 Registers using an Evaluation board and the required software tool.

1. ALP to multiply two 16 bit binary numbers.
2. ALP to find the sum of first 10 integer numbers.

PART-B: Conduct the following experiments on an ARM CORTEX M3 evaluation board using evaluation version of Embedded 'C' & Keil uVision-4 tool/compiler.

1. Display “Hello World” message using Internal UART.
2. Interface and Control a DC Motor.
3. Interface a Stepper motor and rotate it in clockwise and anti-clockwise direction.

4. Interface a DAC and generate Triangular and Square waveforms.
5. Interface a 4x4 keyboard and display the key code on an LCD.
6. Using the Internal PWM module of ARM controller generate PWM and vary its duty cycle.
7. Demonstrate the use of an external interrupt to toggle an LED On/Off.
8. Display the Hex digits 0 to F on a 7-segment LED interface, with an appropriate delay in between.
9. Interface a simple Switch and display its status through Relay, Buzzer and LED.
10. Measure Ambient temperature using a sensor and SPI ADC IC.

Course outcomes: After studying this course, students will be able to:

- Understand the instruction set of 32 bit microcontroller ARM Cortex M3, and the software tool required for programming in Assembly and C language.
- Develop assembly language programs using ARM Cortex M3 for different applications.
- Interface external devices and I/O with ARM Cortex M3.
- Develop C language programs and library functions for embedded system applications.

Conduction of Practical Examination:

1. PART-B experiments using Embedded-C are only to be considered for the practical examination. PART-A ALP programs are for study purpose and can be considered for Internal Marks evaluation.
2. Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.
3. Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

COMPUTER NETWORKS LABORATORY

B.E., VI Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15ECL68	IA Marks	20
Number of Lecture Hours/Week	01Hr Tutorial (Instructions) + 02 Hours Laboratory = 03	Exam Marks	80
RBT Levels	L1, L2, L3	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to:

- Choose suitable tools to model a network and understand the protocols at various OSI reference levels.
- Design a suitable network and simulate using a Network simulator tool.
- Simulate the networking concepts and protocols using C/C++ programming.
- Model the networks for different configurations and analyze the results.

Laboratory Experiments

PART-A: Simulation experiments using NS2/ NS3/ OPNET/ NCTUNS/ NetSim/ QualNet or any other equivalent tool

1. Implement a point to point network with four nodes and duplex links between them. Analyze the network performance by setting the queue size and varying the bandwidth.
2. Implement a four node point to point network with links n0-n2, n1-n2 and n2-n3. Apply TCP agent between n0-n3 and UDP between n1-n3. Apply relevant applications over TCP and UDP agents changing the parameter and determine the number of packets sent by TCP/UDP.
3. Implement Ethernet LAN using n (6-10) nodes. Compare the throughput by changing the error rate and data rate.
4. Implement Ethernet LAN using n nodes and assign multiple traffic to the nodes and obtain congestion window for different sources/ destinations.
5. Implement ESS with transmission nodes in Wireless LAN and obtain the performance parameters.
6. Implementation of Link state routing algorithm.

PART-B: Implement the following in C/C++

1. Write a program for a HLDC frame to perform the following.

i) Bit stuffing

ii) Character stuffing.

2. Write a program for distance vector algorithm to find suitable path for transmission.

3. Implement Dijkstra's algorithm to compute the shortest routing path.
4. For the given data, use CRC-CCITT polynomial to obtain CRC code. Verify the program for the cases

   a. Without error

b. With error

5. Implementation of Stop and Wait Protocol and Sliding Window Protocol
6. Write a program for congestion control using leaky bucket algorithm.

Course outcomes: On the completion of this laboratory course, the students will be able to:

- Use the network simulator for learning and practice of networking algorithms.
- Illustrate the operations of network protocols and algorithms using C programming.
- Simulate the network with different configurations to measure the performance parameters.
- Implement the data link and routing protocols using C programming.

Conduct of Practical Examination:

- All laboratory experiments are to be included for practical examination.
- For examination one question from software and one question from hardware or only one hardware experiments based on the complexity to be set.
- Students are allowed to pick one experiment from the lot.
- Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.
- Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

6th Semester Open Electives Syllabus for the courses offered by EC/TC Board:

<table>
<tr><td colspan="4">DATA STRUCTURE USING C++
B.E VI Semester (Open Elective)
[As per Choice Based Credit System (CBCS) Scheme]</td></tr>
<tr><td>Course Code</td><td>15EC661</td><td>IA Marks</td><td>20</td></tr>
<tr><td>Number of Lecture Hours/Week</td><td>03</td><td>Exam Marks</td><td>80</td></tr>
<tr><td>Total Number of Lecture Hours</td><td>40 (08 Hrs per Module)</td><td>Exam Hours</td><td>03</td></tr>
<tr><td colspan="4">CREDITS – 03</td></tr>
</table>

Course objectives: This course will enable students to

- Explain fundamentals of data structures and their applications essential for programming/problem solving
- Analyze Linear Data Structures: Stack, Queues, Lists
- Analyze Non Linear Data Structures: Trees
- Assess appropriate data structure during program development/Problem Solving

Module -1

INTRODUCTION: Functions and parameters, Dynamic memory allocation, Recursion.
LINEAR LISTS: Data objects and structures, Linear list data structures, Array Representation, Vector Representation, Singly Linked lists and chains. **L1, L2**

Module -2

ARRAYS AND MATRICS: Arrays, Matrices, Special matrices, Sparse matrices.

STACKS: The abstract data types, Array Representation, Linked Representation, Applications-Parenthesis Matching & Towers of Hanoi. **L1, L2, L3**

Module -3

QUEUES: The abstract data types, Array Representation, Linked Representation, Applications-Railroad car arrangement.

HASHING: Dictionaries, Linear representation, Hash table representation. **L1, L2, L3**

Module -4

BINARY AND OTHER TREES: Trees, Binary trees, Properties and representation of binary trees, Common binary tree operations, Binary tree traversal the ADT binary tree, ADT binary tree and the class linked binary tree. **L1, L2, L3**

Module -5

Priority Queues: Linear lists, Heaps, Applications-Heap Sorting.
Search Trees: Binary search trees operations and implementation, Binary Search trees with duplicates. **L1, L2, L3**

Course outcomes: After studying this course, students will be able to:

- Acquire knowledge of Dynamic memory allocation, Various types of data structures, operations and algorithms and Sparse matrices and Hashing
- Understand non Linear data structures trees and their applications
- Design appropriate data structures for solving computing problems
- Analyze the operations of Linear Data structures: Stack, Queue and Linked List and their applications

Text Book:

Data structures, Algorithms, and applications in C++, Sartaj Sahni, Universities Press, 2nd Edition, 2005.

Reference Books:

1. **Data structures, Algorithms, and applications in C++,** Sartaj Sahni, Mc. Graw Hill, 2000.
2. **Object Oriented Programming with C++,** E.Balaguruswamy**,** TMH, 6th Edition, 2013.
3. **Programming in C++,** E.Balaguruswamy. TMH, 4th, 2010.

POWER ELECTRONICS

B.E., VI Semester (Open Elective)

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC662	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (08 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to

- Understand the working of various power devices.
- Study and analysis of thyristor circuits with different triggering techniques.
- Learn the applications of power devices in controlled rectifiers, converters and inverters.
- Study of power electronics circuits under different load conditions.

Module-1	RBT Level
Introduction - Applications of Power Electronics, Power Semiconductor Devices, Control Characteristics of Power Devices, types of Power Electronic Circuits. Power Transistors: Power BJTs: Steady state characteristics. Power MOSFETs: device operation, switching characteristics, IGBTs: device operation, output and transfer characteristics. (Text 1)	L1, L2
Module-2	
Thyristors - Introduction, Principle of Operation of SCR, Static Anode-Cathode Characteristics of SCR, Two transistor model of SCR, Gate Characteristics of SCR, Turn-ON Methods, Turn-OFF Mechanism, Turn-OFF Methods: Natural and Forced Commutation – Class A and Class B types, Gate Trigger Circuit: Resistance Firing Circuit, Resistance capacitance firing circuit. (Text 2)	L1, L2, L3
Module-3	
Controlled Rectifiers - Introduction, principle of phase controlled converter operation, Single phase full converters, Single phase dual converters. AC Voltage Controllers - Introduction, Principles of ON-OFF Control, Principle of Phase Control, Single phase control with resistive and inductive loads. (Text 1)	L1, L2, L3
Module-4	
DC-DC Converters - Introduction, principle of step-down operation and it's analysis with RL load, principle of step-up operation, Step-up converter with a resistive load, Performance parameters, Converter classification, Switching mode regulators: Buck regulator, Boost regulator, Buck-Boost Regulators. (Text 1)	L1, L2

Module-5	
Pulse Width Modulated Inverters- Introduction, principle of operation, performance parameters, Single phase bridge inverters, voltage control of single phase inverters, current source inverters, Variable DC-link inverter, Boost inverter. (Text 1)	L1, L2

Course outcomes: After studying this course, students will be able to:

- Describe the characteristics of different power devices and identify the applications.
- Illustrate the working of DC-DC converter and inverter circuit.
- Determine the output response of a thyristor circuit with various triggering options.
- Determine the response of controlled rectifier with resistive and inductive loads.

Evaluation of Internal Assessment Marks:

It is suggested that at least a few experiments of Power Electronics are conducted by the students for better understanding of the course. This activity can be considered for the evaluation of 5 marks out of 20 Internal assessment marks, reserved for the other activities.

Question paper pattern:

- The question paper will have ten questions
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module
- The students will have to answer 5 full questions, selecting one full question from each module

Text Book:

1. Mohammad H Rashid, Power Electronics, Circuits, Devices and Applications, 3rd/4th Edition, Pearson Education Inc, 2014, ISBN: 978-93-325-1844-5.
2. M.D Singh and K B Khanchandani, Power Electronics, 2nd Edition, Tata Mc-Graw Hill, 2009, ISBN: 0070583897.

Reference Books:

4. L. Umanand, Power Electronics, Essentials and Applications, John Wiley India Pvt. Ltd, 2009.
5. Dr. P. S. Bimbhra, “Power Electronics”, Khanna Publishers, Delhi, 2012.
6. P.C. Sen, “Modern Power Electronics”, S Chand & Co New Delhi, 2005.

DIGITAL SYSTEM DESIGN USING VERILOG

B.E., VI Semester (Open Elective)

[As per Choice Based Credit System (CBCS) scheme]

Subject Code:	15EC663	IA Marks: 20
Number of Lecture Hours/Week:	03	Exam Marks: 80
Total Number of Lecture Hours:	40 (08 Hrs per module)	Exam Hours: 03
CREDITS – 03		

Course objectives: This course will enable students to:

- Understand the concepts of Verilog Language.
- Design the digital systems as an activity in a larger systems design context.
- Study the design and operation of semiconductor memories frequently used in application specific digital system.
- Inspect how effectively IC's are embedded in package and assembled in PCB's for different application.
- Design and diagnosis of processors and I/O controllers used in embedded systems.

Module -1	RBT Level
Introduction and Methodology: Digital Systems and Embedded Systems, Real-World Circuits, Models, Design Methodology (1.1, 1.3 to 1.5 of Text). **Combinational Basics:** Combinational Components and Circuits, Verification of Combinational Circuits.(2.3 and 2.4 of Text) **Sequential Basics**: Sequential Datapaths and Control Clocked Synchronous Timing Methodology (4.3 up to 4.3.1,4.4 up to 4.4.1 of Text).	L1, L2, L3
Module -2	
Memories: Concepts, Memory Types, Error Detection and Correction (Chap 5 of Text).	L1, L2, L3
Module -3	
Implementation Fabrics: Integrated Circuits, Programmable Logic Devices, Packaging and Circuit boards, Interconnection and Signal integrity (Chap 6 of Text).	L1, L2, L3
Module -4	
I/O interfacing: I/O devices, I/O controllers, Parallel Buses, Serial Transmission, I/O software (Chap 8 of Text).	L1, L2, L3
Module -5	
Design Methodology: Design flow, Design optimization, Design for test, Nontechnical Issues (Chap 10 of Text).	L1, L2, L3, L4

Course outcomes: After studying this course, students will be able to:

- Construct the combinational circuits, using discrete gates and programmable logic devices.
- Describe Verilog model for sequential circuits and test pattern generation.

- Design a semiconductor memory for specific chip design.
- Design embedded systems using small microcontrollers, larger CPUs/DSPs, or hard or soft processor cores.
- Synthesize different types of processor and I/O controllers that are used in embedded system.

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks. There will be 2 full questions (with a maximum of Three sub questions from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Peter J. Ashenden, “Digital Design: An Embedded Systems Approach Using VERILOG”, Elesvier, 2010.

B.E E&C SEVENTH SEMESTER SYLLABUS

MICROWAVES AND ANTENNAS

B.E., VII Semester, Electronics &Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Course Code	**15EC71**	**IA Marks**	**20**
Number of Lecture Hours/Week	**04**	**Exam Marks**	**80**
Total Number of Lecture Hours	**50 (10 Hours / Module)**	**Exam Hours**	**03**
CREDITS – 04			

Course objectives: This course will enable students to:
- Describe the microwave properties and its transmission media
- Describe microwave devices for several applications
- Understand the basics of antenna theory
- Select antennas for specific applications

Module-1

Microwave Tubes: Introduction, Reflex Klystron Oscillator, Mechanism of Oscillations, Modes of Oscillations, Mode Curve (Qualitative Analysis only). (Text 1: 9.1, 9.2.2)
Microwave Transmission Lines: Microwave Frequencies, Microwave devices, Microwave Systems, Transmission Line equations and solutions, Reflection Coefficient and Transmission Coefficient, Standing Wave and Standing Wave Ratio, Smith Chart, Single Stub matching. (Text 2: 0.1, 0.2, 0.3, 3.1, 3.2, 3.3, 3.5, 3.6 Except Double stub matching) **L1, L2**

Module-2

Microwave Network theory: Symmetrical Z and Y-Parameters for Reciprocal Networks, S matrix representation of Multi-Port Networks. (Text 1: 6.1, 6.2, 6.3)
Microwave Passive Devices: Coaxial Connectors and Adapters, Attenuators, Phase Shifters, Waveguide Tees, Magic tees. (Text 1: 6.4.2, 6.4.14, 6.4.15, 6.4.16) **L1, L2**

Module-3

Strip Lines: Introduction, Micro Strip lines, Parallel Strip lines, Coplanar Strip lines, Shielded Strip Lines. (Text 2: Chapter 11)
Antenna Basics: Introduction, Basic Antenna Parameters, Patterns, Beam Area, Radiation Intensity, Beam Efficiency, Directivity and Gain, Antenna Apertures, Effective Height, Bandwidth, Radio Communication Link, Antenna Field Zones & Polarization. (Text 3: 2.1- 2.11, 2.13,2.15) **L1, L2, L3**

Module-4
Point Sources and Arrays: Introduction, Point Sources, Power Patterns, Power Theorem, Radiation Intensity, Field Patterns, Phase Patterns, Arrays of Two Isotropic Point Sources, Pattern Multiplication, Linear Arrays of n Isotropic Point Sources of equal Amplitude and Spacing.(Text 3: 5.1 – 5.10,5.13) **Electric Dipoles:** Introduction, Short Electric Dipole, Fields of a Short Dipole (General and Far Field Analyses), Radiation Resistance of a Short Dipole, Thin Linear Antenna (Field Analyses), Radiation Resistances of Lambda/2 Antenna. (Text 3: 6.1 -6.6) **L1, L2, L3, L4**
Module-5
Loop and Horn Antenna: Introduction, Small loop, Comparison of Far fields of Small Loop and Short Dipole, The Loop Antenna General Case, Far field Patterns of Circular Loop Antenna with Uniform Current, Radiation Resistance of Loops, Directivity of Circular Loop Antennas with Uniform Current, Horn antennas Rectangular Horn Antennas.(Text 3: 7.1-7.8, 7.19, 7.20) **Antenna Types:** Helical Antenna, Helical Geometry, Practical Design Considerations of Helical Antenna, Yagi-Uda array, Parabola General Properties, Log Periodic Antenna. (Text 3: 8.3, 8.5, 8.8, 9.5, 11.7) **L1, L2, L3**
Course Outcomes: At the end of the course, students will be able to: • Describe the use and advantages of microwave transmission • Analyze various parameters related to microwave transmission lines and waveguides • Identify microwave devices for several applications • Analyze various antenna parameters necessary for building an RF system • Recommend various antenna configurations according to the applications
Text Books: 1. **Microwave Engineering** – Annapurna Das, Sisir K Das TMH Publication, 2nd, 2010. 2. **Microwave Devices and circuits**- Liao, Pearson Education. 3. **Antennas and Wave Propagation,** John D. Krauss, Ronald J Marhefka and Ahmad S Khan,4th Special Indian Edition , McGraw- Hill Education Pvt. Ltd., 2010.
Reference Books: 1. **Microwave Engineering** – David M Pozar, John Wiley India Pvt. Ltd. 3rdEdn, 2008. 2. **Microwave Engineering** – Sushrut Das, Oxford Higher Education, 2ndEdn, 2015. 3. **Antennas and Wave Propagation** – Harish and Sachidananda: Oxford University Press, 2007.

DIGITAL IMAGE PROCESSING

B.E., VII Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC72	IA Marks	20
Number of Lecture Hours/Week	04	Exam Marks	80
Total Number of Lecture Hours	50 (10 Hours / Module)	Exam Hours	03
CREDITS – 04			

Course Objectives: The objectives of this course are to:

- Understand the fundamentals of digital image processing
- Understand the image transform used in digital image processing
- Understand the image enhancement techniques used in digital image processing
- Understand the image restoration techniques and methods used in digital image processing
- Understand the Morphological Operations and Segmentation used in digital image processing

Module-1	RBT Level
Digital Image Fundamentals: What is Digital Image Processing?, Origins of Digital Image Processing, Examples of fields that use DIP, Fundamental Steps in Digital Image Processing, Components of an Image Processing System, Elements of Visual Perception, Image Sensing and Acquisition, Image Sampling and Quantization, Some Basic Relationships Between Pixels, Linear and Nonlinear Operations. [Text: Chapter 1 and Chapter 2: Sections 2.1 to 2.5, 2.6.2]	L1, L2
Module-2	
Spatial Domain: Some Basic Intensity Transformation Functions, Histogram Processing, Fundamentals of Spatial Filtering, Smoothing Spatial Filters, Sharpening Spatial Filters **Frequency Domain**: Preliminary Concepts, The Discrete Fourier Transform (DFT) of Two Variables, Properties of the 2-D DFT, Filtering in the Frequency Domain, Image Smoothing and Image Sharpening Using Frequency Domain Filters, Selective Filtering. [Text: Chapter 3: Sections 3.2 to 3.6 and Chapter 4: Sections 4.2, 4.5 to 4.10]	L1, L2, L3
Module-3	
Restoration: Noise models, Restoration in the Presence of Noise Only using Spatial Filtering and Frequency Domain Filtering, Linear, Position-Invariant Degradations, Estimating the Degradation Function, Inverse Filtering, Minimum Mean Square Error (Wiener) Filtering, Constrained Least Squares Filtering. [Text: Chapter 5: Sections 5.2, to 5.9]	L1, L2, L3
Module-4	

Color Image Processing: Color Fundamentals, Color Models, Pseudocolor Image Processing. **Wavelets:** Background, Multiresolution Expansions. **Morphological Image Processing:** Preliminaries, Erosion and Dilation, Opening and Closing, The Hit-or-Miss Transforms, Some Basic Morphological Algorithms. [Text: Chapter 6: Sections 6.1 to 6.3, Chapter 7: Sections 7.1 and 7.2, Chapter 9: Sections 9.1 to 9.5]	L1, L2, L3
Module-5	
Segmentation: Point, Line, and Edge Detection, Thresholding, Region-Based Segmentation, Segmentation Using Morphological Watersheds. **Representation and Description:** Representation, Boundary descriptors. [Text: Chapter 10: Sections 10.2, to 10.5 and Chapter 11: Sections 11.1 and 11.2]	L1, L2, L3

Course Outcomes: At the end of the course students should be able to:

- Understand image formation and the role human visual system plays in perception of gray and color image data.
- Apply image processing techniques in both the spatial and frequency (Fourier) domains.
- Design image analysis techniques in the form of image segmentation and to evaluate the Methodologies for segmentation.
- Conduct independent study and analysis of Image Enhancement techniques.

Question paper pattern:

- The question paper will have ten questions.
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Digital Image Processing- Rafel C Gonzalez and Richard E. Woods, PHI 3rd Edition 2010.

Reference Books:

1. **Digital Image Processing**- S.Jayaraman, S.Esakkirajan, T.Veerakumar, Tata McGraw Hill 2014.
2. **Fundamentals of Digital Image Processing**-A. K. Jain, Pearson 2004.

POWER ELECTRONICS

B.E., VII Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

POWER ELECTRONICS

B.E., VII Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) Scheme]

Course Code	**15EC73**	**IA Marks**	**20**
Number of Lecture Hours/Week	**04**	**Exam Marks**	**80**
Total Number of Lecture Hours	**50 (10 Hours / Module)**	**Exam Hours**	**03**

CREDITS – 04

Course Objectives: This course will enable students to:

- Understand the construction and working of various power devices.
- Study and analysis of thyristor circuits with different triggering conditions.
- Learn the applications of power devices in controlled rectifiers, converters and inverters.
- Study of power electronics circuits under various load conditions.

Module-1

Introduction - Applications of Power Electronics, Power Semiconductor Devices, Control Characteristics of Power Devices, types of Power Electronic Circuits, Peripheral Effects.
Power Transistors: Power BJTs: Steady state characteristics. Power MOSFETs: device operation, switching characteristics, IGBTs: device operation, output and transfer characteristics, di/dt and dv/dt limitations. (Text 1) **L1, L2**

Module-2

Thyristors - Introduction, Principle of Operation of SCR, Static Anode-Cathode Characteristics of SCR, Two transisitor model of SCR, Gate Characteristics of SCR, Turn-ON Methods, Turn-OFF Mechanism, Turn-OFF Methods: Natural and Forced Commutation – Class A and Class B types, Gate Trigger Circuit: Resistance Firing Circuit, Resistance capacitance firing circuit, UJT Firing Circuit. (Text 2) **L1, L2, L3**

Module-3

Controlled Rectifiers - Introduction, Principle of Phase-Controlled Converter Operation, Single-Phase Full Converter with RL Load, Single-Phase Dual Converters, Single-Phase Semi Converter with RL load.
AC Voltage Controllers - Introduction, Principles of ON-OFF Control, Principle of Phase Control, Single phase controllers with resistive and inductive loads. (Text 1) **L1, L2, L3**

Module-4

DC-DC Converters - Introduction, principle of step-down operation and it's analysis with RL load, principle of step-up operation, Step-up converter with a resistive load, Performance parameters, Converter classification, Switching mode regulators: Buck regulator, Boost regulator, Buck-Boost Regulators, Chopper circuit design. (Text 1) **L1, L2**

Module-5

Pulse Width Modulated Inverters- Introduction, principle of operation, performance parameters, Single phase bridge inverters, voltage control of single phase inverters, current source inverters, Variable DC-link inverter, Boost inverter, Inverter circuit design.
Static Switches: Introduction, Single phase AC switches, DC Switches, Solid state

relays, Microelectronic relays. (Text 1) **L1, L2**

Course Outcomes: At the end of the course students should be able to:

- Describe the characteristics of different power devices and identify the various applications associated with it.
- Illustrate the working of power circuit as DC-DC converter.
- Illustrate the operation of inverter circuit and static switches.
- Determine the output response of a thyristor circuit with various triggering options.
- Determine the response of controlled rectifier with resistive and inductive loads.

Evaluation of Internal Assessment Marks:

It is suggested that at least 4 experiments of Power Electronics to be conducted by the students. This activity can be considered for the evaluation of 10 marks out of 40 Continuous Internal Evaluation marks, reserved for the other activities.

Text Books:

1. Mohammad H Rashid, Power Electronics, Circuits, Devices and Applications, 3rd/4th Edition, Pearson Education Inc, 2014, ISBN: 978-93-325-1844-5.
2. M.D Singh and K B Khanchandani, Power Electronics, 2nd Edition, Tata Mc-Graw Hill, 2009, ISBN: 0070583897

Reference Books:

1. L. Umanand, Power Electronics, Essentials and Applications, John Wiley India Pvt. Ltd, 2009.
2. Dr. P. S. Bimbhra, “Power Electronics”, Khanna Publishers, Delhi, 2012.
3. P.C. Sen, “Modern Power Electronics”, S Chand & Co New Delhi, 2005.
4. Earl Gose, Richard Johnsonbaugh, Steve Jost, Pattern Recognition and Image Analysis, ePub eBook.

MULTIMEDIA COMMUNICATION

B.E., VII Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based credit System (CBCS) Scheme

Subject Code	15EC741	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (08 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course objectives: This course will enable students to:

- Gain fundamental knowledge in understanding the basics of different multimedia networks and applications.
- Understand digitization principle techniques required to analyze different media types.
- Analyze compression techniques required to compress text and image and gain knowledge of DMS.
- Analyze compression techniques required to compress audio and video.
- Gain fundamental knowledge about multimedia communication across different networks.

Module-1	RBT Level
Multimedia Communications: Introduction, Multimedia information representation, multimedia networks, multimedia applications, Application and networking terminology. (Chap 1 of Text 1)	L1, L2
Module-2	
Information Representation: Introduction, Digitization principles, Text, Images, Audio and Video (Chap 2 of Text 1)	L1, L2
Module-3	
Text and image compression: Introduction, Compression principles, text compression, image Compression. (Chap 3 of Text 1) **Distributed multimedia systems:** Introduction, main Features of a DMS, Resource management of DMS, Networking, Multimedia operating systems (Chap. 4 - Sections 4.1 to 4.5 of Text 2).	L1, L2, L3
Module-4	
Audio and video compression: Introduction, Audio compression, video compression, video compression principles, video compression. (Chap. 4 of Text 1).	L1, L2, L3
Module-5	
Multimedia Communication Across Networks: Packet audio/video in the network environment, Video transport across generic networks, Multimedia Transport across ATM Networks (Chap. 6 - Sections 6.1, 6.2, 6.3 of Text 2).	L1, L2

Course Outcomes: After studying this course, students will be able to:

- Understand basics of different multimedia networks and applications.
- Understand different compression techniques to compress audio and video.
- Describe multimedia Communication across Networks.
- Analyse different media types to represent them in digital form.
- Compress different types of text and images using different compression techniques and analyse DMS.

Question paper pattern:

- The question paper will have ten questions.
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Fred Halsall, “Multimedia Communications”, Pearson education, 2001 ISBN - 9788131709948.

2. K. R. Rao, Zoran S. Bojkovic, Dragorad A. Milovanovic, “Multimedia Communication Systems”, Pearson education, 2004. ISBN -9788120321458

Reference Book:

Raifsteinmetz, Klara Nahrstedt, “Multimedia: Computing, Communications and Applications”, Pearson education, 2002. ISBN -9788177584417

BIOMEDICAL SIGNAL PROCESSING
B.E., VII Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC742	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03

CREDITS – 03

Course Objectives: The objectives of this course are to:

- Describe the origin, properties and suitable models of important biological signals such as ECG and EEG.
- Introduce students to basic signal processing techniques in analysing biological signals.
- Develop the students mathematical and computational skills relevant to the field of biomedical signal processing.
- Develop a thorough understanding on basics of ECG signal compression algorithms.
- Increase the student's awareness of the complexity of various biological phenomena and cultivate an understanding of the promises, challenges of the biomedical engineering.

Module-1	RBT Level
Introduction to Biomedical Signals: The nature of Biomedical Signals, Examples of Biomedical Signals, Objectives and difficulties in Biomedical analysis. **Electrocardiography:** Basic electrocardiography, ECG lead systems, ECG signal characteristics. **Signal Conversion** :Simple signal conversion systems, Conversion requirements for biomedical signals, Signal conversion circuits (Text-1)	L1, L2
Module-2	
Signal Averaging: Basics of signal averaging, signal averaging as a digital filter, a typical averager, software for signal averaging, limitations of signal averaging. **Adaptive Noise Cancelling:** Principal noise canceller model, 60-Hzadaptive cancelling using a sine wave model, other applications of adaptive filtering (Text-1)	L1, L2, L3
Module-3	
Data Compression Techniques: Turning point algorithm, AZTEC algorithm, Fan algorithm, Huffman coding, data reduction algorithms The Fourier transform, Correlation, Convolution, Power spectrum estimation, Frequency domain analysis of the ECG (Text-1)	L1, L2, L3
Module-4	

Cardiological signal processing: Basic Electrocardiography, ECG data acquisition, ECG lead system, ECG signal characteristics (parameters and their estimation), Analog filters, ECG amplifier, and QRS detector, Power spectrum of the ECG, Bandpass filtering techniques, Differentiation techniques, Template matching techniques, A QRS detection algorithm, Realtime ECG processing algorithm, ECG interpretation, ST segment analyzer, Portable arrhythmia monitor. (Text -2)	L1, L2, L3
Module-5	
Neurological signal processing: The brain and its potentials, The electrophysiological origin of brain waves, The EEG signal and its characteristics (EEG rhythms, waves, and transients), Correlation. **Analysis of EEG channels:** Detection of EEG rhythms, Template matching for EEG, spike and wave detection (Text-2).	L1, L2, L3

Course outcomes: At the end of the course, students will be able to:

- Possess the basic mathematical, scientific and computational skills necessary to analyse ECG and EEG signals.
- Apply classical and modern filtering and compression techniques for ECG and EEG signals
- Develop a thorough understanding on basics of ECG and EEG feature extraction.

Question paper pattern:

- The question paper will have ten questions.
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. **Biomedical Digital Signal Processing-** Willis J. Tompkins, PHI 2001.
2. **Biomedical Signal Processing Principles and Techniques-** D C Reddy, McGraw-Hill publications 2005

Reference Book:

Biomedical Signal Analysis-Rangaraj M. Rangayyan, John Wiley & Sons 2002

REAL TIME SYSTEMS

B.E., VII Semester, Electronics & Communication Engineering /Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC743	IA Marks	20
Number of Lecture Hours/Week	03	Exam marks	80
Total Number of Lecture Hours	40 (08 Hours per Module)	Exam Hours	03
Credits – 03			

Course Objectives: This Course will enable students to:

- Discuss the historical background of Real-time systems and its classifications.
- Describe the concepts of computer control and hardware components for Real-Time Application.
- Discuss the languages to develop software for Real-Time Applications.
- Explain the concepts of operating system and RTS development methodologies.

Modules	RBT Level
Module-1	
Introduction to Real-Time Systems: Historical background, Elements of a Computer Control System, RTS- Definition, Classification of Real-time Systems, Time Constraints, Classification of Programs. **Concepts of Computer Control:** Introduction, Sequence Control, Loop Control, Supervisory Control, Centralized Computer Control, Hierarchical Systems. (Text Book: 1.1 to 1.6 and 2.1 to 2.6)	**L1, L2**
Module-2	
Computer Hardware Requirements for Real-Time Applications: Introduction, General Purpose Computer, Single Chip Microcomputers and Microcontrollers, Specialized Processors, Process-Related Interfaces, Data Transfer Techniques, Communications, Standard Interface.(Text Book: 3.1 to 3.8)	**L1, L2**
Module-3	
Languages for Real-Time Applications: Introduction, Syntax Layout and Readability, Declaration and Initialization of Variables and Constants, Modularity and Variables, Compilation of Modular Programs, Data types, Control Structures, Exception Handling, Low-level facilities, Co-routines, Interrupts and Device Handling, Concurrency, Real-Time Support, Overview of Real-Time Languages. (Text Book: 5.1 to 5.14)	**L1, L2, L3**
Module-4	
Operating Systems: Introduction, Real-Time Multi-Tasking OS, Scheduling Strategies, Priority Structures, Task Management, Scheduler and Real-Time Clock Interrupt Handler, Memory Management, Code Sharing, Resource Control, Task Co-Operation and Communication, Mutual Exclusion.(Text Book: 6.1 to 6.11)	**L1, L2**

Module-5	
Design of RTS – General Introduction: Introduction, Specification Document, Preliminary Design, Single-Program Approach, Foreground/Background System. **RTS Development Methodologies:** Introduction, Yourdon Methodology, Ward and Mellor Method, Hately and Pirbhai Method. (Text Book: 7.1 to 7.5 and 8.1, 8.2, 8.4,8.5)	**L1, L2, L3**

Course Outcomes: At the end of the course, students should be able to:

- Understand the fundamentals of Real time systems and its classifications.
- Understand the concepts of computer control, operating system and the suitable computer hardware requirements for real-time applications.
- Develop the software languages to meet Real time applications.
- Apply suitable methodologies to design and develop Real-Time Systems.

Question Paper Pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Real-Time Computer Control, by Stuart Bennet, 2nd Edn. Pearson Education. 2008.

Reference Books:

1. C.M. Krishna, Kang G. Shin, “Real –Time Systems”, McGraw –Hill International Editions, 1997.
2. Real-Time Systems Design and Analysis, Phillip. A. Laplante, second edition, PHI, 2005.
3. Embedded Systems, Raj Kamal, Tata McGraw Hill, India, third edition, 2005.

Cryptography

B.E., VII Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC744	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (08 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This Course will enable students to:

- Enable students to understand the basics of symmetric key and public key cryptography.
- Equip students with some basic mathematical concepts and pseudorandom number generators required for cryptography.
- Enable students to authenticate and protect the encrypted data.
- Enrich knowledge about Email, IP and Web security.

Modules	
Module-1	**RBT Level**
Basic Concepts of Number Theory and Finite Fields: Divisibility and the divisibility algorithm, Euclidean algorithm, Modular arithmetic, Groups, Rings and Fields, Finite fields of the form GF(p), Polynomial arithmetic, Finite fields of the form GF(2^n)(Text 1: Chapter 3)	L1, L2
Module-2	
Classical Encryption Techniques: Symmetric cipher model, Substitution techniques, Transposition techniques, Steganography (Text 1: Chapter 1) **SYMMETRIC CIPHERS:** Traditional Block Cipher structure, Data Encryption Standard (DES) (Text 1: Chapter 2: Section1, 2)	L1, L2
Module-3	
SYMMETRIC CIPHERS: The AES Cipher. (Text 1: Chapter 4: Section 2, 3, 4) **Pseudo-Random-Sequence Generators and Stream Ciphers:** Linear Congruential Generators, Linear Feedback Shift Registers, Design and analysis of stream ciphers, Stream ciphers using LFSRs (Text 2: Chapter 16: Section 1, 2, 3, 4)	L1, L2, L3
Module-4	
More number theory: Prime Numbers, Fermat's and Euler's theorem, Primality testing, Chinese Remainder theorem, discrete logarithm. (Text 1: Chapter 7) **Principles of Public-Key Cryptosystems**: The RSA algorithm, Diffie - Hellman Key Exchange, Elliptic Curve Arithmetic, Elliptic Curve Cryptography (Text 1: Chapter 8, Chapter 9: Section 1, 3, 4)	L1, L2, L3
Module-5	

One-Way Hash Functions: Background, Snefru, N-Hash, MD4, MD5, Secure Hash Algorithm [SHA],One way hash functions using symmetric block algorithms, Using public key algorithms, Choosing a one-way hash functions, Message Authentication Codes. Digital Signature Algorithm, Discrete Logarithm Signature Scheme (Text 2: Chapter 18: Section 18.1 to 18.5, 18.7, 18.11 to 18.14 and Chapter 20: Section 20.1, 20.4)	L1, L2, L3

Course Outcomes: After studying this course, students will be able to:

- Use basic cryptographic algorithms to encrypt the data.
- Generate some pseudorandom numbers required for cryptographic applications.
- Provide authentication and protection for encrypted data.

Question paper pattern:

- The question paper will have 10 full questions carrying equal marks.
- Each full question consists of 16 marks with a maximum of Three sub questions.
- There will be 2 full questions from each module covering all the topics of the module
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. William Stallings , "Cryptography and Network Security Principles and Practice", Pearson Education Inc., 6th Edition, 2014, ISBN: 978-93-325-1877-3
2. Bruce Schneier, "Applied Cryptography Protocols, Algorithms, and Source code in C", Wiley Publications, 2nd Edition, ISBN: 9971-51-348-X

Reference Books:

1. Cryptography and Network Security, Behrouz A. Forouzan, TMH, 2007.
2. Cryptography and Network Security, Atul Kahate, TMH, 2003.

CAD for VLSI

B.E., VII Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC745	IA Marks	20
Number of Lecture Hours/Week	03	Exam marks	80
Total Number of Lecture Hours	40 (8 Hours per Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to:

- Understand various stages of Physical design of VLSI circuits
- Know about mapping a design problem to a realizable algorithm
- Become aware of graph theoretic, heuristic and genetic algorithms
- Compare performance of different algorithms

Modules	RBT Level
Module 1	
Data Structures and Basic Algorithms: Basic terminology, Complexity issues and NP-Hardness. Examples - Exponential, heuristic, approximation and special cases. Basic Algorithms. Graph Algorithms for Search, spanning tree, shortest path, min-cut and max-cut, Steiner tree. Computational Geometry Algorithms: Line sweep and extended line sweep methods.	L1, L2
Module 2	
Basic Data Structures. Atomic operations for layout editors, Linked list of blocks, Bin-based method, Neighbor pointers, corner-stitching, Multi-layer operations, Limitations of existing data structures. Layout specification languages. **Graph algorithms for physical design**: Classes of graphs in physical design, Relationship between graph classes, Graph problems in physical design, Algorithms for Interval graphs, permutation graphs and circle graphs.	L1, L2
Module 3	

Partitioning: Problem formulation, Design style specific partitioning problems, Classification of Partitioning Algorithms. Group migration algorithms: Kernighan-Lin algorithm, Fiduccia-Mattheyses Algorithm, Simulated Annealing, Simulated Evolution. **Floor Planning:** Problem formulation, Constraint based floor planning, Rectangular dualization, Simulated evolution algorithms.	L1, L2,L3
Module 4	
Pin Assignment: Problem formulation. Classification of pin assignment problems, General pin assignment problem. **Placement:** Problem formulation, Classification of placement algorithms. Simulation based placement: Simulated annealing, simulated evolution, force directed placement. Partitioning based algorithms: Breur's Algorithm, Terminal propagation algorithm, Other algorithms for placement.	L1,L2,L3
Module 5	
Global Routing: Problem formulation, Classification of Global routing algorithms, Maze routing algorithms: Lee's algorithm, Soukup's algorithm and Hadlock's Algorithm, Line probe algorithms. **Detailed Routing:** Problem formulation, Routing considerations, models, channel routing and switch box routing problems. General river routing problem, Single row routing problem. Two-layer channel routing algorithms: Basic Left Edge Algorithm, Dogleg router, Symbolic router-YACR2.	L1,L2,L3
Course Outcomes: After studying this course, students will be able to: • Appreciate the problems related to physical design of VLSI • Use genralized graph theoretic approach to VLSI problems • Design Simulated Annealing and Evolutionary algorithms • Know various approaches to write generalized algorithms	
Question paper pattern: • The question paper will have 10 full questions carrying equal marks. • Each full question consists of 16 marks with a maximum of Three sub questions. • There will be 2 full questions from each module covering all the topics of the module • The students will have to answer 5 full questions, selecting one full question from each module.	

Text Book:

Algorithms for VLSI Physical Design Automation, 3rd Ed, Naveed Sherwani, 1999 Kluwer Academic Publishers, Reprint 2009 Springer (India) Private Ltd. ISBN 978-81-8128-317-7.

DSP Algorithms and Architecture
B.E., VII Semester, Electronics & Communication Engineering /Telecommunication Engineering
[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC751	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to:

- Figure out the knowledge and concepts of digital signal processing techniques.
- Understand the computational building blocks of DSP processors and its speed issues.
- Understand the various addressing modes, peripherals, interrupts and pipelining structure of TMS320C54xx processor.
- Learn how to interface the external devices to TMS320C54xx processor in various modes.
- Understand basic DSP algorithms with their implementation.

Module-1	RBT Level
Introduction to Digital Signal Processing: Introduction, A Digital Signal – Processing System, The Sampling Process, Discrete Time Sequences, Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear Time-Invariant Systems, Digital Filters, Decimation and Interpolation. **Computational Accuracy in DSP Implementations:** Number Formats for Signals and Coefficients in DSP Systems, Dynamic Range and Precision, Sources of Error in DSP Implementation.	L1, L2
Module-2	
Architectures for Programmable Digital Signal – Processing Devices: Introduction, Basic Architectural Features, DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation Unit, Programmability and Program Execution, Speed Issues, Features for External Interfacing.	L1, L2, L3
Module-3	
Programmable Digital Signal Processors: Introduction, Commercial Digital Signal-processing Devices, Data Addressing Modes of TMS32OC54XX, Memory Space of TMS32OC54xx Processors, Program Control. Detail Study of TMS320C54X & 54xx Instructions and Programming, On – Chip Peripherals, Interrupts of TMS32OC54XX Processors, Pipeline Operation of TMS32OC54xx Processor.	L1, L2, L3
Module-4	

Implementation of Basic DSP Algorithms: Introduction, The Q – notation, FIR Filters, IIR Filters, Interpolation and Decimation Filters (one example in each case). **Implementation of FFT Algorithms:** Introduction, An FFT Algorithm for DFT Computation, Overflow and Scaling, Bit – Reversed Index. Generation & Implementation on the TMS32OC54xx.	L1, L2, L3
Module-5	
Interfacing Memory and Parallel I/O Peripherals to Programmable DSP Devices: Introduction, Memory Space Organization, External Bus Interfacing Signals. Memory Interface, Parallel I/O Interface, Programmed I/O, Interrupts and I/O Direct Memory Access (DMA). **Interfacing and Applications of DSP Processors:** Introduction, Synchronous Serial Interface, A CODEC Interface Circuit, DSP Based Bio-telemetry Receiver, A Speech Processing System, An Image Processing System.	L1, L2, L3
Course Outcomes: At the end of this course, students would be able to • Comprehend the knowledge and concepts of digital signal processing techniques. • Apply the knowledge of DSP computational building blocks to achieve speed in DSP architecture or processor. • Apply knowledge of various types of addressing modes, interrupts, peripherals and pipelining structure of TMS320C54xx processor. • Develop basic DSP algorithms using DSP processors. • Discuss about synchronous serial interface and multichannel buffered serial port (McBSP) of DSP device. • Demonstrate the programming of CODEC interfacing.	

Question paper pattern:

- The question paper will have 10 full questions carrying equal marks.
- Each full question consists of 16 marks with a maximum of Three sub questions.
- There will be 2 full questions from each module covering all the topics of the module
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

"Digital Signal Processing", Avatar Singh and S. Srinivasan, Thomson Learning, 2004.

Reference Books:

1. "Digital Signal Processing: A practical approach", Ifeachor E. C., Jervis B. W Pearson-Education, PHI, 2002.
2. "Digital Signal Processors", B Venkataramani and M Bhaskar, TMH, 2nd, 2010
3. "Architectures for Digital Signal Processing", Peter Pirsch John Weily, 2008

IoT & WIRELESS SENSOR NETWORKS

B.E., VII Semester, Electronics & Communication Engineering /Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC752	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to:

- Understand various sources of IoT & M2M communication protocols.
- Describe Cloud computing and design principles of IoT.
- Become aware of MQTT clients, MQTT server and its programming.
- Understand the architecture and design principles of WSNs.
- Enrich the knowledge about MAC and routing protocols in WSNs.

Module-1	RBT Level
Overview of Internet of Things: IoT Conceptual Framework, IoT Architectural View, Technology Behind IoT, Sources of IoT,M2M communication, Examples of IoT. Modified OSI Model for the IoT/M2M Systems, data enrichment, data consolidation and device management at IoT/M2M Gateway, web communication protocols used by connected IoT/M2M devices, Message communication protocols (CoAP-SMS, CoAP-MQ, MQTT,XMPP) for IoT/M2M devices.	L1, L2
Module-2	
Architecture and Design Principles for IoT: Internet connectivity, Internet-based communication,IPv4, IPv6,6LoWPAN protocol, IP Addressing in the IoT, Application layer protocols: HTTP, HTTPS,FTP,TELNET and ports. **Data Collection, Storage and Computing using a Cloud Platform:** Introduction, Cloud computing paradigm for data collection, storage and computing, Cloud service models, IoT Cloud- based data collection, storage and computing services using Nimbits.	L1, L2
Module-3	

Prototyping and Designing Software for IoT Applications: Introduction, Prototyping Embedded device software, Programming Embedded Device Arduino Platform using IDE, Reading data from sensors and devices, Devices, Gateways, Internet and Web/Cloud services software development. Programming MQTT clients and MQTT server. Introduction to IoT privacy and security. Vulnerabilities, security requirements and threat analysis, IoT Security Tomography and layered attacker model.	L1, L2, L3
Module-4	
Overview of Wireless Sensor Networks: Challenges for Wireless Sensor Networks, Enabling Technologies for Wireless Sensor Networks. **Architectures**: Single-Node Architecture - Hardware Components, Energy Consumption of Sensor Nodes, Operating Systems and Execution Environments, Network Architecture-Sensor Network Scenarios, Optimization Goals and Figures of Merit, Design principles for WSNs, Service interfaces of WSNs Gateway Concepts.	L1, L2, L3
Module-5	
Communication Protocols: Physical Layer and Transceiver Design Considerations, MAC Protocols for Wireless Sensor Networks, Low Duty Cycle Protocols And Wakeup Concepts - S-MAC , The Mediation Device Protocol, Wakeup Radio Concepts, Contention based protocols(CSMA,PAMAS), Schedule based protocols (LEACH, SMACS, TRAMA) Address and Name Management in WSNs, Assignment of MAC Addresses, Routing Protocols- Energy-Efficient Routing, Geographic Routing, Hierarchical networks by clustering.	L1, L2, L3
Course Outcomes: At the end of the course, students will be able to: • Describe the OSI Model for the IoT/M2M Systems. • Understand the architecture and design principles for IoT. • Learn the programming for IoT Applications. • Identify the communication protocols which best suits the WSNs.	

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. Raj Kamal, ”Internet of Things-Architecture and design principles”, McGraw Hill Education.
2. Holger Karl & Andreas Willig, "Protocols And Architectures for Wireless Sensor Networks" , John Wiley, 2005.
3. Feng Zhao & Leonidas J. Guibas, “Wireless Sensor Networks- An Information Processing Approach", Elsevier, 2007.

Reference Books:

1. Kazem Sohraby, Daniel Minoli, & Taieb Znati, “Wireless Sensor Networks- Technology, Protocols, And Applications”, John Wiley, 2007.
2. Anna Hac, “Wireless Sensor Network Designs”, John Wiley, 2003.

PATTERN RECOGNITION

B.E., VII Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC753	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: The objectives of this course are to:

- Introduce mathematical tools needed for Pattern Recognition
- Impart knowledge about the fundamentals of Pattern Recognition.
- Provide knowledge of recognition, decision making and statistical learning problems
- Introduce parametric and non-parametric techniques, supervised learning and clustering concepts of pattern recognition

Modules

Module-1	RBT Level
Introduction: Importance of pattern recognition, Features, Feature Vectors, and Classifiers, Supervised, Unsupervised, and Semi-supervised learning, Introduction to Bayes Decision Theory, Discriminant Functions and Decision Surfaces, Gaussian PDF and Bayesian Classification for Normal Distributions.	L1, L2
Module-2	
Data Transformation and Dimensionality Reduction: Introduction, Basis Vectors, The Karhunen Loeve (KL) Transformation, Singular Value Decomposition, Independent Component Analysis (Introduction only). Nonlinear Dimensionality Reduction, Kernel PCA.	L1, L2
Module-3	
Estimation of Unknown Probability Density Functions: Maximum Likelihood Parameter Estimation, Maximum a Posteriori Probability estimation, Bayesian Interference, Maximum Entropy Estimation, Mixture Models, Naive-Bayes Classifier, The Nearest Neighbor Rule.	L1, L2, L3
Module-4	
Linear Classifiers: Introduction, Linear Discriminant Functions and Decision Hyperplanes, The Perceptron Algorithm, Mean Square Error Estimate, Stochastic Approximation of LMS Algorithm, Sum of Error Estimate.	L1, L2, L3
Module-5	
Nonlinear Classifiers: The XOR Problem, The two Layer Perceptron, Three Layer Perceptron, Back propagation Algorithm, Basic Concepts of Clustering, Introduction to Clustering , Proximity Measures.	L1, L2, L3

Course outcomes: At the end of the course, students will be able to:

- Identify areas where Pattern Recognition and Machine Learning can offer a solution.
- Describe the strength and limitations of some techniques used in computational Machine Learning for classification, regression and density estimation problems
- Describe genetic algorithms, validation methods and sampling techniques
- Describe and model data to solve problems in regression and classification
- Implement learning algorithms for supervised tasks

Question paper pattern:

The question paper will have ten questions.

- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Pattern Recognition: Sergios Theodoridis, Konstantinos Koutroumbas, Elsevier India Pvt. Ltd (Paper Back), 4th edition.

Reference Books:

1. **The Elements of Statistical Learning:** Trevor Hastie, Springer-Verlag New York, LLC (Paper Back), 2009.
2. **Pattern Classification:** Richard O. Duda, Peter E. Hart, David G. Stork. John Wiley & Sons, 2012.
3. **Pattern Recognition and Image Analysis Earl Gose:** Richard Johnsonbaugh, Steve Jost, ePub eBook.

ADVANCED COMPUTER ARCHITECTURE

B.E., VII Semester, Electronics & Communication Engineering /Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC754	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03

CREDITS – 03

Course Objectives: This course will enable students to:

- Understand the various parallel computer models and conditions of parallelism
- Explain the control flow, dataflow and demand driven machines
- Study CISC, RISC, superscalar, VLIW and multiprocessor architectures
- Understand the concept of pipelining and memory hierarchy design
- Explain cache coherence protocols.

Module	RBT Level
Module-1	
Parallel Computer Models: The state of computing, Classification of parallel computers, Multiprocessors and multicomputer, Multivectors and SIMD computers. **Program and Network Properties:** Conditions of parallelism, Data and resource Dependences, Hardware and software parallelism, Program partitioning and scheduling, Grain Size and latency.	L1, L2
Module-2	
Program flow mechanisms: Control flow versus data flow, Data flow Architecture, Demand driven mechanisms, Comparisons of flow mechanisms. **Principles of Scalable Performance**: Performance Metrics and Measures, Parallel Processing Applications, Speedup Performance Laws, Scalability Analysis and Approaches.	L1, L2, L3
Module-3	
Speedup Performance Laws: Amdhal's law, Gustafson's law, Memory bounded speed up model, Scalability Analysis and Approaches. **Advanced Processors:** Advanced processor technology, Instruction-set Architectures, CISC Scalar Processors, RISC Scalar Processors, Superscalar Processors, VLIW Architectures.	L1, L2, L3
Module-4	
Pipelining: Linear pipeline processor, nonlinear pipeline processor, Instruction pipeline Design, Mechanisms for instruction pipelining, Dynamic instruction scheduling, Branch Handling techniques, branch prediction, Arithmetic Pipeline Design. **Memory Hierarchy Design:** Cache basics & cache performance, reducing miss rate and miss penalty, multilevel cache hierarchies, main memory organizations, design of memory hierarchies.	L1, L2, L3

Module-5	
Multiprocessor Architectures: Symmetric shared memory architectures, distributed shared memory architectures, models of memory consistency, cache coherence protocols (MSI, MESI, MOESI), scalable cache coherence, overview of directory based approaches, design challenges of directory protocols, memory based directory protocols, cache based directory protocols.	L1, L2, L3

Course Outcomes: At the end of the course, the students will be able to:

- Explain parallel computer models and conditions of parallelism
- Differentiate control flow, dataflow, demand driven mechanisms
- Explain the principle of scalable performance
- Discuss advanced processors architectures like CISC, RISC, superscalar and VLIW
- Understand the basics of instruction pipelining and memory technologies
- Explain the issues in multiprocessor architectures

Question paper pattern:

The question paper will have ten questions.

- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Kai Hwang, “Advanced computer architecture”; TMH.

Reference Books:

1. Kai Hwang and Zu, “Scalable Parallel Computers Architecture”; MGH.
2. M.J Flynn, “Computer Architecture, Pipelined and Parallel Processor Design”; Narosa Publishing.
3. D.A.Patterson, J.L.Hennessy, “Computer Architecture :A quantitative approach”; Morgan Kauffmann Feb, 2002.

SATELLITE COMMUNICATION

B.E., VII Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS)]

Subject Code	15EC755	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to

- Understand the basic principle of satellite orbits and trajectories.
- Study of electronic systems associated with a satellite and the earth station.
- Understand the various technologies associated with the satellite communication.
- Focus on a communication satellite and the national satellite system.
- Study of satellite applications focusing various domains services such as remote sensing, weather forecasting and navigation.

Module-1	RBT Level
Satellite Orbits and Trajectories: Definition, Basic Principles, Orbital parameters, Injection velocity and satellite trajectory, Types of Satellite orbits, Orbital perturbations, Satellite stabilization, Orbital effects on satellite's performance, Eclipses, Look angles: Azimuth angle, Elevation angle.	L1, L2
Module-2	
Satellite subsystem: Power supply subsystem, Attitude and Orbit control, Tracking, Telemetry and command subsystem, Payload. **Earth Station:** Types of earth station, Architecture, Design considerations, Testing, Earth station Hardware, Satellite tracking.	L1, L2
Module-3	
Multiple Access Techniques: Introduction, FDMA (No derivation), SCPC Systems, MCPC Systems, TDMA, CDMA, SDMA. **Satellite Link Design Fundamentals**: Transmission Equation, Satellite Link Parameters, Propagation considerations.	L1, L2, L3
Module-4	
Communication Satellites: Introduction, Related Applications, Frequency Bands, Payloads, Satellite Vs. Terrestrial Networks, Satellite Telephony, Satellite Television, Satellite radio, Regional satellite Systems, National Satellite Systems.	L1, L2
Module-5	

Remote Sensing Satellites: Classification of remote sensing systems, orbits, Payloads, Types of images: Image Classification, Interpretation, Applications. **Weather Forecasting Satellites**: Fundamentals, Images, Orbits, Payloads, Applications. **Navigation Satellites**: Development of Satellite Navigation Systems, GPS system, Applications.	L1, L2, L3

Course Outcomes: At the end of the course, the students will be able to:

- Describe the satellite orbits and its trajectories with the definitions of parameters associated with it.
- Describe the electronic hardware systems associated with the satellite subsystem and earth station.
- Describe the various applications of satellite with the focus on national satellite system.
- Compute the satellite link parameters under various propagation conditions with the illustration of multiple access techniques.

Question Paper pattern:

- The Question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full Questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The Students will have to answer 5 full Questions, selecting one full Question from each module.

Text Book:

Anil K. Maini, Varsha Agrawal, Satellite Communications, Wiley India Pvt. Ltd., 2015, ISBN: 978-81-265-2071-8.

Reference Books :

1. Dennis Roddy, Satellite Communications, 4th Edition, McGraw- Hill International edition, 2006
2. Timothy Pratt, Charles Bostian, Jeremy Allnutt, Satellite Communications, 2nd Edition, Wiley India Pvt. Ltd , 2017, ISBN: 978-81-265-0833-4

ADVANCED COMMUNICATION LAB

B.E., VII Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15ECL76	IA Marks	20
Number of Lecture Hours/Week	01Hr Tutorial (Instructions) + 02 Hours Laboratory = 03	Exam Marks	80
RBT Levels	L1, L2, L3	Exam Hours	03
CREDITS – 02			

Course objectives: This course will enable students to:

- Design and demonstrate the digital modulation techniques
- Demonstrate and measure the wave propagation in microstrip antennas
- Characteristics of microstrip devices and measurement of its parameters.
- Model an optical communication system and study its characteristics.
- Simulate the digital communication concepts and compute and display various parameters along with plots/figures.

Laboratory Experiments

PART-A: Following Experiments No. 1 to 4 has to be performed using discrete components.

1. Time Division Multiplexing and Demultiplexing of two bandlimited signals.
2. ASK generation and detection
3. FSK generation and detection
4. PSK generation and detection
5. Measurement of frequency, guide wavelength, power, VSWR and attenuation in microwave test bench.
6. Measurement of directivity and gain of microstrip dipole and Yagi antennas.
7. Determination of
   a. Coupling and isolation characteristics of microstrip directional coupler.
   b. Resonance characteristics of microstrip ring resonator and computation of dielectric constant of the substrate.
   c. Power division and isolation of microstrip power divider.
8. Measurement of propagation loss, bending loss and numerical aperture of an optical fiber.

PART-B: Simulation Experiments using SCILAB/MATLAB/Simulink or LabView

1. Simulate NRZ, RZ, half-sinusoid and raised cosine pulses and generate eye diagram for binary polar signaling.

2. Simulate the Pulse code modulation and demodulation system and display the waveforms.

3. Simulate the QPSK transmitter and receiver. Plot the signals and its constellation diagram.

4. Test the performance of a binary differential phase shift keying system by simulating the non-coherent detection of binary DPSK.

Course outcomes: On the completion of this laboratory course, the students will be able to:

- Determine the characteristics and response of microwave devices and optical waveguide.
- Determine the characteristics of microstrip antennas and devices and compute the parameters associated with it.
- Simulate the digital modulation schemes with the display of waveforms and computation of performance parameters.
- Design and test the digital modulation circuits/systems and display the waveforms.

Conduct of Practical Examination:

- All laboratory experiments are to be considered for practical examination.
- For examination one question from **PART-A** and one question from **PART-B or** only one question from **PART-B** experiments based on the complexity, to be set.
- Students are allowed to pick one experiment from the lot.
- Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.
- Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

VLSI LAB

B.E., VII Semester, Electronics & Communication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15ECL77	IA Marks	20
Number of Lecture Hours/Week	01Hr Tutorial (Instructions) + 02 Hours Laboratory = 03	Exam Marks	80
RBT Levels	L1, L2, L3	Exam Hours	03

CREDITS – 02

Course objectives: This course will enable students to:

- Explore the CAD tool and understand the flow of the Full Custom IC design cycle.
- Learn DRC, LVS and Parasitic Extraction of the various designs.
- Design and simulate the various basic CMOS analog circuits and use them in higher circuits like data converters using design abstraction concepts.
- Design and simulate the various basic CMOS digital circuits and use them in higher circuits like adders and shift registers using design abstraction concepts.

Experiments can be conducted using any of the following or equivalent design tools: Cadence/Synopsis/Mentor Graphics/Microwind

Laboratory Experiments

PART - A

ASIC-DIGITAL DESIGN

1. Write Verilog Code for the following circuits and their Test Bench for verification, observe the waveform and synthesize the code with technological library with given constraints*. Do the initial timing verification with gate level simulation.
    i. An inverter
    ii. A Buffer
    iii. Transmission Gate
    iv. Basic/universal gates
    v. Flip flop -RS, D, JK, MS, T
    vi. Serial & Parallel adder
    vii. 4-bit counter [Synchronous and Asynchronous counter]
    viii. Successive approximation register [SAR]

PART - B
ANALOG DESIGN

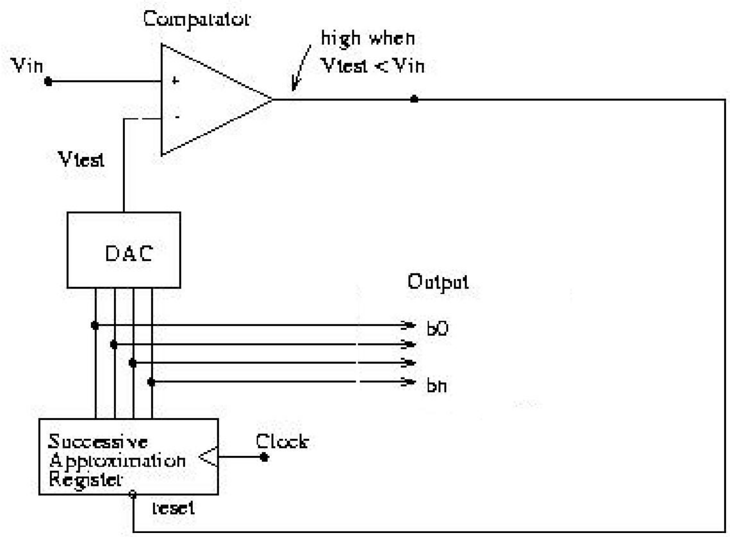
1. Design an Inverter with given specifications**, completing the design flow mentioned below:
   a. Draw the schematic and verify the following
      i) DC Analysis
      ii) Transient Analysis
   b. Draw the Layout and verify the DRC, ERC
   c. Check for LVS
   d. Extract RC and back annotate the same and verify the Design
   e. Verify & Optimize for Time, Power and Area to the given constraint*

2. Design the (i) Common source and Common Drain amplifier and (ii) A Single Stage differential amplifier, with given specifications**, completing the design flow mentioned below:
   a. Draw the schematic and verify the following
      i) DC Analysis
      ii) AC Analysis
      iii) Transient Analysis
   b. Draw the Layout and verify the DRC, ERC
   c. Check for LVS
   d. Extract RC and back annotate the same and verify the Design.

3. Design an op-amp with given specification** using given differential amplifier Common source and Common Drain amplifier in library*** and completing the design flow mentioned below:
   a. Draw the schematic and verify the following
      i) DC Analysis
      ii). AC Analysis
      iii) Transient Analysis
   b. Draw the Layout and verify the DRC, ERC
   c. Check for LVS
   d. Extract RC and back annotate the same and verify the Design.

4. Design a 4 bit R-2R based DAC for the given specification and completing the design flow mentioned using given op-amp in the library***.
   a. Draw the schematic and verify the following
      i) DC Analysis
      ii) AC Analysis
      iii) Transient Analysis
   b. Draw the Layout and verify the DRC, ERC

5. For the SAR based ADC mentioned in the figure below draw the mixed signal schematic and verify the functionality by completing ASIC Design FLOW. [Specifications to GDS-II]



* An appropriate constraint should be given.

** Appropriate specification should be given.

*** Applicable Library should be added & information should be given to the Designer.

Course outcomes: On the completion of this laboratory course, the students will be able to:

- Write test bench to simulate various digital circuits.
- Interpret concepts of DC Analysis, AC Analysis and Transient Analysis in analog circuits.
- Design and simulate basic CMOS circuits like inverter, common source amplifier and differential amplifiers.
- Use basic amplifiers and further design higher level circuits like operational amplifier and analog/digital converters to meet desired parameters.
- Use transistors to design gates and further using gates realize shift registers and adders to meet desired parameters.

Conduct of Practical Examination:

- All laboratory experiments are to be included for practical examination.
- For examination, one question from **PART-A** and one question from **PART-B** to be set.
- Students are allowed to pick one experiment from the lot.
- Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

B.E E&C EIGTH SEMESTER SYLLABUS

Wireless Cellular and LTE 4G Broadband

B.E., VIII Semester, Electronics &Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC81	IA Marks	20
Number of Lecture	04	Exam Marks	80
Total Number	50 (10 Hours / Module)	Exam Hours	03
CREDITS – 04			

Course Objectives: This course will enable students to:

- Understand the basics of LTE standardization phases and specifications.
- Explain the system architecture of LTE and E-UTRAN, the layer of LTE, based on the use of OFDMA and SC-FDMA principles.
- Analyze the role of LTE radio interface protocols to set up, reconfigure and release the Radio Bearer, for transferring the EPS bearer.
- Analyze the main factors affecting LTE performance including mobile speed and transmission bandwidth.

Module – 1	RBT Level
Key Enablers for LTE features: OFDM, Single carrier FDMA, Single carrier FDE, Channel Dependent Multiuser Resource Scheduling, Multi antenna Techniques, IP based Flat network Architecture, LTE Network Architecture. (Sec 1.4- 1.5 of Text). **Wireless Fundamentals:** Cellular concept, Broadband wireless channel (BWC), Fading in BWC, Modeling BWC – Empirical and Statistical models, Mitigation of Narrow band and Broadband Fading (Sec 2.2 – 2.7of Text).	L1, L2
Module – 2	
Multicarrier Modulation: OFDM basics, OFDM in LTE, Timing and Frequency Synchronization, PAR, SC-FDE (Sec 3.2 – 3.6 of Text). **OFDMA and SC-FDMA:** OFDM with FDMA,TDMA,CDMA, OFDMA, SC-FDMA, OFDMA and SC-FDMA in LTE (Sec 4.1 – 4.3, 4.5 of Text). **Multiple Antenna Transmission and Reception:** Spatial Diversity overview, Receive Diversity, Transmit Diversity, Interference cancellation and signal enhancement, Spatial Multiplexing, Choice between Diversity, Interference suppression and Spatial Multiplexing (Sec 5.1 – 5.6 of Text).	L1, L2
Module – 3	
Overview and Channel Structure of LTE: Introduction to LTE, Channel Structure of LTE, Downlink OFDMA Radio Resource, Uplink	L1, L2

SC-FDMA Radio Resource(Sec 6.1 – 6.4 of Text). . **Downlink Transport Channel Processing:** Overview, Downlink shared channels, Downlink Control Channels, Broadcast channels, Multicast channels, Downlink physical channels, H-ARQ on Downlink(Sec 7.1 – 7.7 of Text).	
Module – 4	
Uplink Channel Transport Processing: Overview, Uplink shared channels, Uplink Control Information, Uplink Reference signals, Random Access Channels, H-ARQ on uplink (Sec 8.1 – 8.6 of Text). **Physical Layer Procedures:** Hybrid – ARQ procedures, Channel Quality Indicator CQI feedback, Precoder for closed loop MIMO Operations, Uplink channel sounding, Buffer status Reporting in uplink, Scheduling and Resource Allocation, Cell Search, Random Access Procedures, Power Control in uplink(Sec 9.1- 9.6, 9.8, 9.9, 9.10 Text).	L1, L2
Module – 5	
Radio Resource Management and Mobility Management: PDCP overview, MAC/RLC overview, RRC overview, Mobility Management, Inter-cell Interference Coordination(Sec 10.1 – 10.5 of Text).	L1, L2

Course Outcomes: At the end of the course, students will be able to:

- Understand the system architecture and the functional standard specified in LTE 4G.
- Analyze the role of LTE radio interface protocols and EPS Data convergence protocols to set up, reconfigure and release data and voice from users.
- Demonstrate the UTRAN and EPS handling processes from set up to release including mobility management for a variety of data call scenarios.
- Test and Evaluate the Performance of resource management and packet data processing and transport algorithms.

Question Paper pattern:

- The Question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full Questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The Students will have to answer 5 full Questions, selecting one full Question from each module.

Text Book:

Arunabha Ghosh, Jan Zhang, Jefferey Andrews, Riaz Mohammed, 'Fundamentals of LTE', Prentice Hall, Communications Engg. and Emerging Technologies.

Reference Books:

1. LTE for UMTS Evolution to LTE-Advanced' Harri Holma and Antti Toskala, Second Edition - 2011, John Wiley & Sons, Ltd. Print ISBN: 9780470660003.
2. 'EVOLVED PACKET SYSTEM (EPS) ; THE LTE AND SAE EVOLUTION OF 3G UMTS' by Pierre Lescuyer and Thierry Lucidarme, 2008, John Wiley & Sons, Ltd. Print ISBN:978-0-470-05976-0.
3. 'LTE – The UMTS Long Term Evolution ; From Theory to Practice' by Stefania Sesia, Issam Toufik, and Matthew Baker, 2009 John Wiley & Sons Ltd, ISBN 978-0-470-69716-0.

FIBER OPTICS and NETWORKS

B.E., VIII Semester, Electronics &Communication Engineering

[As per Choice Based Credit System (CBCS)]

Subject Code	15EC82	IA Marks	20
Number of Lecture Hours/Week	4	Exam Marks	80
Total Number of Lecture Hours	50(10 Hours / Module)	Exam Hours	03
CREDITS – 04			

Course Objectives: This course will enable students to:

- Learn the basic principle of optical fiber communication with different modes of light propagation.
- Understand the transmission characteristics and losses in optical fiber.
- Study of optical components and its applications in optical communication networks.
- Learn the network standards in optical fiber and understand the network architectures along with its functionalities.

Module -1	RBT Level
Optical fiber Communications: Historical development, The general system, Advantages of optical fiber communication, Optical fiber waveguides: Ray theory transmission, Modes in planar guide, Phase and group velocity, Cylindrical fiber: Modes, Step index fibers, Graded index fibers, Single mode fibers, Cutoff wavelength, Mode field diameter, effective refractive index. Fiber Materials, Photonic crystal fibers. (Text 2)	L1, L2
Module -2	
Transmission characteristics of optical fiber: Attenuation, Material absorption losses, Linear scattering losses, Nonlinear scattering losses, Fiber bend loss, Dispersion, Chromatic dispersion, Intermodal dispersion: Multimode step index fiber. **Optical Fiber Connectors:** Fiber alignment and joint loss, Fiber splices, Fiber connectors, Fiber couplers. (Text 2)	L1, L2
Module -3	
Optical sources: Energy Bands, Direct and Indirect Bandgaps, Light Emitting diodes: LED Structures, Light Source Materials, Quantum Efficiency and LED Power, Modulation. Laser Diodes: Modes and Threshold conditions, Rate equation, External Quantum Efficiency, Resonant frequencies, Laser Diode structures and Radiation Patterns: Single mode lasers. **Photodetectors:** Physical principles of Photodiodes, Photodetector noise, Detector response time. **Optical Receiver:** Optical Receiver Operation: Error sources,	L1, L2

Front End Amplifiers, Receiver sensitivity, Quantum Limit. (Text 1)	
Module -4	
WDM Concepts and Components: Overview of WDM: Operational Principles of WDM, WDM standards, Mach-Zehnder Interferometer Multiplexers, Isolators and Circulators, Fiber grating filters, Dielectric Thin-Film Filters, Diffraction Gratings, Active Optical Components, Tunable light sources, **Optical amplifiers:** Basic application and Types, Semiconductor optical amplifiers, Erbium Doped Fiber Amplifiers, Raman Amplifiers, Wideband Optical Amplifiers. (Text 1)	L1, L2
Module -5	
Optical Networks: Optical network evolution and concepts: Optical networking terminology, Optical network node and switching elements, Wavelength division multiplexed networks, Public telecommunication network overview. Optical network transmission modes, layers and protocols: Synchronous networks, Asynchronous transfer mode, OSI reference model, Optical transport network, Internet protocol, Wavelength routing networks: Routing and wavelength assignment, Optical switching networks: Optical circuit switched networks, packet switched networks, Multiprotocol Label Switching, Optical burst switching networks, Optical network deployment: Long-haul networks, Metropoliton area networks, Access networks, Local area networks. (Text 2)	L1, L2

Course Outcomes: At the end of the course, students will be able to:

1. Classification and working of optical fiber with different modes of signal propagation.
2. Describe the transmission characteristics and losses in optical fiber communication.
3. Describe the construction and working principle of optical connectors, multiplexers and amplifiers.
4. Describe the constructional features and the characteristics of optical sources and detectors.
5. Illustrate the networking aspects of optical fiber and describe various standards associated with it.

Question Paper pattern:

- The Question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full Questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The Students will have to answer 5 full Questions, selecting one full Question from each module.

Text Books:

1. Gerd Keiser , Optical Fiber Communication, 5th Edition, McGraw Hill

Education(India) Private Limited, 2015. ISBN:1-25-900687-5. 2. John M Senior, Optical Fiber Communications, Principles and Practice, 3rd Edition, Pearson Education, 2010, ISBN:978-81-317-3266-3
Reference Book: Joseph C Palais, Fiber Optic Communication , Pearson Education, 2005, ISBN:0130085103

Micro Electro Mechanical Systems

B.E., VIII Semester, Electronics &Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC831	IA Marks	20
Number of Lecture Hours/Week	03	Exam marks	80
Total Number of Lecture Hours	40 (8 Hours per Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to:

- Understand overview of microsystems, their fabrication and application areas.
- Working principles of several MEMS devices.
- Develop mathematical and analytical models of MEMS devices.
- Know methods to fabricate MEMS devices.
- Various application areas where MEMS devices can be used.

Module 1	RBT Level
Overview of MEMS and Microsystems: MEMS and Microsystem, Typical MEMS and Microsystems Products, Evolution of Microfabrication, Microsystems and Microelectronics, Multidisciplinary Nature of Microsystems, Miniaturization. Applications and Markets.	L1, L2
Module 2	
Working Principles of Microsystems: Introduction, Microsensors, Microactuation, MEMS with Microactuators, Microaccelerometers, Microfluidics. **Engineering Science for Microsystems Design and Fabrication**: Introduction, Molecular Theory of Matter and Inter-molecular Forces, Plasma Physics, Electrochemistry.	L1, L2
Module 3	
Engineering Mechanics for Microsystems Design: Introduction, Static Bending of Thin Plates, Mechanical Vibration, Thermomechanics, Fracture Mechanics, Thin Film Mechanics, Overview on Finite Element Stress Analysis.	L1,L2,L3
Module 4	

Scaling Laws in Miniaturization: Introduction, Scaling in Geometry, Scaling in Rigid-Body Dynamics, Scaling in Electrostatic Forces, Scaling in Fluid Mechanics, Scaling in Heat Transfer.	L1,L2,L3
Module 5	
Overview of Micromanufacturing: Introduction, Bulk Micromanufacturing, Surface Micromachining, The LIGA Process, Summary on Micromanufacturing.	L1,L2

Course Outcomes: After studying this course, students will be able to:

- Appreciate the technologies related to Micro Electro Mechanical Systems.
- Understand design and fabrication processes involved with MEMS devices.
- Analyse the MEMS devices and develop suitable mathematical models
- Know various application areas for MEMS device

Question paper pattern:

- The question paper will have 10 full questions carrying equal marks.
- Each full question consists of 16 marks with a maximum of Three sub questions.
- There will be 2 full questions from each module covering all the topics of the module
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Tai-Ran Hsu, MEMS and Micro systems: Design, Manufacture and Nanoscale Engineering, 2nd Ed, Wiley.

Reference Books:

1. Hans H. Gatzen, Volker Saile, JurgLeuthold, Micro and Nano Fabrication: Tools and Processes, Springer, 2015.
2. Dilip Kumar Bhattacharya, Brajesh Kumar Kaushik, Microelectromechanical Systems (MEMS), Cenage Learning.

SPEECH PROCESSING

B.E., VIII Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC832	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course enables students to:

- Introduce the models for speech production
- Develop time and frequency domain techniques for estimating speech parameters
- Introduce a predictive technique for speech compression
- Provide fundamental knowledge required to understand and analyse speech recognition, synthesis and speaker identification systems.

Modules	
Module-1	**RBT Level**
Fundamentals of Human Speech Production: The Process of Speech Production, Short-Time Fourier Representation of Speech, The Acoustic Theory of Speech Production, Lossless Tube Models of the Vocal Tract, Digital Models for Sampled Speech Signals	L1, L2
Module-2	
Time-Domain Methods for Speech Processing: Introduction to Short-Time Analysis of Speech, Short-Time Energy and Short-Time Magnitude, Short-Time Zero-Crossing Rate, The Short-Time Autocorrelation Function, The Modified Short-Time Autocorrelation Function, The Short-Time Average Magnitude Difference Function.	L1, L2
Module-3	
Frequency Domain Representations: Discrete-Time Fourier Analysis, Short-Time Fourier Analysis, Spectrographic Displays, Overlap Addition(OLA),Method of Synthesis, Filter Bank Summation(FBS) Method of Synthesis, Time-Decimated Filter Banks, Two-Channel Filter Banks, Implementation of the FBS Method Using the FFT, OLA Revisited, Modifications of the STFT.	L1, L2
Module-4	
The Cepstrum and Homomorphic Speech Processing: Homomorphic Systems for Convolution, Homomorphic Analysis of the Speech Model, Computing the Short-Time Cepstrum and Complex Cepstrum of Speech, Homomorphic Filtering of Natural Speech, Cepstrum Analysis of All-Pole Models, Cepstrum Distance Measures.	L1, L2, L3
Module-5	
Linear Predictive Analysis of Speech Signals: Basic Principles of Linear	L1, L2,

Predictive Analysis, Computation of the Gain for the Model, Frequency Domain Interpretations of Linear Predictive Analysis, Solution of the LPC Equations, The Prediction Error Signal, Some Properties of the LPC Polynomial A(z), Relation of Linear Predictive Analysis to Lossless Tube Models, Alternative Representations of the LP Parameters.	L3

Course outcomes: Upon completion of the course, students will be able to:

- Model speech production system and describe the fundamentals of speech.
- Extract and compare different speech parameters.
- Choose an appropriate speech model for a given application.
- Analyse speech recognition, synthesis and speaker identification systems

Question paper pattern:

- The question paper will have ten questions.
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Theory and Applications of Digital Speech Processing-Rabiner and Schafer, Pearson Education 2011

Reference Books:

3. **Fundamentals of Speech Recognition-** Lawrence Rabiner and Biing-Hwang Juang, Pearson Education, 2003.
4. **Speech and Language Processing–An Introduction to Natural Language Processing, Computational Linguistics, and Speech Recognition-** Daniel Jurafsky and James H Martin, Pearson Prentice Hall 2009.

Radar Engineering

B.E., VIII Semester, Electronics & Communication Engineering/
Telecommunication Engineering
[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC833	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03

CREDITS – 03

Course objectives: This course will enable students to:

- Understand the Radar fundamentals and analyze the radar signals.
- Understand various technologies involved in the design of radar transmitters and receivers.
- Learn various radars like MTI, Doppler and tracking radars and their comparison

Modules	RBT Level
Module-1	
Basics of Radar: Introduction, Maximum Unambiguous Range, Radar Waveforms, Definitions with respect to pulse waveform - PRF, PRI, Duty Cycle, Peak Transmitter Power, Average transmitter Power. **Simple form of the Radar Equation**, Radar Block Diagram and Operation, Radar Frequencies, Applications of Radar, The Origins of Radar, Illustrative Problems. **(Chapter 1 of Text)**	L1, L2, L3
Module-2	
The Radar Equation: Prediction of Range Performance, Detection of signal in Noise, Minimum Detectable Signal, Receiver Noise, SNR, Modified Radar Range Equation, Envelope Detector — False Alarm Time and Probability, Probability of Detection, **Radar Cross Section of Targets:** simple targets – sphere, cone-sphere, Transmitter Power, PRF and Range Ambiguities, System Losses (qualitative treatment), Illustrative Problems. **(Chapter 2 of Text, Except 2.4, 2.6, 2.8 & 2.11)**	L1, L2, L3
Module-3	
MTI and Pulse Doppler Radar: Introduction, Principle, Doppler Frequency Shift, Simple CW Radar, Sweep to Sweep subtraction and Delay Line Canceler, MTI Radar with – Power Amplifier Transmitter, Delay Line Cancelers — Frequency Response of Single Delay- Line Canceler, Blind Speeds, Clutter Attenuation, MTI Improvement Factor, N- Pulse Delay-Line Canceler, **Digital MTI Processing** – Blind phases, I and Q Channels, Digital MTI Doppler signal processor, Moving Target Detector- Original MTD. **(Chapter 3: 3.1, 3.2, 3.5, 3.6 of Text)**	L1, L2, L3
Module-4	
Tracking Radar: Tracking with Radar- Types of Tracking Radar Systems, Monopulse Tracking- Amplitude Comparison Monopulse (one-and two-coordinates), Phase Comparison Monopulse. **Sequential Lobing**, Conical Scan Tracking, Block Diagram of Conical Scan	L1, L2, L3

Tracking Radar, Tracking in Range, Comparison of Trackers. **(Chapter 4: 4.1, 4.2, 4.3 of Text)**	
Module-5	
The Radar Antenna: Functions of The Radar Antenna, Antenna Parameters, Reflector Antennas and Electronically Steered Phased array Antennas. **(Chapter 9: 9.1, 9.2 9.4, 9.5 of Text)** **Radar Receiver:** The Radar Receiver, Receiver Noise Figure, Super Heterodyne Receiver, Duplexers and Receivers Protectors, Radar Displays. **(Chapter 11 of Text)**	L1, L2, L3

Course outcomes: At the end of the course, students will be able to:

- Understand the radar fundamentals and radar signals.
- Explain the working principle of pulse Doppler radars, their applications and limitations
- Describe the working of various radar transmitters and receivers.
- Analyze the range parameters of pulse radar system which affect the system performance

Question paper pattern:

- The question paper will have ten questions.
- Each full Question consisting of 16 marks
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module.
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Introduction to Radar Systems- Merrill I Skolink, 3e, TMH, 2001.

Reference Books:

1. Radar Principles, Technology, Applications — Byron Edde, Pearson Education, 2004.
2. Radar Principles – Peebles. Jr, P.Z. Wiley. New York, 1998.
3. Principles of Modem Radar: Basic Principles – Mark A. Rkhards, James A. Scheer, William A. Holm. Yesdee, 2013

MACHINE LEARNING

B.E., VIII Semester, Electronics & Communication Engineering/ Telecommunication Engineering

[As per Choice Based Credit System (CBCS) scheme]

Subject Code	15EC834	IA Marks	20
Number of Lecture Hours/Week	03	Exam Marks	80
Total Number of Lecture Hours	40 (8 Hours / Module)	Exam Hours	03
CREDITS – 03			

Course Objectives: This course will enable students to:

- Introduce some concepts and techniques that are core to Machine Learning.
- Understand learning and decision trees.
- Acquire knowledge of neural networks, Bayesian techniques and instant based learning.
- Understand analytical learning and reinforced learning.

Modules	
Module-1	**RBT Level**
Learning: Designing Learning systems, Perspectives and Issues, Concept Learning, Version Spaces and Candidate Elimination Algorithm, Inductive bias.	L1, L2
Module-2	
Decision Tree and ANN: Decision Tree Representation, Hypothesis Space Search, Inductive bias in decision tree, issues in Decision tree. Neural Network Representation, Perceptrons, Multilayer Networks and Back Propagation Algorithms.	L1, L2
Module-3	
Bayesian and Computational Learning: Bayes Theorem, Bayes Theorem Concept Learning, Maximum Likelihood, Minimum Description Length Principle, Bayes Optimal Classifier, Gibbs Algorithm, Naïve Bayes Classifier.	L1, L2
Module-4	
Instant Based Learning and Learning set of rules: K- Nearest Neighbour Learning, Locally Weighted Regression, Radial Basis Functions, Case-Based Reasoning. Sequential Covering Algorithms, Learning Rule Sets, Learning First Order Rules, Learning Sets of First Order Rules.	L1, L2
Module-5	
Analytical Learning and Reinforced Learning: Perfect Domain Theories, Explanation Based Learning, Inductive-Analytical Approaches, FOCL Algorithm, Reinforcement Learning.	L1, L2

Course outcomes: At the end of the course, students should be able to:

- Understand the core concepts of Machine learning.
- Appreciate the underlying mathematical relationships within and across Machine Learning algorithms.
- Explain paradigms of supervised and un-supervised learning.
- Recognize a real world problem and apply the learned techniques of Machine Learning to solve the problem.

Question paper pattern:

- The question paper will have ten questions.
- Each full question consists of 16 marks.
- There will be 2 full questions (with a maximum of Three sub questions) from each module.
- Each full question will have sub questions covering all the topics under a module. The students will have to answer 5 full questions, selecting one full question from each module.

Text Book:

Machine Learning-Tom M. Mitchell, McGraw-Hill Education, (INDIAN EDITION), 2013.

Reference Books:

1. **Introduction to Machine Learning-** Ethem Alpaydin, 2nd Ed., PHI Learning Pvt. Ltd., 2013.
2. **The Elements of Statistical Learning-**T. Hastie, R. Tibshirani, J. H. Friedman, Springer; 1st edition, 2001.

NETWORK AND CYBER SECURITY

B.E., VIII Semester, Electronics & Communication Engineering

[As per Choice Based credit System (CBCS) Scheme

Subject Code	15EC835	IA Marks	20
Number of Lecture Hours/Week	03	Exam marks	80
Total Number of Lecture Hours	40 (8 Hours per Module)	Exam Hours	03

CREDITS – 03

Course Objectives: This course will enable students to:

- Know about security concerns in Email and Internet Protocol.
- Understand cyber security concepts.
- List the problems that can arise in cyber security.
- Discuss the various cyber security frame work.

Module-1	RBT Level
Transport Level Security: Web Security Considerations, Secure Sockets Layer, Transport Layer Security, HTTPS, Secure Shell (SSH) (Text 1: Chapter 15)	L1, L2
Module-2	
E-mail Security: Pretty Good Privacy, S/MIME, Domain keys identified mail (Text 1: Chapter 17)	L1, L2
Module-3	
IP Security: IP Security Overview, IP Security Policy, Encapsulation Security Payload (ESP), Combining security Associations Internet Key Exchange. Cryptographic Suites(Text 1: Chapter 18)	L1, L2
Module-4	
Cyber network security concepts: Security Architecture, antipattern: signature based malware detection versus polymorphic threads, document driven certification and accreditation, policy driven security certifications. Refactored solution: reputational, behavioural and entropy based malware detection. **The problems:** cyber antipatterns concept, forces in cyber antipatterns, cyber anti pattern templates, cyber security antipattern catalog (Text-2: Chapter1 & 2)	L1, L2, L3
Module-5	
Cyber network security concepts contd. : **Enterprise security using Zachman framework** Zachman framework for enterprise architecture, primitive models versus composite models, architectural problem solving patterns, enterprise workshop, matrix mining, mini patterns for problem solving meetings. **Case study:** cyber security hands on – managing administrations	L1, L2, L3

and root accounts, installing hardware, reimaging OS, installing system protection/ antimalware, configuring firewalls (Text-2: Chapter 3 & 4).	

Course Outcomes: After studying this course, students will be able to:

- Explain network security protocols
- Understand the basic concepts of cyber security
- Discuss the cyber security problems
- Explain Enterprise Security Framework
- Apply concept of cyber security framework in computer system administration

Question paper pattern:

- The question paper will have 10 full questions carrying equal marks.
- Each full question consists of 16 marks with a maximum of Three sub questions.
- There will be 2 full questions from each module covering all the topics of the module
- The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

1. William Stallings, "Cryptography and Network Security Principles and Practice", Pearson Education Inc., 6th Edition, 2014, ISBN: 978-93-325-1877-3.

2. Thomas J. Mowbray, "Cyber Security – Managing Systems, Conducting Testing, and Investigating Intrusions", Wiley.

Reference Books:

1. Cryptography and Network Security, Behrouz A. Forouzan, TMH, 2007.
2. Cryptography and Network Security, Atul Kahate, TMH, 2003.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY
BELAGAVI

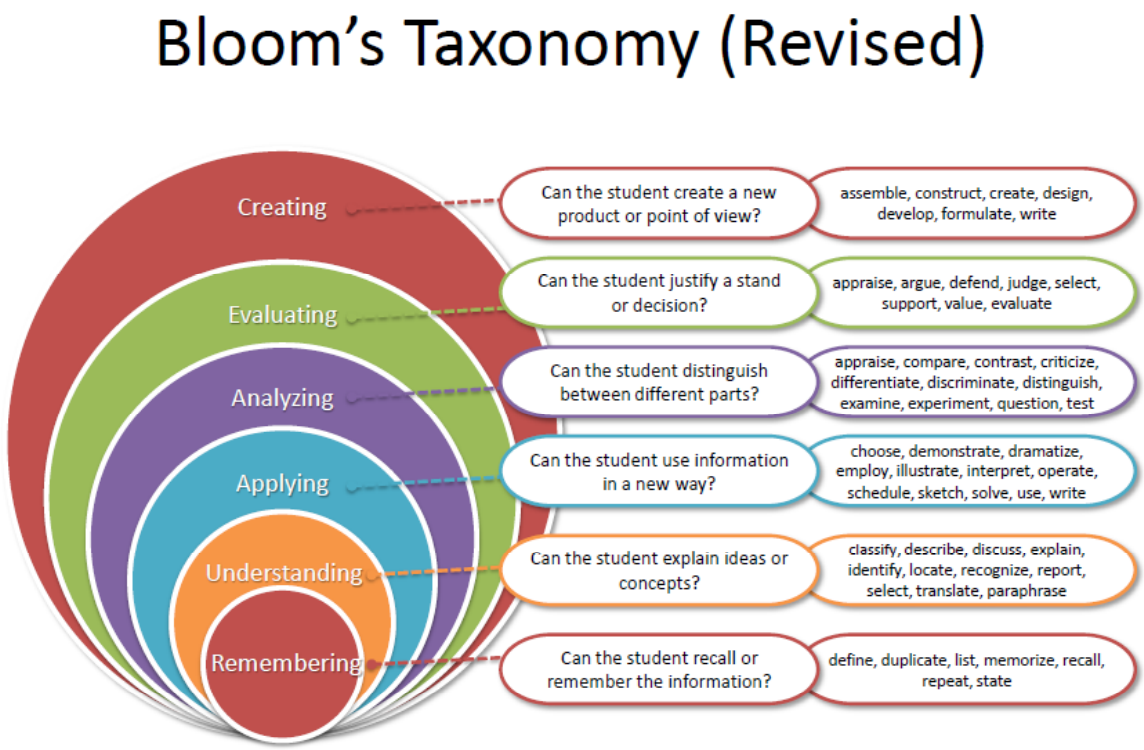
Scheme of Teaching and Examination and Syllabus

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING

III TO VIII SEMESER

(Effective from Academic year 2015-16)

CATEGORIZATION FOR THE THINKING PROCESS



Bloom's Revised Taxonomy
Levels, Level Definitions and attributes levels
along with action verbs that can be used when developing learning outcomes.

	Level	Level Definitions and attributes	Verbs(not comprehensive)
Lower order thinking skills (LOTS)	Remembering (Knowledge) L_1	Students exhibit memory/rote memorization of previously learnt materials by recognition,recalling facts, terms, basic concepts, and simple answers. Able to remember, but not necessarily fully understanding the material.	Copy, Choose, Define, Discover, Describe, Duplicate, Enumerate, Find, How, Identify, Label, List, Locate, Listen, Memorize, Match, Name, Omit, Quote, Recall, Relate, Reproduce, Recognize, Select, Show, Spell, Tell, Tabulate, Who, When, Where etc.
	Understanding (Comprehension) L_2	Students demonstrate understanding of facts and ideas by interpreting, exemplifying, classifying, inferring, summarizing, comparing and explaining main ideas with own words.	Ask, Classify, Compare, Contrast, Demonstrate, Describe, Extend, Differentiate, Distinguish, Discuss, Express, Explain, Group, Illustrate, Infer, Interpret, Outline, Paraphrase, Rephrase, Relate, Show, Summarize, Select, Translate, Restate etc.
	Applying (Application) L_3	Students solve problems in new situations by applying acquired knowledge, facts, techniques and rules in a different way.	Calculate, Predict, Apply, Solve, Illustrate, Use, Demonstrate, Determine, Model, Build, Construct, Develop, Experiment With, Identify, Make Use Of, Organize, Plan, Select etc.
Higher order thinking skills (HOTS)	Analysing (Analysis) L_4	Students are able to examine and break information into component parts by identifying motives, causes arrangement, logic and semantics. They can make inferences and find evidence to support generalization.	Analyse, Assume, Break Down, Classify, Categorize, Conclusion, Compare, Contrast, Diagram, Discover, Dissect, Distinguish, Divide, Examine, Function, Illustrate, Inference, Inspect, List,Motive, Outline,Relationships, Simplify, Survey, Take Part In, Test For etc.
	Evaluating (Evaluation) L_5	Students are able to present and defend opinions by making judgments about information, validity of ideas, or quality of work based on a set of criteria. They can justify a decision or course of action.	Agree, Appraise, Assess, Award, Build, Create, Compose, Choose, Compare, Conclude, Criteria, Criticize, Design, Derive, Develop, Decide, Deduct, Determine, Disprove, Defend, Estimate, Formulate, Generate, Invent, Modify, Evaluate, Explain, Influence, Judge, Interpret, Justify, Mark, Measure, Perceive, Rate, Prioritize, Recommend, Rule On, Select, Support, Value etc.
	Creating (Synthesis) L_6	Students are able to compile, generate or view information,ideas or products together in a different way by combining elements in a new pattern or by proposing alternative solutions. Also, use information to form a unique product. This requires creativity and originality.	Assemble, Adapt, Anticipate, Build, Change, Choose, Combine, Collaborate, Collect, Create, Compile, Compose, Construct, Delete, Design, Develop, Discuss, Develop, Devise, Elaborate, Estimate, Formulate, Happen, Hypothesize, Imagine, Improve, Invent, Imagine, Intervene, Make Up, Maximize, Modify, Originate, Plan, Predict, Propose, Rearrange, Solve, Suppose, Substitute, Test etc.

Graduate attributes: Graduate attributes are the qualities, skills and understandings a university community agrees its students should develop during their time with the institution. These attributes include but go beyond the disciplinary expertise or technical knowledge that has traditionally formed the core of most university courses. They are qualities that also prepare graduates as agents of social good in an unknown future.

Bowden, Hart, King, Trigwell& Watts (2000)

Scheme of Teaching and Examination

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

SCHEME OF TEACHING AND EXAMINATION - 2015-16

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING

CHOICE BASED CREDIT SYSTEM (CBCS)

III SEMESTER

Sl. No	Subject Code	Subject (Course)	Title	Teaching Dept.	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15MAT31	Core Subject	Engineering Mathematics-III	Mathematics	04	--	03	20	80	100	4
2	15EE32	Core Subject	Electric Circuit Analysis	EEE	04	--	03	20	80	100	4
3	15EE33	Core Subject	Transformers and Generators	EEE	04	--	03	20	80	100	4
4	15EE34	Core Subject	Analog Electronic Circuits	EEE	04	--	03	20	80	100	4
5	15EE35	Core Subject	Digital System Design	EEE	04	--	03	20	80	100	4
6	15EE36	Foundation Course	Electrical and Electronic Measurements	EEE	04	--	03	20	80	100	4
7	15EEL37	Laboratory	Electrical Machines Laboratory -1	EEE	01-Hour Instruction 02-Hour Practical		03	20	80	100	2
8	15EEL38	Laboratory	Electronics Laboratory	EEE	01-Hour Instruction 02-Hour Practical		03	20	80	100	2
				TOTAL	**Theory:24 hours Practical: 06 hours**		**24**	**160**	**640**	**800**	**28**

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.

2. FoundationCourse: The courses based upon the content that leads to Knowledge enhancement.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

SCHEME OF TEACHING AND EXAMINATION - 2015-16

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING

CHOICE BASED CREDIT SYSTEM (CBCS)

IV SEMESTER

Sl. No	Subject Code	Subject (Course)	Title	Teaching Dept.	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15MAT41	Core Subject	Engineering Mathematics-IV	Maths	04	--	03	20	80	100	4
2	15EE42	Core Subject	Power Generation and Economics	EEE	04	--	03	20	80	100	4
3	15EE43	Core Subject	Transmission and Distribution	EEE	04	--	03	20	80	100	4
4	15EE44	Core Subject	Electric Motors	EEE	04	--	03	20	80	100	4
5	15EE45	Core Subject	Electromagnetic Field Theory	EEE	04	--	03	20	80	100	4
6	15EE46	Foundation Course	Operational Amplifiers and Linear ICs	EEE	04	--	03	20	80	100	4
7	15EEL47	Laboratory	Electrical Machines Laboratory -2	EEE	01-Hour Instruction 02-Hour Practical		03	20	80	100	2
8	15EEL48	Laboratory	Op- amp and Linear ICs Laboratory	EEE	01-Hour Instruction 02-Hour Practical		03	20	80	100	2
				TOTAL	**Theory:24 hours Practical: 06 hours**		**24**	**160**	**640**	**800**	**28**

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.

2. **Foundation Course:**The courses based upon the content that leads to Knowledge enhancement.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

SCHEME OF TEACHING AND EXAMINATION - 2015-16

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING

CHOICE BASED CREDIT SYSTEM (CBCS)

V SEMESTER

Sl. No	Subject Code	Subject (Course)	Title	Teaching Department	Teaching Hours /Week: Theory	Teaching Hours /Week: Practical/ Drawing	Examination: Duration in hours	Examination: Theory/ Practical Marks	Examination: I.A. Marks	Examination: Total Marks	Credits
1	15EE51	Core Subject	Management and Entrepreneurship	EEE	04	--	03	80	20	100	4
2	15EE52	Core Subject	Microcontroller	EEE	04	--	03	80	20	100	4
3	15EE53	Core Subject	Power Electronics	EEE	04	--	03	80	20	100	4
4	15EE54	Core Subject	Signals and Systems	EEE	04	--	03	80	20	100	4
5	15EE55X	Professional Elective	Professional Elective – I	EEE	03	--	03	80	20	100	3
6	15EE56Y	Open Elective	Open Elective - I	EEE	03	--	03	80	20	100	3
7	15EEL57	Laboratory	Microcontroller Laboratory	EEE	01-Hour Instruction 02-Hour Practical		03	80	20	100	2
8	15EEL58	Laboratory	Power Electronics Laboratory	EEE	01-Hour Instruction 02-Hour Practical		03	80	20	100	2
				TOTAL	**Theory:22hours Practical: 06 hours**		**24**	**160**	**640**	**800**	**26**

Elective

Professional Elective		**Open Elective*** Offered by the Department of Electrical and Electronics Engineering**	
Courses under Code 15EE55X	**Title**	**Courses under Code 15EE55X**	**Title**
15EE551	Introduction to Nuclear Power	15EE561	Electronic Communication systems
15EE552	Electrical Engineering Materials	15EE562	Programmable Logic controllers
15EE553	Estimating and Costing	15EE563	Renewable Energy Systems
15EE554	Special Electrical Machines	15EE564	Business Communication

***Students can select any one of the open electives offered by any Department (Please refer to consolidated list of VTU for open electives). Selection of an open elective is not allowed provided;

- The candidate has pre – requisite knowledge.
- The candidate has not studied during I and II year of the programme.
- The syllabus content of open elective is similar to that of Departmental core courses or professional electives.
- A similar course, under any category, is prescribed in the higher semesters.

Registration to electives shall be documented under the guidance of Programme Coordinator and Adviser.

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.

2. Professional Elective: Electives relevant to chosen specialization/ branch.

3. Open Elective: Electives from other technical and/ or emerging subject areas.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

SCHEME OF TEACHING AND EXAMINATION - 2015-16

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING

CHOICE BASED CREDIT SYSTEM (CBCS)

VI SEMESTER

Sl. No	Subject Code	Subject (Course)	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15EE61	Core Subject	Control Systems	EEE	04	--	03	80	20	100	4
2	15EE62	Core Subject	Power System Analysis – 1	EEE	04	--	03	80	20	100	4
3	15EE63	Core Subject	Digital Signal Processing	EEE	04	--	03	80	20	100	4
4	15EE64	Core Subject	Electrical Machine Design	EEE	04	--	03	80	20	100	4
5	15EE65X	Professional Elective	Professional Elective – II	EEE	03	--	03	80	20	100	3
6	15EE66Y	Open Elective	Open Elective - II	EEE	03	--	03	80	20	100	3
7	15EEL67	Laboratory	Control System Laboratory	EEE	01-Hour Instruction 02-Hour Practical		03	80	20	100	2
8	15EEL68	Laboratory	Digital Signal Processing Laboratory	EEE	01-Hour Instruction 02-Hour Practical		03	80	20	100	2
				TOTAL	**Theory:22 hours** **Practical: 06 hours**		**24**	**160**	**640**	**800**	**26**

Elective

Professional Elective		**Open Elective***** **Offered by the Department of Electrical and Electronics Engineering**	
Courses under Code 15EE65X	**Title**	**Courses under Code 15EE66Y**	**Title**
15EE651	Computer Aided Electrical Drawing	15EE661	Artificial Neural Networks and Fuzzy logic
15EE652	Advanced Power Electronics	15EE662	Sensors and Transducers
15EE653	Energy Audit and Demand side Management	15EE663	Batteries and Fuel Cells for Commercial, Military and Space Applications
15EE654	Solar and Wind Energy	15EE664	Industrial Servo Control Systems

***Students can select any one of the open electives offered by any Department (Please refer to consolidated list of VTU for open electives). Selection of an open elective is not allowed provided;

- The candidate has pre – requisite knowledge.
- The candidate has not studied during I and II year of the programme.
- The syllabus content of open elective is similar to that of Departmental core courses or professional electives.
- A similar course, under any category, is prescribed in the higher semesters.

Registration to electives shall be documented under the guidance of Programme Coordinator and Adviser.

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.

2. Professional Elective: Electives relevant to chosen specialization/ branch.

3. Open Elective: Electives from other technical and/ or emerging subject areas.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

SCHEME OF TEACHING AND EXAMINATION - 2015-16

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING

CHOICE BASED CREDIT SYSTEM (CBCS)

VII SEMESTER

Sl. No	Course Code	Subject (Course)	Title	Teaching Department	Teaching Hours/Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15EE71	Core Subject	Power System Analysis - 2	EEE	04	--	03	20	80	100	4
2	15EE72	Core Subject	Power System Protection	EEE	04	--	03	20	80	100	4
3	15EE73	Core Subject	High Voltage Engineering	EEE	04	--	03	20	80	100	4
4	15EE74X	Professional Elective	Professional Elective – III	EEE	04	--	03	20	80	100	3
5	15EE75Y	Professional Elective	Professional Elective – IV	EEE	04	--	03	20	80	100	3
6	15EEL76	Laboratory	Power system Simulation Laboratory	EEE	01-Hour Instruction 02-Hour Practical		03	20	80	100	2
7	15EEL77	Laboratory	Rely and High Voltage Laboratory	EEE	01-Hour Instruction 02-Hour Practical		03	20	80	100	2
8	15EEP78	Project Phase – I + Seminar		EEE	--		--	100	--	100	2
				TOTAL	**Theory:24 hours Practical: 06 hours**		**21**	**240**	**560**	**800**	**24**

Elective

Professional Elective – III		**Professional Elective – IV**	
Courses under Code 15EE74X	**Title**	**Courses under Code 15EE75Y**	**Title**
15EE741	Advanced Control Systems	15EE751	FACTs and HVDC Transmission
15EE742	Utilization of Electrical Power	15EE752	Testing and Commissioning of Power System Apparatus
15EE743	Carbon Capture and Storage	15EE753	Spacecraft Power Technologies
15EE744	Power System Planning	15EE754	Industrial Heating

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.

2. Professional Elective: Elective relevant to chosen specialization/ branch.

3. Project Phase –I + Seminar: Literature Survey, Problem Identification, objectives and Methodology. Submission of synopsis and seminar.

4. Internship / Professional Practice: To be carried between the VI and VIIsemester vacation or VII and VIII semester vacation period.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

SCHEME OF TEACHING AND EXAMINATION - 2015-16

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING

CHOICE BASED CREDIT SYSTEM (CBCS)

VIII SEMESTER

Sl. No	Course Code	Subject (Course)	Title	Teaching Department	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15EE81	Core Subject	Power System Operation and Control	EEE	04	--	03	20	80	100	4
2	15EE82	Core Subject	Industrial Drives and Applications	EEE	04	--	03	20	80	100	4
3	15EE83X	Professional Elective	Professional Elective – V	EEE	03	--	03	20	80	100	3
4	15EE84	Core Subject	Internship / Professional Practice	EEE	Industry Oriented		03	50	50	100	2
5	15EEP85	Core Subject	Project Work Phase -II	EEE	--	06	03	100	100	200	6
6	15EES86	Core Subject	Seminar	EEE	--	04	--	100	--	100	1
				TOTAL	**Theory:11 hours Practical: 10 hours**		**15**	**310**	**390**	**700**	**20**

Professional Elective – V

Courses under Code 15EE83X	Title
15EE831	Smart Grid
15EE832	Operation and Maintenance of Solar Electric Systems
15EE833	Integration of Distributed Generation
15EE834	Power System in Emergencies

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.

2. Professional Elective: Elective relevant to chosen specialization/ branch.

3. Internship / Professional Practice: To be carried between the VI and VIIsemester vacation or VII and VIII semester vacation period.

III SEMESTER DETAILED SYLLABUS

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - III			
ENGINEERING MATHEMATICS –III (Core Course)			
Subject Code	15MAT31	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80
Credits - 04			

Course objectives:

- The objectives of this course is to introduce students to the mostly used analytical and numerical methods in the different engineering fields by making them to learn Fourier series, Fourier transforms and Z-transforms, statistical methods , numerical methods to solve algebraic and transcendental equations, vector integration and calculus of variations. ■

Module		Teaching Hours
Module-1		
Fourier Series: Periodic functions, Dirichlet's condition, Fourier Series of periodic functions with period 2π and with arbitrary period 2c. Fourier series of even and odd functions. Half range Fourier Series, practical harmonic analysis-Illustrative examples from engineering field. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_4 – Analysing.	
Module-2		
Fourier Transforms: Infinite Fourier transforms, Fourier sine and cosine transforms. Inverse Fourier transform. **Z-transform:** Difference equations, basic definition, z-transform-definition, Standard z-transforms, Damping rule, Shifting rule, Initial value and final value theorems (without proof) and problems, Inverse z-transform. Applications of z-transforms to solve difference equations. ■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Statistical Methods: Review of measures of central tendency and dispersion. Correlation-Karl Pearson's coefficient of correlation-problems. Regression analysis- lines of regression (without proof) –problems Curve Fitting: Curve fitting by the method of least squares- fitting of the curves of the form,$y = ax + b, y = ax^2 + bx + c \text{ and } y = ae^{bx}$. Numerical Methods: Numerical solution of algebraic and transcendental equations by Regula-Falsi Method and Newton-Raphson method. ■		**10**
Revised Bloom's Taxonomy Level	L_3 – Applying.	
Module-4		
Finite differences: Forward and backward differences, Newton's forward and backward interpolation formulae. Divided differences- Newton's divided difference formula. Lagrange's interpolation formula and inverse interpolation formula (all formulae without proof)-Problems. **Numerical integration:** Simpson's $(1/3)^{th}$and $(3/8)^{th}$ rules, Weddle's rule (without proof) – Problems. ■		**10**
Revised Bloom's Taxonomy Level	L_3 – Applying.	
Module-5		
Vector integration: Line integrals-definition and problems, surface and volume integrals-definition, Green's theorem in a plane, Stokes and Gauss-divergence theorem(without proof) and problems. **Calculus of Variations:** Variation of function and Functional, variational problems. Euler's equation, Geodesics, hanging chain, problems. ■		**10**
Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing. L_2 – Understanding, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - III

15MAT31 ENGINEERING MATHEMATICS –III (Core Subject) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Know the use of periodic signals and Fourier series to analyze circuits and system communications.
- Explain the general linear system theory for continuous-time signals and digital signal processing using the Fourier Transform and z-transform.
- Employ appropriate numerical methods to solve algebraic and transcendental equations.
- Apply Green's Theorem, Divergence Theorem and Stokes' theorem in various applications in the field of electro-magnetic and gravitational fields and fluid flow problems.
- Determine the extremals of functional and solve the simple problems of the calculus of variations. ■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Life-Long Learning, Accomplishment of Complex Problems.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks. Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module.

Text Books				
1	Higher Engineering Mathematics	B.S. Grewal	Khanna Publishers	43rd Edition, 2015
2	Advanced Engineering Mathematics	E. Kreyszig	John Wiley & Sons	10thEdition, 2015
Reference books				
3	A Text Book of Engineering Mathematics	N.P.Bali and Manish Goyal	Laxmi Publishers	7th Edition, 2010
4	Higher Engineering Mathematics	B.V.Ramana	Tata McGraw-Hill	2006
5	Higher Engineering Mathematics	H. K.DassEr. RajnishVerma	S.Chand	First Edition,2011

Web links and Video Lectures:

1. http://nptel.ac.in/courses.php?disciplineID=111
2. http://wwww.khanacademy.org/
3. http://www.class-central.com/subject/math

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - III

ELECTRIC CIRCUIT ANALYSIS (Core Subject)

Subject Code	15EE32	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To familiarize the basic laws, source transformations, theorems and the methods of analysing electrical circuits.
- To explain the concept of coupling in electric circuits and resonance.
- To familiarize the analysis of three-phase circuits, two port networks and networks with non-sinusoidal inputs.
- To analyze the transient response of circuits with dc and sinusoidal ac input.
- To impart basic knowledge on network analysis using Laplace transforms. ■

Module	Teaching Hours
Module-1	
Basic Concepts: Active and passive elements, Concept of ideal and practical sources. Source transformation and Source shifting, Concept of Super Mesh and Super node analysis. Analysis of networks by (i) Network reduction method including star – delta transformation, (ii) Mesh and Node voltage methods for ac and dc circuits with independent and dependent sources. Equilibrium equations using KCL and KVL, Duality. **Resonant Circuits:** Analysis of simple series RLC and parallel RLC circuits under resonances. Resonant frequency, Bandwidth and Quality factor at resonance. Practical RL-RC circuits.■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-2	
Network Theorems: Analysis of networks, with and without dependent ac and dc sources by Thevenin's and Norton's theorems. Analysis of ac and dc circuits for maximum power transfer to resistive and complex loads. Application of Millman's theorem and Super Position theorem to multisource networks. Reciprocity theorem and its application. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3	
Transient Analysis: Review of ordinary linear non homogeneous first and second order differential equations with constant coefficients. Transient analysis of ac and dc circuits by classical method. Transient analysis of dc and ac circuits. Behaviour of circuit elements under switching action ($t = 0$ and $t = \infty$**)**. Evaluation of initial conditions. ■	**10**
Revised Bloom's Taxonomy Level: L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	
Module-4	
Laplace Transformation: Laplace transformation (LT), LT of Impulse, Step, Ramp, Sinusoidal signals and shifted functions. Waveform synthesis. Initial and Final value theorems. Laplace Transform of network and time domain solution for RL, RC and RLC networks for ac and dc excitations. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5	
Unbalanced Three phase systems: Analysis of three phase systems, calculation of real and reactive powers. **Two Port networks:** Definition, Open circuit impedance, Short circuit admittance and Transmission parameters and their evaluation for simple circuits. Network functions of one port and two port	**10**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - III		
15EE32 ELECTRIC CIRCUIT ANALYSIS (Core Course) (continued)		
Module-5(continued)		**Teaching Hours**
Two Port networks (continued): networks, properties of poles and zeros of network functions. **Complex Wave analysis:** Analysis of simple circuits with non-sinusoidal excitation. ■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Apply knowledge of mathematics, science, and engineering to the analysis and design of electrical circuits.
- Identify, formulate, and solve engineering problems in the area circuits and systems.
- Analyze the solution and infer the authenticity of it.

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem analysis.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module. ■

Text/Reference Books

1	Engineering Circuit Analysis	William H Hayt et al	McGraw Hill	8th Edition,2014
2	Engineering Circuit Analysis	J David Irwin et al	Wiley India	10th Edition,2014
3	Fundamentals of Electric Circuits	Charles K Alexander Matthew N O Sadiku	McGraw Hill	5th Edition,2013
4	Network Analysis	M.E. Vanvalkenburg	Pearson	3rd Edition,2014
5	Electric Circuits	MahmoodNahvi	McGraw Hill	5th Edition,2009
6	Introduction to Electric Circuits	Richard C Dorf and James A Svoboda	Wiley	9th Edition,2015
7	Circuit Analysis; Theory and Practice	Allan H Robbins Wilhelm C Miller	Cengage	5th Edition,2013

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - III			
TRANSFORMERS AND GENERATORS (Core Course)			
Subject Code	15EE33	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80
Credits - 04			

Course objectives:

- To understand the concepts of transformers and their analysis.
- To suggest a suitable three phase transformer connection for a particular operation.
- To understand the concepts of generator and to evaluate their performance.
- To explain the requirement for the parallel operation of transformers and synchronous generators.■

Module-1		Teaching Hours
Single phase Transformers: Review of Principle of operation, constructional details of shell type and core type single-phase transformers, EMF equation, losses and commercial efficiency, conditions for maximum efficiency (No question shall be set from the review portion). Salient features of ideal transformer, operation of practical transformer under no - load and on - load with phasor diagrams. Equivalent circuit, Open circuit and Short circuit tests, calculation of equivalent circuit parameters and predetermination of efficiency- commercial and all-day. Voltage regulation and its significance. **Three-phase Transformers:** Introduction, Constructional features of three-phase transformers. Choice between single unit three-phase transformer and a bank of three single-phase transformers. Transformer connection for three phase operation – star/star, delta/delta, star/delta, zigzag/star and V/V, choice of connection. Phase conversion - Scott connection for three-phase to two-phase conversion. Labelling of three-phase transformer terminals, vector groups. Equivalent circuit of three phase transformers. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-2		
Parallel Operation of Transformers: Necessity of Parallel operation, conditions for parallel operation – Single phase and three phase. Load sharing in case of similar and dissimilar transformers. **Autotransformers and Tap changing transformers:** Introduction to auto transformer - copper economy, equivalent circuit, three phase auto connection and voltage regulation. Voltage regulation by tap changing – off circuit and on load. **Tertiary winding Transformers:** Necessity of tertiary winding, equivalent circuit and voltage regulation, tertiary winding in star/star transformers, rating of tertiary winding.■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Transformers (continuation): Cause and effects of harmonics, Current inrush in transformers, noise in transformers. Objects of testing transformers, polarity test, Sumpner's test. **Direct current Generator –** Review of construction, types, armature windings, relation between no load and terminal voltage (No question shall be set from the review portion). Armature reaction, Commutation and associated problems, no load and full load characteristics. Reasons for reduced dependency on dc generators. **Synchronous generators-** Review of construction and operation of salient & non-salient pole synchronous generators (No question shall be set from the review portion). Armature windings, winding factors, emf equation. Harmonics – causes, reduction and elimination. Armature reaction, Synchronous reactance, Equivalent circuit.■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	
Module-4		
Synchronous generators (continuation): Generator load characteristic. Voltage regulation, excitation control for constant terminal voltage. Generator input and output. Parallel operation of		**10**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - III

15EE33 TRANSFORMERS AND GENERATORS (Core Course) (continued)

Module-4(continued)		**Teaching Hours**
Synchronous generators(continuation): generators and load sharing. Synchronous generator on infinite bus-bars – General load diagram, Electrical load diagram, mechanical load diagram, O – curves and V – curves. Power angle characteristic and synchronizing power. **Synchronous generators(continuation):** Effects of saliency, two-reaction theory, Direct and Quadrature reactance, power angle diagram, reluctance power, slip test. ■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
Synchronous generators(continuation): Open circuit and short circuit characteristics, Assessment of reactance- short circuit ratio, synchronous reactance, adjusted synchronous reactance and Potier reactance. Voltage regulation by EMF, MMF, ZPF and ASA methods. **Performance of synchronous generators:** Capability curve for large turbo generators and salient pole generators. Starting, synchronizing and control. Hunting and dampers. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Explain the construction and operation and performance of transformers.
- Explain different connections for the three phase operations, their advantages and applications.
- Explain the construction and operation of Synchronous machines and evaluate the regulation of synchronous machines by different methods.
- Analyze the operation of the synchronous machine connected to infinite machine.

Graduate Attributes (As per NBA)
Engineering Knowledge, Problem analysis.

Question paper pattern:
- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/Reference Books

1	Electric Machines	D. P. Kothari, et al	McGraw Hill	4th Edition, 2011
2	Performance and Design of A.C. Machines	M. G. Say	CBS Publishers	3rd Edition, 2002
3	Principles of Electric Machines and power Electronics	P.C.Sen	Wiley	2nd Edition, 2013
4	Electric Machines	MulukuntlaS.Sarma,at el	Cengage	1st Edition, 2009
5	Electrical Machines, Drives and Power systems	Theodore Wildi	Pearson	6th Edition, 2014
6	Electrical Machines	M.V. Deshpande	PHI Learning	1st Edition, 2013
7	Electrical Machines	AbhijitChakrabarti et al	McGraw Hill	1st Edition, 2015
8	A Textbook of Electrical Machines	K.R.SiddapuraD.B.Raval	Vikas	1st Edition, 2014

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - III			
ANALOG ELECTRONIC CIRCUITS (Core Course)			
Subject Code	15EE34	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80
Credits - 04			

Course objectives:

- Provide the knowledge for the analysis of diode and transistor circuits.
- Develop skills to design the electronic circuits like amplifiers and oscillators.
- Highlight the importance of FET and MOSFET. ■

Module-1		Teaching Hours
Diode Circuits: Review of diodes as rectifiers (No question shall be set from review portion). Diode clipping and clamping circuits. **Transistor biasing and stabilization**: Operating point, analysis and design of fixed bias circuit, self-bias circuit, Emitter stabilized bias circuit, voltage divider bias circuit, stability factor of different biasing circuits. Problems. **Transistor switching circuits:** Transistor switching circuits,PNP transistors, thermal compensation techniques. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Transistor at low frequencies: BJT transistor modelling, CE fixed bias configuration, voltage divider bias, emitter follower, CB configuration, collector feedback configuration, analysis using h – parameter model, relation between h – parameters model of CE, CC and CB modes, Millers theorem and its dual. **Transistor frequency response:** General frequency considerations, low frequency response, Miller effect capacitance, high frequency response, multistage frequency effects. ■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	
Module-3		
Multistage amplifiers: Cascade and cascode connections, Darlington circuits, analysis and design. **Feedback amplifiers:** Feedback concept, different types, practical feedback circuits, analysis and design of feedback circuits. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Power amplifiers: Amplifier types, analysis and design of different power amplifiers, distortion in power amplifiers. **Oscillators:** Principle of operation, analysis and derivation of frequency of oscillation of phase shift oscillator, Wien bridge oscillator, RF and crystal oscillator and frequency stability. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
FETs: Construction, working andcharacteristics of JFET and MOSFET. Biasing of JFET and MOSFET, JFET and MOSFET amplifiers, analysis and design. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - III

15EE34 ANALOG ELECTRONIC CIRCUITS (Core Subject) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Utilize the characteristics of transistor for different applications.
- Design and analyze biasing circuits for transistor.
- Design, analyze and test transistor circuitry as amplifiers and oscillators.

Graduate Attributes (As per NBA)
Engineering Knowledge, Problem Analysis, Modern tool usage,Ethics.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/Reference Books

1	Electronic Devices and Circuit Theory	Robert L Boylestad Louis Nashelsky	Pearson	11th Edition, 2015
2	Integrated Electronics, Analysis and Digital Circuits and Systems	Jacob Millman et al	McGraw Hill	2nd Edition, 2009
3	Electronic Devices and Circuits	David A Bell	Oxford University Press	5th Edition, 2008
4	Microelectronics Circuits Analysis and Design	Muhammad Rashid	Cengage Learning	2nd Edition, 2014
5	A Text Book of Electrical Technology, Electronic Devices and Circuits	B.L. Theraja, A.K. Theraja,	S. Chand	Reprint, 2013
6	Electronic Devices and Circuits	Anil K. Maini VashaAgarval	Wiley	1st Edition, 2009
7	Electronic Devices and Circuits	S.Salivahanan N.Suresh	McGraw Hill	3rd Edition, 2013
8	Fundamentals of Analog Circuits	Thomas L Floyd	Pearson	2nd Edition, 2012

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - III

DIGITAL SYSTEM DESIGN(Core Course)

Subject Code	15EE35	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To impart the knowledge of combinational circuit design.
- To impart the knowledge of Sequential circuit design.
- To provide the basic knowledge about VHDL & its use.■

Module-1		Teaching Hours
Principles of combinational logic: Definition of combinational, canonical forms, Generation of switching equations from truth tables, Karnaugh maps-3, 4 and 5 variables. Incompletely specified functions (Don't care terms). Simplifying max - term equations. Quine -McClusky minimization technique, Quine - McClusky using don't care terms, Reduced Prime Implicant tables, Map entered variables. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Analysis and design of Combinational Logic: General approach, Decoders-BCD decoders, Encoders. Digital multiplexers-using multiplexers as Boolean function generators. Adders and Subtractors-Cascading full adders, Look ahead carry, Binary comparators. Design methods of building blocks of combinational logics.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Sequential Circuits: Basic Bistable element, Latches, SR latch, application of SR latch, A Switch debouncer, The SR latch, The gated SR latch. The gated D Latch, The Master-Slave Flip-Flops (Pulse-Triggered Flip-Flops): The master-slave SR Flip-Flops, The master-slave JK Flip-Flop, Edge Triggered Flip-flop: The Positive Edge-Triggered D Flip-Flop, Negative-Edge Triggered D Flip-Flop. Characteristic equations, Registers, Counters-Binary Ripple Counter, Synchronous Binary counters, Counters based on Shift Registers, Design of a Synchronous counters, Design of a Synchronous Mod-6 counters using clocked JK Flip-Flops Design of a Synchronous Mod-6 counter using clocked D, T, or SR Flip-Flops. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Sequential Design: Introduction, Mealy and Moore models, State machine notation, synchronous sequential circuit analysis and design. Construction of state Diagrams, Counters Design. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
HDL: Introduction, A brief history of HDL, Structure of HDL Module, Operators, Data types, Types of Descriptions, Simulation and synthesis, Brief comparison of VHDL and Verilog. **Data-Flow Descriptions**: Highlights of Data flow descriptions, Structure of data-flow description, Data type-vectors. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - III

15EE35 DIGITAL SYSTEM DESIGN (Core Course) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Design and analyze combinational & sequential circuits
- Design circuits like adder, sub tractor, code converter etc.
- Understand counters and sequence generators.

Graduate Attributes (As per NBA)

Engineering Knowledge,Problem Analysis,Modern tool usage,Ethics.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/Reference Books

1	Digital Logic Applications and	John M Yarbrough	CengageLearn	2011
2	Digital Principles and Design	Donald D Givone	McGraw Hill	1st Edition, 2002
3	Logic and computer design Fundamentals	M. Morries Mano and Charles Kime	Pearson Learning	4th Edition, 2014
4	Fundamentals of logic design	Charles H Roth, JR and Larry L. Kinney	Cengage Learning	6th Edition, 2013
5	Fundamentals of Digital Circuits	A. Anand Kumar	PHI	3rd Edition, 2014
6	Digital Logic Design and VHDL	A.A.Phadke, S.M.Deokar	Wiley India	1st Edition, 2009
7	Digital Circuits and Design	D.P.KothariJ.S.Dhillon	Pearson	First Print 2015
8	HDL Programming (VHDL and Verilog)	Nazeih M. Botros	Cengage Learning	1st Edition, 2011
9	Circuit Design and Simulation with VHDL	Volnei A Pedroni	PHI	2nd Edition,

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - III

ELECTRICAL AND ELECTRONIC MEASUREMENTS (Foundation Course)

Subject Code	15EE36	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To understand the concept of units and dimensions.
- To measure resistance, inductance, capacitance by use of different bridges.
- To study the construction and working of various meters used for measurement.
- To have the working knowledge of electronic instruments and display devices.■

Module-1		Teaching Hours
Units and Dimensions: Review of fundamental and derived units. SI units (No question shall be set from the review portion). Dimensional equations, problems. **Measurement of Resistance:** Wheatstone's bridge, sensitivity, limitations. Kelvin's double bridge. Earth resistance measurement by fall of potential method and by using Megger. **Measurement of Inductance and Capacitance:** Sources and detectors, Maxwell's inductance bridge, Maxwell's inductance and capacitance bridge, Hay's bridge, Anderson's bridge, Desauty's bridge, Schering bridge. Shielding of bridges. Problems. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Measurement of Power, Energy, Power factor and Frequency: Review ofDynamometer wattmeter construction and operation (No question shall be set from the review portions), Torque expression, Errors and minimization, UPF and LPF wattmeters. Measurement of real and reactive power in 3 phase circuits. Review of Induction type energy meter construction and operation (No question shall be set from the review portions)]. Errors, adjustments and calibration of single and three phase energy meters, Problems. Construction and operation of single-phase and three phase dynamometer type power factor meter. Weston frequency meter and phase sequence indicator. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Extension of Instrument Ranges: Desirable features of ammeters and voltmeters.Shunts and multipliers. Construction and theory of instrument transformers, Desirable characterises, Errors of CT and PT. Turns compensation, Illustrative examples, Silsbee's method of testing CT. **Magnetic measurements:** Introduction, measurement of flux/ flux density, magnetising force and leakage factor. Hopkinson permeameter. Measurement of iron loss by wattmeter method. A brief discussion on measurement of air gap flux and field strength.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Electronic and digital Instruments: Introduction. Essentials of electronic instruments, Advantages of electronic instruments. True rms reading voltmeter. Electronic multimeters. Digital voltmeters (DVM) - Ramp type DVM, Integrating type DVM, Continuous – balance DVM and Successive - approximation DVM. Q meter. Principle of working of electronic energy meter (block diagram treatment), extra features offered by present day meters and their significance in billing.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - III

15EE36 ELECTRICAL AND ELECTRONIC MEASUREMENTS (Foundation Course) (continued)

Module-5	**Teaching Hours**
Display Devices: Introduction, character formats, segment displays, Dot matrix displays, Bar graph displays. Cathode ray tubes, Light emitting diodes, Liquid crystal displays, Nixes, Incandescent, Fluorescent, Liquid vapour and Visual displays. Display multiplexing and zero suppression. **Recording Devices:** Introduction, Strip chart recorders, Galvanometer recorders, Null balance recorders, Potentiometer type recorders, Bridge type recorders, LVDT type recorders, Circular chart and xy recorders. Magnetic tape recorders, Direct recording, Frequency modulation recording, Pulse duration modulation recording, Digital tape recording, Ultraviolet recorders. Biomedical recorders, Electro Cardio Graph (ECG),Electroencephalograph, Electromyograph. Noise in reproduction. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Explain the importance of units and dimensions.
- Measure resistance, inductance and capacitance by different methods.
- Explain the working of various meters used for measurement of power and energy.
- Explain the working of different electronic instruments and display devices.

Graduate Attributes (As per NBA)

Engineering Knowledge

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/Reference Books

1	Electrical and electronic Measurements and Instrumentation	A.K. Sawhney	DhanpatRai and Co	10th Edition
2	A Course in Electronics and Electrical Measurements and Instrumentation	J. B. Gupta	Katson Books	2013 Edition
3	Electrical and electronic Measurements and Instrumentation	Er.R.K. Rajput	S Chand	5th Edition, 2012
4	Electrical Measuring Instruments and Measurements	S.C. Bhargava	BS Publications	2013
5	Modern Electronic Instrumentation and Measuring Techniques	Cooper D and A.D. Heifrick	Pearson	First Edition, 2015
6	Electronic Instrumentation and Measurements	David A Bell	Oxford University	3rd Edition, 2013
7	Electronic Instrumentation	H.S.Kalsi	McGraw Hill	3rd Edition,2010

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - III			
ELECTRICAL MACHINES LABORATORY - 1			
Subject Code	15EEL37	IA Marks	20
Number ofPracticalHours/Week	03	Exam Hours	03
Total Number of PracticalHours	42	Exam Marks	80
Credits - 02			

Course objectives:

- Conducting of different tests on transformers and synchronous machines and evaluation of their performance.
- Verify the parallel operation of two single phase transformers.
- Study the connection of single phase transformers for three phase operation and phase conversion.
- Study of synchronous generator connected to infinite bus. ■

Sl. NO	Experiments
1	Open Circuit and Short circuit tests on single phase step up or step down transformer and predetermination of (i) Efficiency and regulation (ii) Calculation of parameters of equivalent circuit.
2	Sumpner's test on similar transformers and determination of combined and individual transformer efficiency.
3	Parallel operation of two dissimilar single-phase transformers of different kVA and determination of load sharing and analytical verification given the Short circuit test data.
4	Polarity test and connection of 3 single-phase transformers in star – delta and determination of efficiency and regulation under balanced resistive load.
5	Comparison of performance of 3 single-phase transformers in delta – delta and V – V (open delta) connection under load.
6	Scott connection with balanced and unbalanced loads.
7	Separation of hysteresis and eddy current losses in single phase transformer.
8	Voltage regulation of an alternator by EMF and MMF methods.
9	Voltage regulation of an alternator by ZPF method.
10	Slip test – Measurement of direct and quadrature axis reactance and predetermination of regulation of salient pole synchronous machines.
11	Performance of synchronous generator connected to infinite bus, under constant power and variable excitation & vice - versa.
12	Power angle curve of synchronous generator.

Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating

Course outcomes:

At the end of the course the student will be able to:

- Conduct different tests on transformers and synchronous generators and evaluate their performance.
- Connect and operate two single phase transformers of different KVA rating in parallel.
- Connect single phase transformers for three phase operation and phase conversion.
- Assess the performance of synchronous generator connected to infinite bus.

Graduate Attributes (As per NBA)

Engineering Knowledge,Problem Analysis, Individual and Team work,Communication.

Conduct of Practical Examination:

1. All laboratory experiments are to be included for practical examination.
2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners.
3. Students can pick one experiment from the questions lot prepared by the examiners.
4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - III

ELECTRONICS LABORATORY

Subject Code	15EEL38	IA Marks	20
Number ofPracticalHours/Week	03	Exam Hours	03
Total Number of PracticalHours	42	Exam Marks	80

Credits - 02

Course objectives:

- To design and test half wave and full wave rectifier circuits.
- To design and test different amplifier and oscillator circuits using BJT.
- To study the simplification of Boolean expressions using logic gates.
- To realize different Adders and Subtractors circuits.
- To design and test counters and sequence generators.■

Sl. No	Experiments
1	Design and Testing of Full wave – centre tapped transformer type and Bridge type rectifier circuits with and without Capacitor filter. Determination of ripple factor, regulation and efficiency.
2	Static Transistor characteristics for CE, CB and CC modes and determination of h parameters.
3	Frequency response of single stage BJT and FET RC coupled amplifier and determination of half power points, bandwidth, input and output impedances.
4	Design and testing of BJT - RC phase shift oscillator for given frequency of oscillation.
5	Determination of gain, input and output impedance of BJT Darlington emitter follower with and without bootstrapping.
6	Simplification, realization of Boolean expressions using logic gates/Universal gates.
7	Realization of half/Full adder and Half/Full Subtractors using logic gates.
8	Realization of parallel adder/Subtractors using 7483 chip- BCD to Excess-3 code conversion and Vice -Versa.
9	Realization of Binary to Gray code conversion and vice versa.
10	Design and testing Ring counter/Johnson counter.
11	Design and testing of Sequence generator.
12	Realization of 3 bit counters as a sequential circuit and MOD – N counter design using 7476, 7490, 74192, 74193.

Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating

Course outcomes:

At the end of the course the student will be able to:

- Design and test different diode circuits.
- Design and test amplifier and oscillator circuits and analyse their performance.
- Use universal gates and ICs for code conversion and arithmetic operations.
- Design and verify on of different counters.

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Individual and Team work, Communication.

Conduct of Practical Examination:

1. All laboratory experiments are to be included for practical examination.
2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners.
3. Students can pick one experiment from the questions lot prepared by the examiners.
4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.■

**** END ****

IV SEMESTER DETAILED SYLLABUS

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - IV			
ENGINEERING MATHEMATICS –IV (Core Subject)			
Subject Code	15MAT41	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80
Credits - 04			

Course Objectives:

The purpose of this course is to make students well conversant with numerical methods to solve ordinary differential equations, complex analysis, sampling theory and joint probability distribution and stochastic processes arising in science and engineering.■

Module-1		Teaching Hours
Numerical Methods: Numerical solution of ordinary differential equations of first order and first degree, Taylor's series method, modified Euler's method, Runge - Kutta method of fourth order. Milne's and Adams-Bashforth predictor and corrector methods (No derivations of formulae). ■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying.	
Module-2		
Numerical Methods: Numerical solution of second order ordinary differential equations, Runge-Kutta method and Milne's method. **Special Functions:** Series solution-Frobenious method. Series solution of Bessel's differential equation leading to $J_n(x)$-Bessel's function of first kind. Basic properties, recurrence relations and orthogonality. Series solution of Legendre's differential equation leading to $P_n(x)$-Legendre polynomials. Rodrigue's formula, problems.■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying.	
Module-3		
Complex Variables: Review of a function of a complex variable, limits, continuity, differentiability. Analytic functions-Cauchy-Riemann equations in cartesian and polar forms. Properties and construction of analytic functions. Complex line integrals-Cauchy's theorem and Cauchy's integral formula, Residue, poles, Cauchy's Residue theorem (without proof) and problems. **Transformations:** Conformal transformations, discussion of transformations: $w = z^2, w = e^z, w = z + (1/z)(z \neq 0)$and bilinear transformations-problems.■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – ApplyingL$_4$ – Analysing.	
Module-4		
Probability Distributions: Random variables (discrete and continuous),probability mass/density functions. Binomial distribution, Poisson distribution.Exponential and normal distributions, problems. **Joint probability distribution:** Joint Probability distribution for two discrete random variables, expectation, covariance, correlation coefficient. ■		**10**
Revised Bloom's Taxonomy Level	L_3 – Applying.	
Module-5		
Sampling Theory: Sampling, Sampling distributions, standard error, test of hypothesis for means and proportions, confidence limits for means, student's t-distribution, Chi-square distribution as a test of goodness of fit. **Stochastic process:** Stochastic processes, probability vector, stochastic matrices, fixed points, regular stochastic matrices, Markov chains, higher transition probability-simple problems. ■		**10**
Revised Bloom's Taxonomy Level	L_3 – ApplyingL$_4$ – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - IV

15MAT41 ENGINEERING MATHEMATICS – IV (Core Subject) (continued)

Course outcomes:

- Use appropriate single step and multi-step numerical methods to solve first and second order ordinary differential equations arising in flow data design problems.
- Explain the idea of analyticity, potential fields residues and poles of complex potentials in field theory and electromagnetic theory.
- Employ Bessel's functions and Legendre's polynomials for tackling problems arising in continuum mechanics, hydrodynamics and heat conduction.
- Describe random variables and probability distributions using rigorous statistical methods to analyze problems associated with optimization of digital circuits, information, coding theory and stability analysis of systems.
- Apply the knowledge of joint probability distributions and Markov chains in attempting engineering problems for feasible random events.■

Graduate Attributes (As per NBA)
Engineering Knowledge, Problem Analysis, Life-Long Learning, Accomplishment of Complex Problems.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks.
- Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module. ■

Text Books:

1	Higher Engineering Mathematics	B.S. Grewal	Khanna Publishers	43rd Edition, 2015
2	Advanced Engineering Mathematics	E. Kreyszig	John Wiley & Sons	10th Edition, 2015

Reference books:

3	A Text Book of Engineering Mathematics	N.P.Bali and Manish Goyal	Laxmi Publishers	7th Edition, 2010
4	Higher Engineering Mathematics	B.V.Ramana	McGraw-Hill	2006
5	Higher Engineerig Mathematics	H. K. Dass and Er. RajnishVerma	S.Chand publishing	First Edition, 2011

Web links and Video Lectures

1. http://nptel.ac.in/courses.php?disciplineID=111
2. http://wwww.khanacademy.org/
3. http://www.class-central.com/subject/math

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - IV

POWER GENERATION AND ECONOMICS(Core Subject)

Subject Code	15EE42	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- Explain the arrangement and operation of hydroelectric, steam, diesel, gas turbine and nuclear power plants and working of major equipment in the plants.
- Classification of substation and explain the operation of different substation equipment.
- Explain the importance of grounding and different grounding methods used in practice.
- Explain the economics of power generation and importance of power factor.

Module-1	**Teaching Hours**
Hydroelectric Power Plants: Hydrology, run off and stream flow, hydrograph, flow duration curve, Mass curve, reservoir capacity, dam storage. Hydrological cycle, merits and demerits of hydroelectric power plants, Selection of site. General arrangement of hydel plant, elements of the plant, Classification of the plants based on water flow regulation, water head and type of load the plant has to supply. Water turbines – Pelton wheel, Francis, Kaplan and propeller turbines. Characteristic of water turbines Governing of turbines, selection of water turbines. Underground, small hydro and pumped storage plants. Choice of size and number of units, plant layout and auxiliaries. ■	**10**
Revised Bloom's Taxonomy Level – L_1 – Remembering, L_2 – Understanding.	
Module-2	
Steam Power Plants: Introduction,Efficiency of steam plants, Merits and demerits of plants, selection of site. Working of steam plant, Power plant equipment and layout, Steam turbines, Fuels and fuel handling, Fuel combustion and combustion equipment, Coal burners, Fluidized bed combustion, Combustion control, Ash handling, Dust collection, Draught systems, Feed water, Steam power plant controls, plant auxiliaries. **Diesel Power Plant:** Introduction, Merits and demerits, selection site, elements of diesel power plant, applications. **Gas Turbine Power Plant**: Introduction, Merits and demerits, selection site, Fuels for gas turbines, Elements of simple gas turbine power plant, Methods of improving thermal efficiency of a simple steam power plant, Closed cycle gas turbine power plants. Comparison of gas power plant with steam and diesel power plants. ■	**10**
Revised Bloom's Taxonomy Level – L_1 – Remembering, L_2 – Understanding.	
Module-3	
Nuclear Power Plants: Introduction, Economics of nuclear plants, Merits and demerits, selection of site, Nuclear reaction, Nuclear fission process, Nuclear chain reaction, Nuclear energy, Nuclear fuels, Nuclear plant and layout, Nuclear reactor and its control, Classification of reactors, power reactors in use, Effects of nuclear plants, Disposal of nuclear waste and effluent, shielding.■	**10**
Revised Bloom's Taxonomy Level – L_1 – Remembering, L_2 – Understanding.	
Module-4	
Substations: Introduction to Substation equipment; Transformers, High Voltage Fuses, High Voltage Circuit Breakers and Protective Relaying, High Voltage Disconnect Switches, Lightning Arresters, High Voltage Insulators and Conductors, Voltage Regulators, Storage Batteries, Reactors, Capacitors, Measuring Instruments, and power line carrier communication equipment. Classification of substations – indoor and outdoor, Selection of site for substation, Busbar arrangement schemes and single line diagrams of substations.	**10**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - IV

15EE42 POWER GENERATION AND ECONOMICS(Core Subject) (continued)

Module-4 (continued)		Teaching Hours
Substations (continued): Interconnection of power stations. Introduction to gas insulated substation, Advantages and economics of Gas insulated substation. **Grounding:** Introduction, Difference between grounded and ungrounded system. System grounding – ungrounded, solid grounding, resistance grounding, reactance grounding, resonant grounding. Earthing transformer. Neutral grounding and neutral grounding transformer. ■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Economics: Introduction, Effect of variable load on power system, classification of costs, Cost analysis. Interest and Depreciation, Methods of determination of depreciation, Economics of Power generation, different terms considered for power plants and their significance, load sharing. Choice of size and number of generating plants. Tariffs, objective, factors affecting the tariff, types. Types of consumers and their tariff. Power factor, disadvantages, causes, methods of improving power factor, Advantages of improved power factor, economics of power factor improvement and comparison of methods of improving the power factor. Choice of equipment. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Describe the working of hydroelectric, steam, nuclear power plants and state functions of major equipment of the power plants.
- Classify various substations and explain the importance of grounding.
- Understand the economic aspects of power system operation and its effects.
- Explain the importance of power factor improvement.

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem analysis, Engineers and Society, Environment and Sustainability.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/Reference Books

1	A Course in Power Systems	J.B. Gupta	Katson	2008
2	Generation of Electrical Energy	B.R.Gupta	S. Chand	2015
3	Electrical power Generation, Transmission and Distribution	S.N. Singh	PHI	2nd Edition, 2009
4	Power Plant Engineering	P.K. Nag	McGrawHill	4th Edition, 2014
5	Electrical Power Distribution Systems	V. Kamaraju	McGrawHill	1st Edition, 2009
6	Electrical Distribution Engineering	Anthony J. Pansini	CRC Press	3rd Edition, 2006
7	Electrical Distribution Systems	Dale R PatrickEt al	CRC Press	2nd Edition, 2009
8	A Text Book on Power System Engineering	A.Chakrabarti, et al	DhanpathRai	2nd Edition, 2010

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -IV

TRANSMISSION AND DISTRIBUTION (Core Subject)

Subject Code	15EE43	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course Objectives:

- To understand the concepts of various methods of generation of power.
- To understand the importance of HVAC, EHVAC, UHVAC and HVDC transmission.
- To design insulators for a given voltage level.
- To calculate the parameters of the transmission line for different configurations and assess the performance of the line.
- To study underground cables for power transmission and evaluate different types of distribution systems.

Module-1	Teaching Hours
Introduction to power system: Structure of electric power system: generation, transmission and distribution. Advantages of higher voltage transmission: HVAC, EHVAC, UHVAC and HVDC. Interconnection. Feeders, distributors and service mains. **Overhead transmission lines:** A brief introduction to types of supporting structures and line conductors-Conventional conductors; Aluminium Conductor steel reinforced (ACSR), All –aluminium alloy conductor (AAAC) and All –aluminium conductor (AAC). High temperature conductors; Thermal resistant aluminium alloy (ATI),Super thermal resistant aluminium alloy (ZTAI), Gap type thermal resistant aluminium alloy conductor steel reinforced (GTACSR), Gap type super thermal resistant aluminium alloy conductor steel reinforced (GZTACSR). Bundle conductor and its advantages. Importance of sag, Sag calculation – supports at same and different levels, effect of wind and ice. Line vibration and vibration dampers. Overhead line protection against lightening; ground wires. **Overhead line Insulators:** A brief introduction to types of insulators, material used- porcelain, toughened glass and polymer (composite). Potential distribution over a string of suspension insulators. String efficiency, Methods of increasing string efficiency. Arcing horns. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding.	
Module-2	
Line parameters: Introduction to line parameters- resistance, inductance and capacitance. Calculation of inductance of single phase and three phase lines with equilateral spacing, unsymmetrical spacing, double circuit and transposed lines. Inductance of composite – conductors, geometric mean radius (GMR) and geometric mean distance (GMD). Calculation of capacitance of single phase and three phase lines with equilateral spacing, unsymmetrical spacing, double circuit and transposed lines. Capacitance of composite – conductor, geometric mean radius (GMR) and geometric mean distance (GMD). Advantages of single circuit and double circuit lines. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-3	
Performance of transmission lines: Classification of lines – short, medium and long. Current and voltage relations, line regulation and Ferranti effect in short length lines, medium length lines considering Nominal T and nominal π circuits, and long lines considering hyperbolic form equations. Equivalent circuit of a long line. ABCD constants in all cases. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4	
Corona: Phenomena, disruptive and visual critical voltages, corona loss. Advantages and disadvantages of corona. Methods of reducing corona.	**10**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -IV		
15EE43 TRANSMISSION AND DISTRIBUTION (Core Subject) (continued)		
Module-4 (continued)		**Teaching Hours**
Underground cable: Types of cables, constructional features, insulation resistance, thermal rating, charging current, grading of cables – capacitance and inter-sheath.Dielectric loss. Comparison between ac and dc cables. Limitations of cables.Specification of power cables. ■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
Distribution: Primary AC distribution systems – Radial feeders, parallel feeders, loop feeders and interconnected network system. Secondary AC distribution systems – Three phase 4 wire system and single phase 2 wire distribution, AC distributors with concentrated and uniform loads. Effect of disconnection of neutral in a 3 phase four wire system. **Reliability and Quality of Distribution system:** Introduction, definition of reliability, failure, probability concepts, limitation of distribution systems, power quality, Reliability aids. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course Outcomes:

At the end of the course the student will be able to:

- Explain the concepts of various methods of generation of power.
- Explain the importance of HVAC, EHVAC, UHVAC and HVDC transmission.
- Design and analyze overhead transmission system for a given voltage level.
- Calculate the parameters of the transmission line for different configurations and assess the performance of line.
- Explain the use of underground cables and evaluate different types of distribution systems.

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design / development of solutions, Engineers and society, Ethics.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/Reference Books:

1	A Course in Electrical Power	Soni Gupta and Bhatnagar	DhanpatRai	-
2	Power System Analysis and Design	J. Duncan Gloverat el	Cengage Learning	4th Edition 2008
3	Principles of Power System	V.K. Mehta, Rohit Mehta	S. Chand	1st Edition 2013
4	Electrical power Generation, Transmission and Distribution	S.N. Singh	PHI	2nd Edition,2009
5	Electrical Power	S.L.Uppal	Khanna Publication	
6	Electrical power systems	C. L. Wadhwa	New Age	5th Edition, 2009
7	Electrical power systems	AshfaqHussain	CBS Publication	
8	Electric Power Distribution	A.S. Pabla	McGraw-Hill	6th Edition,2012
9	*For* High temperature conductors refer www.jpowers.co.jp/english/product/pdf/gap_c1.pdfand Power System Analysis and Design, J. Duncan Glover at el			

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -IV			
ELECTRIC MOTORS (Core Subject)			
Subject Code	15EE44	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80
Credits - 04			

Course Objectives:

- To study the constructional features of Motors and select a suitable drive for specific application.
- To study the constructional features of Three Phase and Single phase induction Motors.
- To study different test to be conducted for the assessment of the performance characteristics of motors.
- To study the speed control of motor by a different methods.
- Explain the construction and operation of Synchronous motor and special motors.

Module-1		**Teaching Hours**
DC Motors: Classification, Back emf, Torque equation, and significance of back emf, Characteristics of shunt, series & compound motors. Speed control of shunt, series and compound motors. Application of motors. DC motor starters – 3 point and 4 point. **Losses and efficiency-** Losses in DC motors, power flow diagram, efficiency, condition for maximum efficiency. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Testing of dc motors: Direct & indirect methods of testing of DC motors-Brake test, Swinburne's test, Retardation test, Hopkinson's test, Field's test, merits and demerits of tests. **Three phase Induction motors:** Review of concept and generation of rotating magnetic field, Principle of operation, construction, classification and types; squirrel-cage, slip-ring (No question shall be set from the review portion). Slip, Torque equation, torque-slip characteristic covering motoring, generating and braking regions of operation, Maximum torque, significance of slip**.** ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Performance of three-phase Induction Motor: Phasor diagram of induction motor on no-load and on load, equivalent circuit, losses, efficiency, No-load and blocked rotor tests. Performance of the motor from the circle diagram and equivalent circuit. Cogging and crawling. High torque rotors-double cage and deep rotor bars. Equivalent circuit and performance evaluation of double cage induction motor. Induction motor working as induction generator; standalone operation and grid connected operation. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Starting and speed Control of Three-phase Induction Motors: Need for starter. Direct on line, Star-Delta and autotransformer starting. Rotor resistance starting. Speed control by voltage, frequency, and rotor resistance methods **Single-phase Induction Motor:** Double revolving field theory and principle of operation. Construction and operation of split-phase, capacitor start, capacitor run, and shaded pole motors. Comparison of single phase motors and applications. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
Synchronous motor: Principle of operation, phasor diagrams, torque and torque angle, Blondel diagram, effect of change in load, effect of change in excitation, V and inverted V curves. Synchronous condenser, hunting and damping. Methods of starting synchronous motors.		**10**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -IV

15EE44 ELECTRIC MOTORS (Core Subject) (continued)

Module-5 (continued)		**Teaching Hours**
Other motors: Construction and operation of Universal motor, AC servomotor, Linear induction motor and stepper motors.■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course Outcomes:

At the end of the course the student will be able to:

- Explain the constructional features of Motors and select a suitable drive for specific application.
- Analyze and assess the performance characteristics of DC motors by conducting suitable tests and control the speed by suitable method.
- Explain the constructional features of Three Phase and Single phase induction Motors and assess their performance.
- Control the speed of induction motor by a suitable method.
- Explain the operation of Synchronous motor and special motors.

Graduate Attributes (As per NBA)

Engineering Knowledge,Problem Analysis,Conduct investigations of complex Problems.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/Reference Books:

1	Electric Machines	D. P. Kothari, I. J. Nagrath	McGraw Hill	4th edition, 2011
2	Principles of Electric Machines and power Electronics	P.C.Sen	Wiley	2nd Edition, 2013
3	Electric Machines	R.K. Srivastava	Cengage Learning	2nd Edition,2013
4	Electrical Machines, Drives and Power systems	Theodore Wildi	Pearson	6th Edition, 2014
5	Electrical Machines	M.V. Deshpande	PHI Learning	2013
6	Electric Machinery and Transformers	Bhag S Guru at el	Oxford University Press	3rd Edition, 2012
7	Electric Machinery and Transformers	Irving Kosow	Pearson	2rd Edition, 2012
8	Theory of Alternating Current Machines	Alexander Langsdorf	McGraw Hill	2nd Edition, 2001

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -IV

ELECTROMAGNETIC FIELD THEORY (Core Subject)

Subject Code	15EE45	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course Objectives:

- To study different coordinate systems for understanding the concept of gradient, divergence and curl of a vector.
- To study the application of Coulomb's Law and Gauss Law for electric fields produced by different charge configurations.
- To evaluate the energy and potential due to a system of charges.
- To study the behavior of electric field across a boundary between a conductor and dielectric and between two different dielectrics.
- To study the magnetic fields and magnetic materials.
- To study the time varying fields and propagation of waves in different media.

Module-1		Teaching Hours
Vector Analysis: Scalars and Vectors**,** Vector algebra, Cartesian co-ordinate system, Vector components and unit vectors. Scalar field and Vector field. Dot product and Cross product, Gradient of a scalar field. Divergence and Curl of a vector field. Co – ordinate systems: cylindrical and spherical, relation between different coordinate systems. Expression for gradient, divergence and curl in rectangular, cylindrical and spherical co-ordinate systems. Problems. **Electrostatics:** Coulomb's law, Electric field intensity and its evaluation for (i) point charge (ii) line charge (iii) surface charge (iv) volume charge distributions. Electric flux density, Gauss law and its applications. Maxwell's first equation (Electrostatics). Divergence theorem. Problems. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Energy and Potential: Energy expended in moving a point charge in an electric field. The line integral. Definition of potential difference and potential. The potential field of a point charge and of a system of charges. Potential gradient. The dipole. Energy density in the electrostatic field. Problems. **Conductor and Dielectrics:** Current and current density. Continuity of current. Metallic conductors, conductor's properties and boundary conditions. Perfect dielectric materials, capacitance calculations. Parallel plate capacitor with two dielectrics with dielectric interface parallel to the conducting plates. Capacitance of two wire line. Problems. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-3		
Poisson's and Laplace equations: Derivations and problems, Uniqueness theorem. **Steady magnetic fields:** Biot - Savart's law, Ampere's circuital law. The Curl. Stokes theorem. Magnetic flux and flux density. Scalar and vector magnetic potentials. Problems.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-4		
Magnetic forces: Force on a moving charge and differential current element. Force between differential current elements. Force and torque on a closed circuit. Problems. **Magnetic materials and magnetism:** Nature of magnetic materials, magnetisation and permeability. Magnetic boundary conditions. Magnetic circuit, inductance and mutual inductance. Problems.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -IV

15EE45 ELECTROMAGNETIC FIELD THEORY (Core Subject) (continued)

Module-5		Teaching Hours
Time varying fields and Maxwell's equations: Faraday's law, Displacement current. Maxwell's equations in point form and integral form. Problems. **Uniform plane wave:** Wave propagation in free space and in dielectrics. Pointing vector and power considerations. Propagation in good conductors, skin effect. Problems. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course Outcomes:

At the end of the course the student will be able to:

- Use different coordinate systems to explain the concept of gradient, divergence and curl of a vector.
- Use Coulomb's Law and Gauss Law for the evaluation of electric fields produced by different charge configurations.
- Calculate the energy and potential due to a system of charges.
- Explain the behavior of electric field across a boundary between a conductor and dielectric and between two different dielectrics.
- Explain the behavior of magnetic fields and magnetic materials.
- Assess time varying fields and propagation of waves in different media.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Conduct investigations of complex Problems.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/Reference Books:

1	Engineering Electromagnetics	William H Hayt et al	McGraw Hill	8^{th}Edition, 2014
2	Principles of Electromagnetics	Matthew N. O. Sadiku	Oxford	6^{th} Edition, 2015
3	Fundamentals of Engineering Electromagnetics	David K. Cheng	Pearson	2014
4	Electromagnetism -Theory (Volume -1) -Applications (Volume-2)	AshutoshPramanik	PHI Learning	2014
5	Electromagnetic Field Theory Fundamentals	Bhag Guru et al	Cambridge	2005
6	Electromagnetic Field Theory	RohitKhurana	Vikas Publishing	1^{st} Edition,2014
7	Electromagnetics	J. A. Edminister	McGraw Hill	3^{rd} Edition, 2010
8	Electromagnetic Field Theory and Transmission Lines	GottapuSasibhushana Rao	Wiley	1st Edition, 2013

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -IV

OPERATIONAL AMPLIFIERS AND LINEAR ICs (Foundation Course)

Subject Code	15EE46	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course Objectives:
- To understand the basics of Linear ICs such as Op-amp, Regulator, Timer & PLL.
- To learn the designing of various circuits using linear ICs.
- To use these linear ICs for specific applications.
- To understand the concept and various types of converters.
- To use these ICs, in Hardware projects.

Module	Teaching Hours
Module-1	
Operational amplifiers: Introduction, Block diagram representation of a typical Op-amp, schematic symbol, characteristics of an Op-amp, ideal op-amp, equivalent circuit, ideal voltage transfer curve, open loop configuration, differential amplifier, inverting & non –inverting amplifier, Op-amp with negative feedback ; voltage series feedback amplifier-gain, input resistance, output resistance, voltage shunt feedback amplifier- gain, input resistance, output resistance. **General Linear Applications:** D.C. & A.C amplifiers, peaking amplifier, summing, scaling & averaging amplifier, inverting and non-inverting configuration, differential configuration, instrumentation amplifier.■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-2	
Active Filters: First & Second order high pass & low pass Butterworth filters, higher order filters Band pass filters, Band reject filters & all pass filters. **DC Voltage Regulators:** voltage regulator basics, voltage follower regulator, adjustable output regulator, LM317 & LM337 Integrated circuits regulators. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3	
Signal generators: Triangular / rectangular wave generator, phase shift oscillator, Wien bridge oscillator, oscillator amplitude stabilization, signal generator output controls. **Comparators & Converters:** Basic comparator, zero crossing detector, inverting & non-inverting Schmitt trigger circuit, voltage to current converter with grounded load, current to voltage converter and basics of voltage to frequency and frequency to voltage converters.■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4	
Signal processing circuits: Precision half wave & full wave rectifiers limiting circuits, clamping circuits, peak detectors, sample & hold circuits. **A/D & D/A Converters:** Basics, R–2R D/A Converter, Integrated circuit 8-bit D/A, successive approximation ADC, linear ramp ADC, dual slope ADC, digital ramp ADC. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5	
Phase Locked Loop (PLL): Basic PLL, components, performance factors, applications of PLL IC 565. **Timer:** Internal architecture of 555 timer, Mono stable, Astable multivibrators and applications. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)

SEMESTER -IV

15EE46 OPERATIONAL AMPLIFIERS AND LINEAR ICs (Foundation Course) (continued)

Course Outcomes:

At the end of the course the student will be able to:

- Explain the basics of linear ICs.
- Design circuits using linear ICs.
- Demonstrate the application of Linear ICs.
- Use ICs in the electronic projects.

Graduate Attributes (As per NBA)

Engineering Knowledge,Design / development of solutions,Conduct investigations of complex Problems.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/Reference Books:

1	Op-Amps and Linear Integrated Circuits	Ramakant A Gayakwad	Pearson	4th Edition 2015
2	Operational Amplifiers and Linear ICs	David A. Bell	Oxford	3rd Edition 2011
3	Linear Integrated Circuits; Analysis, Design and Applications	B. Somanthan Nair	Wiley India	2013
4	Linear Integrated Circuits	S. Salivahanan, et al	McGraw Hill	2nd Edition,2014
5	Operational Amplifiers and Linear Integrated Circuits	K. Lal Kishore	Pearson	1st Edition, 2012
6	Linear Integrated Circuits	Muhammad H Rashid	Cengage Learning	1st Edition,2014
7	Op-Amps and Linear Integrated Circuits, Concept and Application	James M Fiore	Cengage	2009

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - IV

ELECTRICAL MACHINES LABORATORY -2

Subject Code	15EEL47	IA Marks	20
Number of PracticalHours/Week	03	Exam Hours	03
Total Number of PracticalHours	42	Exam Marks	80

Credits - 02

Course Objectives:

- To perform tests on dc machines to determine their characteristics.
- To control the speed of dc motor.
- To conduct test for pre-determination of the performance characteristics of dc machines
- To conduct load test on single phase and three phase induction motor.
- To conduct test on induction motor to determine the performance characteristics.
- To conduct test on synchronous motor to draw the performance curves. ■

Sl. No	Experiments
1	Load test on dc shunt motor to draw speed – torque and horse power – efficiency characteristics.
2	Field Test on dc series machines.
3	Speed control of dc shunt motor by armature and field control.
4	Swinburne's Test on dc motor.
5	Retardation test on dc shunt motor.
6	Regenerative test on dc shunt machines.
7	Load test on three phase induction motor.
8	No - load and Blocked rotor test on three phase induction motor to draw (i) equivalent circuit and (ii)circle diagram. Determination of performance parameters at different load conditions from (i) and (ii).
9	Load test on induction generator.
10	Load test on single phase induction motor to draw output versus torque, current, power and efficiency characteristics.
11	Conduct suitable tests to draw the equivalent circuit of single phase induction motor and determine performance parameters.
12	Conduct an experiment to draw V and Λ curves of synchronous motor at no load and load conditions.

Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating

Course Outcomes:

At the end of the course the student will be able to:

- Test dc machines to determine their characteristics.
- Control the speed of dc motor.
- Pre-determine the performance characteristics of dc machines by conducting suitable tests.
- Perform load test on single phase and three phase induction motor to assess its performance.
- Conduct test on induction motor to pre-determine the performance characteristics.
- Conduct test on synchronous motor to draw the performance curves.

Graduate Attributes (As per NBA)

Engineering Knowledge, Individual and Team work, Communication.

Conduct of Practical Examination:

1. All laboratory experiments are to be included for practical examination.
2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners.
3. Students can pick one experiment from the questions lot prepared by the examiners.
4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - IV

OP- AMP AND LINEAR ICS LABORATORY

Subject Code	15EEL48	IA Marks	20
Number of PracticalHours/Week	03	Exam Hours	03
Total Number of PracticalHours	42	Exam Marks	80

Credits - 02

Course Objectives:

- To conduct different experiments using OP-Amps
- To conduct experiments using Linear IC's

a)Study of pin details, specifications, application features of IC741 (LM741) and IC555 (Timer) through corresponding datasheets (Datasheets are instruction manuals for electronic components. They explain exactly what a component does and how to use it.).
b)Comparison of output performance quantity of an Operational Amplifier obtained by rigging up the circuit with the ideal value of
(i) A Non – Inverting Amplifier ($V_{out} = AV_{in}$) (ii) An Inverting Amplifier ($V_{out} = -AV_{in}$) (iii) A Difference Amplifier ($V_{out} = -A(V_p - V_n)$) (iv) A Difference Amplifier with floating inputs ($V_{out} = AV_{in}$) (v) A Non – Inverting Amplifier with negative feedback (ii) An Inverting Amplifier with negative feedback (vi) A Differential Amplifier with a negative feedback (vii) A Differential Amplifier with negative feedback and equalised amplifications.
(viii) A Voltage follower (ix) A differential – in differential –out amplifier (x) An instrumentation amplifier
c) Plot of input and output transfer characteristics to analyse and conclude that op-amps are rarely used in open-loop.
d) Testing of op – amp.

To be covered in 03 Laboratory classes.

Sl. No	Experiments
1	Design and verify a precision full wave rectifier. Determine the performance parameters.
2	Design and realize to analyse the frequency response of an op – amp amplifier under inverting and non - inverting configuration for a given gain.
3	Design and verify the output waveform of an op – amp RC phase shift oscillator for a desired frequency.
4	Design and realize Schmitt trigger circuit using an op – amp for desired upper trip point (UTP) and lower trip point (LTP).
5	Verify the operation of an op – amp as (a) voltage comparator circuit and (b) zero crossing detector.
6	Design and verify the operation of op – amp as an (a) adder (b) subtractor (c) integrator and (d) differentiator.
7	Design and realize an op – amp based first order Butterworth (a) low pass (b) high pass and (c) band pass filters for a given cut off frequency/frequencies to verify the frequency response characteristic.
8	Design and realize an op – amp based function generator to generate sine, square and triangular waves of desired frequency.
9	Design and realization of R-2R ladder DAC.
10	Realization of Two bit Flash ADC
11	Design and verify an IC 555 timer based pulse generator for the specified pulse.
12	Designing of Fixed voltage power supply (voltage regulator) using IC regulators 78 series and 79 series.

Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating

Course Outcomes:

At the end of the course the student will be able to:

- To conduct experiment to determine the characteristic parameters of OP-Amp
- To design test the OP-Amp as Amplifier, adder, subtractor, differentiator and integrator

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) **CHOICE BASED CREDIT SYSTEM (CBCS)** **SEMESTER - IV**
15EEL48 OP- AMP AND LINEAR ICS LABORATORY (continued)
Course Outcomes (continued): • To design test the OP-Amp as oscillators and filters • Design and study of Linear IC's as multivibrator power supplies.
Graduate Attributes (As per NBA) Engineering Knowledge, Individual and Team work, Communication.
Conduct of Practical Examination: 1. All laboratory experiments are to be included for practical examination. 2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners. 3. Students can pick one experiment from the questions lot prepared by the examiners. 4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.

**** END ****

V SEMESTER DETAILED SYLLABUS

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – V

MANAGEMENT AND ENTREPRENEURSHIP (Core Course)

Subject Code	15EE51	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits – 04

Course objectives:

- To introduce the field of management, task of the manager, importance of planning and types of planning, staff recruitment and selection process.
- To discuss the ways in which work is allocation, structure of organizations, modes of communication and importance of managerial control in business.
- To explain need of coordination between the manager and staff, the social responsibility of business and leadership.
- Toexplaintheroleandimportanceoftheentrepreneurineconomicdevelopmentandtheconceptsof entrepreneurship.
- To explain various types of entrepreneurs and their functions, the myths of entrepreneurship and the factors required for capacity building for entrepreneurs
- To discuss theimportanceofSmallScaleIndustriesandtherelatedtermsandproblemsinvolved.
- To discuss methods for generatingnewbusinessideasandbusinessopportunitiesinIndiaandtheimportance of business plan.
- To introduce the concepts of project management and discuss capitol building process.
- To explain project feasibility study and project appraisal and discuss project financing
- To discuss about different institutions at state and central levels supporting business enterprises. ■

Module-1	Teaching Hours
Management: Definition, Importance – Nature and Characteristics of Management, Management Functions, Roles of Manager, Levels of Management, Managerial Skills, Management & Administration, Management as a Science, Art &Profession. **Planning:** Nature, Importance and Purpose Of Planning, Types of Plans, Steps in Planning, Limitations of Planning, Decision Making – Meaning, Types of Decisions- Steps in Decision Making.■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_4 – Analysing.	
Module-2	
Organizing and Staffing: Meaning, Nature and Characteristics of Organization – Process of Organization, Principles of Organization, Departmentalization, Committees – meaning, Types of Committees, Centralization Versus Decentralization of Authority and Responsibility, Span of Control (Definition only), Nature and Importance of Staffing, Process of Selection and Recruitment. **Directing and Controlling:** Meaning and Nature of Directing-Leadership Styles, Motivation Theories Communication – Meaning and Importance, Coordination- Meaning and Importance, Techniques of Coordination. Controlling – Meaning, Steps in Controlling.■	**10**
Revised Bloom's Taxonomy Level: L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3	
Social Responsibilities of Business: Meaning of Social Responsibility, Social Responsibilities of Business towards Different Groups, Social Audit, Business Ethics and Corporate Governance. **Entrepreneurship**: Definition of Entrepreneur, Importance of Entrepreneurship, concepts of Entrepreneurship, Characteristics of successful Entrepreneur, Classification of Entrepreneurs, Intrapreneur – An Emerging Class, Comparison between Entrepreneur and Intrapreneur, Myths of Entrepreneurship, Entrepreneurial Development models, Entrepreneurial development cycle, Problems faced by Entrepreneurs and capacity building for Entrepreneurship. ■	**10**
Revised Bloom's Taxonomy Level: L_3 – Applying.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER – V		
15EE51 MANAGEMENT AND ENTREPRENEURSHIP (Core Course) (continued)		
Module-4		**Teaching Hours**
Modern Small Business Enterprises: Role of Small Scale Industries, Concepts and definitions of SSI Enterprises, Government policy and development of the Small Scale sector in India, Growth and Performance of Small Scale Industries in India, Sickness in SSI sector, Problems for Small Scale Industries, Impact of Globalization on SSI, Impact of WTO/GATT on SSIs, Ancillary Industry and Tiny Industry (Definition only). **Institutional Support for Business Enterprises:** Introduction, Policies & Schemes of Central–Level Institutions, State-Level Institutions.■		**10**
Revised Bloom's Taxonomy Level	L_3 – Applying.	
Module-5		
Project Management: Meaning of Project, Project Objectives & Characteristics, Project Identification-Meaning & Importance; Project Life Cycle, Project Scheduling, Capital Budgeting, Generating an Investment Project Proposal, Project Report-Need and Significance of Report, Contents, Formulation, Project Analysis-Market, Technical, Financial, Economic, Ecological, Project Evaluation and Selection, Project Financing, Project Implementation Phase, Human & Administrative aspects of Project Management, Prerequisites for Successful Project Implementation. New Control Techniques- PERT and CPM, Steps involved in developing the network, Uses and Limitations of PERT and CPM .■		**10**
Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing. L_2 – Understanding, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Explain the field of management, task of the manager, planning and the need of proper staff, recruitment and selection process.
- Discuss work allocation, the structure of organization, the modes of communication and importance of managerial control in business.
- To explain need of coordination between the manager and staff in exercising the authority and delegating duties.
- To explain the social responsibility of business and leadership
- Explain the concepts of entrepreneurship and the role and importance of the entrepreneur in economic development.
- Show an understanding of the role and importance of Small Scale Industries, business plan and its presentation.
- Discuss the concepts of project management, capitol building process, project feasibility study, project appraisal and project financing.
- Discuss the state /central level institutions / agencies supporting business enterprises.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Life-Long Learning, Accomplishment of Complex Problems.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks. Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module. ■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER – V				
15EE51 MANAGEMENT AND ENTREPRENEURSHIP (Core Course) (continued)				
Textbooks				
1	Principles of Management	P.C.Tripathi, P.N.Reddy	McGraw Hill,	6^{th}Edition, 2017
2	Entrepreneurship Development And Small Business Enterprises	Poornima M.Charanthimath	Pearson	2^{nd}Edition,2014
Reference Books				
1	Dynamics of Entrepreneurial Development and Management	Vasant Desai	Himalaya Publishing House	2007
2	Essentials of Management: An International, Innovation and Leadership perspective	Harold Koontz, Heinz Weihrich	McGraw Hill	10^{th}Edition 2016

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER – V			
MICROCONTROLLER (Core Course)			
Subject Code	15EE52	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80
Credits – 04			

Course objectives:

- To explain the internal organization and working of Computers, microcontrollers and embedded processors.
- Compare and contrast the various members of the 8051 family.
- To explain the registers of the 8051 microcontroller, manipulation of data using registers and MOV instructions.
- To explain in detail the execution of 8051 Assembly language instructions and data types
  To explain loop, conditional and unconditional jump and call, handling and manipulation of I/O instructions.
- To explain different addressing modes of 8051, arithmetic, logic instructions, and programs.
- To explain develop 8051C programs for time delay, I/O operations, I/O bit manipulation,logic, arithmetic operations and data conversion. ■

Module-1		Teaching Hours
8051 Microcontroller Basics: Inside the Computer, Microcontrollers and Embedded Processors, Block Diagram of 8051, PSW and Flag Bits, 8051 Register Banks and Stack, Internal Memory Organization of 8051, IO Port Usage in 8051, Types of Special Function Registers and their uses in 8051, Pins Of 8051. Memory Address Decoding, 8031/51 Interfacing With External ROM And RAM.8051 Addressing Modes. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-2		
Assembly programming and instruction of 8051: Introduction to 8051 assembly programming, Assembling and running an 8051 program, Data types and Assembler directives, Arithmetic, logic instructions and programs, Jump, loop and call instructions, IO port programming. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
8051 programming in C: Data types and time delay in 8051C, IO programming in 8051C, Logic operations in 8051 C, Data conversion program in 8051 C, Accessing code ROM space in 8051C, Data serialization using 8051C **8051 Timer programming in Assembly and C:** Programming 8051 timers, Counter programming, Programming timers 0 and 1 in 8051 C.■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying,L_4 – Analysing, L_5 – Evaluating.	
Module-4		
8051 serial port programming in assembly and C: Basics of serial communication, 8051 connection to RS232, 8051 serial port programming in assembly, serial port programming in 8051 C. **8051 Interrupt programming in assembly and C:** 8051 interrupts, Programming timer, external hardware, serial communication interrupt, Interrupt priority in 8051/52, Interrupt programming in C. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER – V		
15EE52 MICROCONTROLLER (Core Course) (continued)		
Module-5		**Teaching Hours**
Interfacing: LCD interfacing, Keyboard interfacing. **ADC, DAC and sensor interfacing:** ADC 0808 interfacing to 8051, Serial ADC Max1112 ADC interfacing to 8051, DAC interfacing, Sensor interfacing and signal conditioning. **Motor control: Relay, PWM, DC and stepper motor:** Relays and opt isolators, stepper motor interfacing, DC motor interfacing and PWM. **8051 interfacing with 8255:** Programming the 8255, 8255 interfacing, C programming for 8255.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss the history of the 8051 and features of other 8051 family members and the internal architecture of the 8051.
- Explains the use of an 8051 assembler, the stack and the flag register, loop, jump, and call instructions.
- Discuss 8051 addressing modes, accessing data and I/O port programming, arithmetic, logic instructions, and programs.
- Develop 8051C programs for time delay, I/O operations, I/O bit manipulation, logic and arithmetic operations, data conversion and data serialization
- Discuss the hardware connection of the 8051 chip, its timers, serial data communication and its interfacing of 8051to the RS232.
- Discuss in detail 8051 interrupts and writing interrupt handler programs.
- Interface 8051 with real-world devices such as LCDs and keyboards, ADC, DAC chips and sensors.
- Interface 8031/51 with external memories, 8255 chip to add ports and relays, opt isolators and motors.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem analysis.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks. Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module. ■

Textbook				
1	The 8051 Microcontroller and Embedded Systems Using Assembly and C	Muhammad Ali Mazadi	Pearson	2nd Edition, 2008.
Reference Books				
1	The 8051 Microcontroller	Kenneth Ayala	Cengage Learning	3rd Edition, 2005
2	The 8051 Microcontroller and Embedded Systems	Manish K Patel	McGraw Hill	2014
3	Microcontrollers: Architecture, Programming, Interfacing and System Design	Raj Kamal	Pearson	1st Edition, 2012

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER – V			
POWER ELECTRONICS (Core Course)			
Subject Code	15EE53	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80
Credits – 04			

Course objectives:

- To give an overview of applications power electronics, different types of power semiconductor devices, their switching characteristics.
- To explain power diode characteristics, types, their operation and the effects of power diodes on RL circuits.
- To explain the techniques for design and analysis of single phase diode rectifier circuits.
- To explain different power transistors, their steady state and switching characteristics and imitations.
- To explain different types of Thyristors, their gate characteristics and gate control requirements.
- To explain the design, analysis techniques, performance parameters and characteristics of controlled rectifiers, DC- DC, DC -AC converters and Voltage controllers.■

Module-1		Teaching Hours
Introduction: Applications of Power Electronics, Types of Power Electronic Circuits, Peripheral Effects, Characteristics and Specifications of Switches. **Power Diodes:** Introduction, Diode Characteristics, Reverse Recovery Characteristics, Power Diode Types, Silicon Carbide Diodes, Silicon Carbide Schottky Diodes, Diode Switched *RL* Load, Freewheeling Diodes with Switched RLLoad. **Diode Rectifiers:**Introduction, Single-Phase Full-Wave Rectifiers, Single-Phase Full-Wave Rectifier with RL Load, Single-Phase Full-Wave Rectifier with a Highly Inductive Load.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering,L_2 – Understanding,L_3 – Applying,L_4 – Analysing	
Module-2		
Power Transistors: Introduction, Power MOSFETs – Steady State Characteristics, Switching Characteristics Bipolar Junction Transistors – Steady State Characteristics, Switching Characteristics, Switching Limits, IGBTs, MOSFET Gate Drive, BJT Base Drive, Isolation of Gate and Base Drives, Pulse transformers and Opto-couplers.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering,L_2 – Understanding,L_3 – Applying,L_4 – Analysing	
Module-3		
Thyristors: Introduction, Thyristor Characteristics, Two-Transistor Model of Thyristor, Thyristor Turn-On, Thyristor Turn-Off, A brief study on Thyristor Types, Series Operation of Thyristors, Parallel Operation of Thyristors, *di/dt*Protection, *dv/dt*Protection, DIACs, Thyristor Firing Circuits, Unijunction Transistor.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering,L_2 – Understanding,L_3 – Applying,L_4 – Analysing	
Module-4		
Controlled Rectifiers: Introduction, Single-Phase Full Converters, Single-Phase Dual Converters, Three- Phase Full Converters, Three-Phase Dual Converters, **AC Voltage Controllers:** Introduction, Single-Phase Full-Wave Controllers with Resistive Loads, Single-Phase Full-Wave Controllers with Inductive Loads, Three-Phase Full-Wave Controllers. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – V

15EE53 POWER ELECTRONICS (Core Course) (continued)

Module-5		**Teaching Hours**
DC-DC Converters: Introduction, principle of step down and step up chopper with RL load, performance parameters, DC-DC converter classification. **DC-AC converters**: Introduction, principle of operation single phase bridge inverters, three phase bridge inverters, voltage control of single phase inverters, Harmonic reductions, Current source inverters.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Explain application area of power electronics, types of power electronic circuits and switches their characteristics and specifications.
- Explain types of power diodes, their characteristics, and the effects of power diodes on RL circuits.
- Explain the techniques for design, operation and analysis of single phase diode rectifier circuits.
- Explain steady state, switching characteristics and gate control requirements of different power transistors and their limitations.
- Discuss different types of Thyristors, their operation, gate characteristics and gate control requirements.
- Explain designing, analysis techniques and characteristics of thyristor controlled rectifiers.
- Discuss the principle of operation of single phase and three phase DC - DC, DC –AC converters and AC voltage controllers.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem analysis.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Power Electronics: Circuits Devices and Applications	Mohammad H Rashid,	Pearson	4th Edition, 2014

Reference Books

1	Power Electronics: Converters, Applications and Design	Ned Mohan et al	Wiley	3rd Edition, 2014
2	Power Electronics	Daniel W Hart	McGraw Hill	1st Edition, 2011
3	Elements of Power Electronics	Philip T Krein	Oxford	Indian Edition, 2008

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – V

SIGNALS AND SYSTEMS (Core Course)

Subject Code	15EE54	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits – 04

Course objectives:

- To discuss arising of signals in different systems.
- To classify the signals and define certain elementary signals.
- To explain basic operations on signals and properties of systems.
- To explain the use of convolution integral and convolution summation in analyzing the response of linear time invariant systems in continuous and discrete time domains.
- To explain the properties of linear time invariant systems in terms of impulse response description.
- To explain determination of response of a given linear time invariant system and to provide a block diagram representation to it.
- To explain Fourier transform representation of continuous time and discrete time non –periodic signals and the properties of Fourier Transforms.
- To explain the applications of Fourier transform representation to study signals and linear time invariant systems.
- To explain the use of Z-transform in the complex exponential representation of discrete time signals and the analysis of systems. ■

Module-1		Teaching Hours
Introduction: Definitions of signals and a system, classification of signals, basic operations on signals. Elementary signals viewed as interconnections of operations, properties of systems. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L – 4 Analysing, L_5 – Evaluating.	
Module-2		
Time – Domain Representations For LTI Systems: Convolution, impulse response, properties, solution of differential and difference equations, block diagram representation.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	
Module-3		
The Continuous-Time Fourier Transform: Representation of a non -periodic signals: continuous-time Fourier transform (FT), Properties of continuous-time Fourier transform, Applications. Frequency response of LTI systems, Solutions of differential equations■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	
Module-4		
The Discrete-Time Fourier Transform: Representations of non-periodic signals: The discrete-time Fourier transform (DTFT), Properties of DTFT and applications. Frequency response of LTI system, Solutions of differential equations.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating..	
Module-5		
Z- Transforms: Introduction, Z-transform, properties of ROC, properties of Z-transforms, inversion of Z-transform methods - power series and partial expansion, Transforms analysis of LTI systems, transfer function, stability and causality, unilateral Z-transform and its application to solve difference equations. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – V

15EE54 SIGNALS AND SYSTEMS (Core Subject) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Classify the signals and systems.
- Explain basic operations on signals and properties of systems.
- Use convolution in both continuous and discrete domain for the analysis of systems given the impulse response of a system.
- Evaluate response of a given linear time invariant system.
- Provide block diagram representation of a linear time invariant system.
- Apply continuous time Fourier transform representation to study signals and linear time invariant systems.
- Apply discrete time Fourier transform representation to study signals and linear time invariant systems. Use Z-transform and properties of Z transform for the analysis of discrete time systems.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Modern tool usage, Ethics.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Signals and Systems	Simon Haykin, Berry Van Veen	Wiley	2nd Edition,2002

Reference Books

2	Fundamentals of Signals and Systems	Michael J. Roberts, Govind K Sharma	McGraw Hill	2nd Edition 2010
3	Signals and Systems	NagoorKani	McGraw Hill	1st Edition 2010
4	Signals and Systems A Primer with MATLAB	Matthew N.O. Sadiku Warsame H. Ali	CRC Press	1st Edition, 2016
5	Signals and Systems	Anand Kumar	PHI	3rd Edition, 2015

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER –V			
INTRODUCTION TO NUCLEAR POWER (Professional Elective)			
Subject Code	15EE551	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits – 03			

Course objectives:

- To explain the fission process in nuclear materials and how the nuclear reactors work and the basic components of nuclear reactors and their types.
- Explanation about cooling of reactors, features of coolant, different types of coolants used in the reactors and the losses of cooling.
- Discussion on loss of cooling accidents in different reactors.
- Discussion on postulated severe accidents in water cooled reactors and other reactors and cooling of reactor during removal and processing.
- Discussion on cooling and disposing the nuclear waste and prospect of fusion energy in the future.■

Module		Teaching Hours
Module-1		
The Earth and Nuclear Power: Sources and Resources: Introduction, Earth's Internal Heat Generation, The Earth's Energy Flow, The Fission Process, Thermal Energy Resources. **How Reactors Work:** Introduction, The Fission Process, Basic Components of a Nuclear Reactor, Thermal Reactors, Fast Reactors. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Cooling Reactors: Introduction, General Features of a Reactor Coolant, Principles of Heat Transfer, Gaseous Coolants, Liquid Coolants, Boiling Coolants. **Loss of Cooling:** Introduction, The Electric Kettle, Pressurized-Water Reactor, Boiling-Water Reactor, CANDU Reactor, Gas-Cooled Reactors, Sodium- Cooled Fast Reactor. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Loss-of-Cooling Accidents: Introduction, Incidents in light Water-Cooled Reactors, Heavy Water-Moderated Reactors, Gas-Cooled Reactors, Liquid Metal-Cooled Fast Reactors.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Postulated Severe Accidents Introduction: Introduction, Postulated Severe Accidents in Water-Cooled Reactors, Specific Phenomena relating to Severe Accidents, Severe Accidents in other Reactor Types, Fission Product Dispersion following Containment Failure. **Cooling during Fuel Removal and Processing**: Introduction, Refuelling, Spent Fuel Storage and Transport, Reprocessing Plant. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
Cooling and Disposing of the Waste: Introduction, Classification of Waste Products, Fission Products and Their Biological Significance, Options for Nuclear Waste Disposal, Long-Term Storage and Disposal of Spent Nuclear Fuel, Storage and Disposal of Fission Products from Reprocessing Plants, Disposal of other Materials. **Fusion Energy -Prospect for the Future:** Introduction, The Fusion Process, Confinement, Current Technical Position, Conclusions. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER –V

15EE551INTRODUCTION TO NUCLEAR POWER (Professional Elective) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Explain the fission process in nuclear materials, basic components of nuclear reactors, types of nuclear reactors and their working.
- Discuss different types of coolants, their features, and cooling of reactors,
- Discuss loss of cooling accidents in different reactors.
- Discuss postulated severe accidents in reactors and cooling of reactor during removal of spent fuel.
- Discuss cooling and disposing the nuclear waste and prospect of fusion energy in the future.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Design/ Development of Solutions, The Engineer and Society, Environment and Sustainability, Ethics, Project Management and Finance.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Introduction to Nuclear Power	Geoffrey F. Hewitt	Taylor & Francis	1st Edition, 2000

Reference Books

1	Nuclear Reactor Engineering	G.Vaidyanathan	S.Chand	1st Edition, 2013
2	Introduction to Nuclear Engineering	John R Lamarsh Anthony J Baratta	Pearson	3rd Edition, 2016

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER – V			
ELECTRICAL ENGINEERING MATERIALS (Professional Elective)			
Subject Code	15EE552	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits – 03			

Course objectives:

- To impart the knowledge of conducting, dielectric, insulating and magnetic materials and their applications.
- To impart the knowledge of superconducting materials and their applications
- To impart the knowledge of plastics and materials for Opto - Electronic devices. ■

Module-1		Teaching Hours
Introduction to Electrical and Electronic Materials: Importance of materials, Classification of electrical and electronic materials, Scope of electrical and electronic materials, Requirement of Engineering materials, Operational requirements of electrical and electronic materials, Classification of solids on the basis of energy gap, Products – working principle and materials, Types of engineering materials, Levels of material structure. Spintronics and Spintronic materials, Ferromagnetic semiconductors, Left handed materials. **Conductors:** Conductor materials, Factors affecting conductivity, Thermal conductivity, Heating effect of current, Thermoelectric effect, Seebeck effect, Thomson effect, Wiedemann – Franz law and Lorentz relation, Problems .■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-2		
Conductive Materials and Applications: Mechanically processed forms of electrical materials, Types of conducting materials, Low resistivity materials, High resistivity materials, Contact materials, Fusible materials, Filament materials, Carbon as filamentary and brush material, Material for conductors, cables, wires, solder, sheathing and sealing. **Dielectrics:** Introduction to dielectric materials, classification of dielectric materials, Dielectric constant, Dielectric strength and Dielectric loss. Polarization, Mechanisms of polarization, Comparison of different polarization process, Factors affecting polarization, Spontaneous polarization, Behaviour of polarization under impulse and frequency switching, Decay and build-up of polarization under ac field, Complex dielectric constant.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-3		
Insulating Materials: Insulating materials and applications – Ceramic, Mica, Porcelain, Glass, Micanite and Glass bonded mica. Polymeric materials – Bakelite, Polyethylene. Natural and synthetic rubber. Paper. Choice of solid insulating material for different applications, Liquid insulating materials – Requirements, Transformer oil, Bubble theory, Aging of mineral insulating oils. Gaseous insulating Materials – Air, Nitrogen, Vacuum. **Magnetic Materials:** Origin of permanent magnetic dipole, Magnetic terminology, Relation between relative permeability and magnetic susceptibility. Classification of magnetic materials, Diamagnetic, Paramagnetism, Ferromagnetism, Antiferromagnetism and the corresponding materials. Ferrimagnetism and ferrites – properties and applications, Soft and hard ferrites. Curie temperature, Laws of magnetic materials. Magnetization curve, Initial and maximum permeability. Hysteresis loop and loss, Eddy current loss. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-4		
Magnetic Materials (continued): Types of magnetic materials, Soft and hard magnetic materials, High energy magnetic materials, Commercial grade soft and hard magnetic materials. **Superconductive Materials:** Concept of superconductors, Meaning of phenomenon of superconductivity, Properties of superconductors, Types of superconductors, Critical magnetic field		**08**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – V

15EE552 ELECTRICAL ENGINEERING MATERIALS (Professional Elective) (continued)

Module-4 (continued)		Teaching Hours
Superconductive Materials (continued): and critical temperature, Effects of Isotopic mass on critical temperature, Silsbee rule, Depth of penetration and coherence length. Ideal and Hard superconductors, Mechanism of super conduction, London's theory for Type I superconductors, GLAG theory for Type I superconductors, BCS theory, Applications and limitations. Applications of high temperature superconductors, Superconducting solenoids and magnets, MRI for medical diagnostics.■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Plastics: Introduction, Thermoplastics, Rubbers, Thermosets, DC and AC properties, Mechanical properties and processing of plastic. **Materials for Opto – Electronic Devices:** Introduction, Optical phenomena, Reflection, Refraction, Transmittivity, Scattering, Optical absorption, Optical properties of non-metals, Optical properties of metals, Optical properties of semiconductors, Optical properties of insulators. Luminescence, Opto – Electronic devices, Photoconductivity, Photoconductive cell.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:
At the end of the course the student will be able to:
- Discuss electrical and electronics materials, their importance, classification and operational requirement
- Discuss conducting materials used in engineering, their properties and classification.
- Discuss dielectric materials used in engineering, their properties and classification.
- Discuss insulating materials used in engineering, their properties and classification.
- Discuss magnetic materials used in engineering, their properties and classification
- Explain the phenomenon superconductivity, super conducting materials and their application in engineering.
- Explain the plastic and its properties and applications.
- Discuss materials used for Opto electronic devices.■

Graduate Attributes (As per NBA)
Engineering Knowledge

Question paper pattern:
- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Advanced Electrical and Electronics Materials; Processes and Applications	K.M. Gupta Nishu Gupta	Wiley	First Edition, 2015

Reference Books

1	Electronic Engineering Materials	R.K. Shukla Archana Singh	McGraw Hill	2012
2	Electrical Properties of Materials	L Solymar et al	Oxford	9th Edition, 2014
3	Electrical Engineering Materials	A.J. Dekker	Pearson	2016
4	Principle of Electronic Materials and Devices	S.O. Kasap	McGraw Hill	3rd Edition 2010

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -V

ELECTRICAL ESTMATION AND COSTING (Professional Elective)

Subject Code	15EE553	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80

Credits - 03

Course objectives:

- To discuss the purpose of estimation and costing.
- To discuss market survey, estimates, purchase enquiries, tenders, comparative statement and payment of bills and Indian electricity act and some of the rules.
- To discuss distribution of energy in a building, wiring and methods of wiring, cables used in internal wiring, wiring accessories, fittings and fuses.
- To discuss design of lighting points and its number, total load, sub-circuits, size of conductor.
- To discuss different types of service mains and estimation of power circuits.
- To discuss estimation of overhead transmission and distribution system and its components.
  To discuss main components of a substation, their graphical representation and preparation of single line diagram of a substation. ■

Module-1		Teaching Hours
Principles of Estimation: Introduction to Estimation and Costing, Electrical Schedule, Catalogues, Market Survey and Source Selection, Recording of Estimates, Determination of Required Quantity of Material, LabourConditions, Determination of Cost Material and Labour, Contingencies, Overhead Charges, Profit, Purchase System, Purchase Enquiry and Selection of Appropriate Purchase Mode, Comparative Statement, Purchase Orders, Payment Of Bills, Tender Form, General Idea about IE Rule, Indian Electricity(IE) Act and IE Rules -29,30,45,46,47,50,51,54,55,77 and79.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-2		
Wiring: Introduction, Distribution of energy in a Building, PVC Casing and Capping, Conduit Wiring, Desirabilities of Wiring. Types of cables used in Internal Wiring, Multi Strand Cables, Voltage Grading and Specification of Cables **Wiring (continued):** Main Switch and Distribution Board, Conduits and its accessories and Fittings. Lighting Accessories and Fittings, Types of Fuses, Size of Fuse, Fuse Units, Earthing Conductor. **Internal Wiring:** General rules for wiring, Design of Lighting Points (Refer to Seventh Chapter of the Textbook), Number of Points, Determination of Total Load, Number of Sub –Circuits, Ratings Main Switch and Distribution Board and Size of Conductor. Current Density, Layout. ..■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Service Mains: Introduction, Types, Estimation of Underground and Overhead Service Connections. **Design and Estimation of Power Circuits:** Introduction, Important Considerations Regarding Motor Installation Wiring, Input Power, Input Current to Motors, Rating of Cables, Rating of Fuse, Size of Condit, Distribution Board Main Switch and Starter. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Estimation of Overhead Transmission and Distribution Lines: (Review of Line Supports, Conductor Materials, Size of Conductor for Overhead Transmission Line, Types of Insulators)[No Question Shall be Set From the Review Portion]. Cross Arms, Pole Brackets and Clamps, Guys and Stays, Conductors Configuration Spacing and Clearances, Span Lengths, Lightning Arrestors, Phase Plates, Danger Plates, Anti Climbing Devices, Bird Guards, Beads of Jumpers, Muffs, Points to be Considered at the Time of Erection of Overhead Lines, Erection of Supports, Setting of Stays, Fixing of Cross Arms, Fixing of Insulators, Conductor Erection.		**08**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -V		
15EE553 ELECTRICAL ESTMATION AND COSTING (Professional Elective) (continued)		
Module-4 (continued)		**Teaching Hours**
Estimation of Overhead Transmission and Distribution Lines (continued): Repairing and Jointing of Conductors, Dead End Clamps, Positioning of Conductors and Attachment to Insulators, Jumpers, Tee-Offs, Earthing of Transmission Lines, Guarding of Overhead Lines, Clearances of Conductor From Ground, Spacing Between Conductors, Important Specifications. ■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding. L_3 – Applying, L_4 – Analysing	
Module-5		
Estimation of Substations: Main Electrical connection, Graphical Symbols for Various Types of Apparatus and Circuit Elements on Substation main Connection Diagram, Single Line Diagram of Typical Substations, Equipmentfor Substation, Substation Auxiliaries Supply, Substation Earthing.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Explain the purpose of estimation and costing.
- Discuss market survey, estimates, purchase enquiries, preparation of tenders, comparative statements and payment of bills.
- Discuss Indian Electricity act and Indian Electricity rules.
- Discuss distribution of energy in a building, wiring and methods of wiring, cables used in internal wiring, wiring accessories and fittings, fuses and types of fuses.
- Discuss design of lighting points and its number, total load, sub-circuits, size of conductor.
- Discuss types of service mainsand estimation of service mains and power circuits.
- Discuss estimation of overhead transmission and distribution system and its components.
- Discuss main components of a substation, preparation of single line diagram of a substation and earthing of a substation.■

Graduate Attributes (As per NBA)

Engineering Knowledge,

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	A Course in Electrical Installation Estimating and Costing	J. B. Gupta	Katson Books,	9th Edition, 2012

<table>
<tr><td colspan="4">B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – V</td></tr>
<tr><td colspan="4">SPECIAL ELECTRICAL MACHINES (Professional Elective)</td></tr>
<tr><td>Subject Code</td><td>15EE554</td><td>IA Marks</td><td>20</td></tr>
<tr><td>Number of Lecture Hours/Week</td><td>03</td><td>Exam Hours</td><td>03</td></tr>
<tr><td>Total Number of Lecture Hours</td><td>40</td><td>Exam Marks</td><td>80</td></tr>
<tr><td colspan="4">Credits – 03</td></tr>
</table>

Course objectives:

- To impart knowledge on the Construction, principle of operation, control and performance of stepping motors.
- To impart knowledge on the Construction, principle of operation, control and performance of switched reluctance motors and permanent magnet brushless D.C. motors.
- To impart knowledge on the Construction, principle of operation and performance of permanent magnet synchronous motors and synchronous reluctance motor.
- To impart knowledge on single phase special machines and servo motors.
- To impart knowledge on Linear electrical machine and permanent magnet axial flux machines.■

<table>
<tr><td colspan="2">Module-1</td><td>Teaching Hours</td></tr>
<tr><td colspan="2">Stepper Motor: Introduction, Variable Reluctance Stepper Motor, Permanent Magnet Stepper Motor, Hybrid Stepper Motor, Other Types of Stepper Motor, Windings in Stepper Motors, Torque Equation, Characteristics of Stepper Motor, Open – loop Control of Stepper Motor, Closed – loop Control of Stepper Motor, Microprocessor – Based Control of Stepper Motor, Applications of Stepper Motor. ■</td><td>08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding.</td><td></td></tr>
<tr><td colspan="3">Module-2</td></tr>
<tr><td colspan="2">Switched Reluctance Motor (SRM): Construction, Principle of Working, Basics of SRM Analysis, Constraints on Pole Arc and Tooth Arc, Torque Equation and Characteristics, Power Converter Circuits, Control of SRM, Rotor Position Sensors, Current Regulators, Microprocessor – Based Control of SRM, Sensorless Control of SRM.
Permanent Magnet DC Motor and Brushless Permanent Magnet DC Motor: Permanent Magnet DC (PMDC) motor, Brushless Permanent Magnet DC (BLDC) Motors.■</td><td>08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding.</td><td></td></tr>
<tr><td colspan="3">Module-3</td></tr>
<tr><td colspan="2">Permanent Magnet Synchronous Motor (PMSM): Construction, Principle of Operation, EMF Equation, Torque Equation, Phasor Diagram, Circle Diagram, Comparison of Conventional and PMSM, Control of PMSM, Applications.
Synchronous Reluctance Motor (SyRM): Constructional of SyRM, Working, Phasor Diagram and Torque Equation, Control of SyRM, Advantages and Applications. ■</td><td>08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding.</td><td></td></tr>
<tr><td colspan="3">Module-4</td></tr>
<tr><td colspan="2">Single Phase Special Electrical Machines: AC series Motor, Repulsion Motor, Hysteresis Motor, Single Phase Reluctance Motor, Universal Motor.
Servo Motors: DC Servo Motors, AC Servo Motors. ■</td><td>08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding.</td><td></td></tr>
<tr><td colspan="3">Module-5</td></tr>
<tr><td colspan="2">Linear Electric Machines: Linear Induction Motor, Linear Synchronous Motor, DC Linear Motor, Linear Reluctance Motor, Linear Levitation Machines.
Permanent Magnet Axial Flux (PMAF) Machines: Comparison of Permanent Radial and Axial Flux Machines, Construction of PMAF Machines, Armature Windings, torque and EMF Equations of PMAF, Phasor Diagram, Output Equation, Pulsating Torque And its Minimisation, Control and Applications of PMAF. ■</td><td>08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding.</td><td></td></tr>
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B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – V

15EE554 SPECIAL ELECTRICAL MACHINES (Professional Elective) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Explain the performance and control of stepper motors, and their applications.
- Explain theory of operation and control of switched reluctance motor and permanent magnet brushless D.C. motors.
- Explain theory of operation and control of permanent magnet synchronous motors and synchronous reluctance motor.
- Explain operation of single phase special machines and servo motors.
- Explain operation of linear electrical machine and permanent magnet axial flux machines. ■

Graduate Attributes (As per NBA):

Engineering Knowledge, Problem analysis.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.

Textbook

1	Special Electrical Machines	E.G. Janardanan	PHI	1st Edition 2014.

Reference Books

1	Special Electrical Machines	K Venkataratham	University Press	2009
2	Brushless Permanent Magnet and Reluctance Motor Drives	T J E Miller	Clerendon Press, Oxford	1989
3	Permanent Magnet and Brushless DC Motors	Kenjo T and Nagamori S	Clerendon Press, Oxford	1985
4	Stepping Motors and their Microprocessor Control	KenjoT	Clerendon Press Oxford	1984
5	Switched Reluctance Motor Drives Modeling, Simulation Design and Applications	Krishan R	CRC	2001

<table>
<tr><td colspan="4">B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – V</td></tr>
<tr><td colspan="4">ELECTRONIC COMMUNICATION SYSTEMS(Open Elective)</td></tr>
<tr><td>Subject Code</td><td>15EE561</td><td>IA Marks</td><td>20</td></tr>
<tr><td>Number of Lecture Hours/Week</td><td>03</td><td>Exam Hours</td><td>03</td></tr>
<tr><td>Total Number of Lecture Hours</td><td>40</td><td>Exam Marks</td><td>80</td></tr>
<tr><td colspan="4">Credits - 03</td></tr>
</table>

Course objectives:

- To explain elements of communication system, noise and its effects.
- To describe the theory of amplitude, angle, pulse and digital modulation techniques
- To explain principles of radio communication, transmitters and receivers
- To explain basics of Television Broadcasting
- To explain basic principles of radar systems.
- To discuss multiplexing used in broadband communications.
- To explain the basic routing process used for long-distance telephony
- To explain fiber optic technology used for communication and its components and systems and their installation.
- To discuss basics of information theory, coding and data communication.

<table>
<tr><td colspan="2">Module-1</td><td>Teaching Hours</td></tr>
<tr><td colspan="2">Introduction to Communication: Elements of a Communication System, Need for Modulation, Electromagnetic Spectrum and Typical Applications, Terminologies in Communication Systems, Basics of Signal Representation and Analysis.
Noise: External Noise, internal Noise, Noise Calculations, Noise Figure, Noise Temperature.
Amplitude Modulation Techniques: Elements of Analog Communication, Theory of Amplitude Modulation Techniques, Generation of Amplitude Modulated Signals.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.</td></tr>
<tr><td colspan="3">Module-2</td></tr>
<tr><td colspan="2">Angle Modulation Techniques: Theory of Angle Modulation Techniques, Practical Issues in Frequency Modulation, Generation of Frequency Modulation.
Pulse Modulation Techniques: Introduction, Pulse Analog Modulation Techniques, Pulse Digital Modulation Techniques.
Digital Modulation Techniques: Introduction, Basic Digital Modulation Schemes, M-ary Digital Modulation Techniques.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.</td></tr>
<tr><td colspan="3">Module-3</td></tr>
<tr><td colspan="2">Radio Transmitters and Receivers: Introduction lo Radio Communication, Radio Transmitters, Receiver Types, AM Receivers, FM Receivers, Single- and Independent-Sideband Receivers.
Television Broadcasting: Requirements and Standards, Black-and-White Transmission, Black-and-White Reception, Colour Transmission and Reception.■</td><td>08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.</td><td></td></tr>
<tr><td colspan="3">Module-4</td></tr>
<tr><td colspan="2">Radar Systems: Basic Principles, Pulsed Systems, Other Radar Systems.
Broadband Communication Systems: Multiplexing, Short-and Medium-Haul Systems, Long-Haul Systems, Elements of Long-Distance Telephony.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing</td></tr>
<tr><td colspan="3"></td></tr>
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B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER – V		
15EE561 ELECTRONIC COMMUNICATION SYSTEMS(Open Elective) (continued)		
Module-5		**Teaching Hours**
Introduction to Fiber Optic Technology: History of Fiber Optics, Need of Optical Fibers, Introduction to Light, The Optical Fiber and Fiber Cables, Fiber Optic Components and Systems, Installation, Testing, and Repair. **Information Theory, Coding and Data Communication:** Information Theory, Digital Codes, Error Detection and Correction, Fundamentals of Data Communication System, Data Sets and Interconnection Requirements, Network and Control Considerations.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing	

Course outcomes:

At the end of the course the student will be able to:

- Understand communication systems and its terminologies.
- Explain noise, computation of noise level in communication systems.
- Describe the theory of amplitude, angle, pulse and digital modulation techniques
- Explain principles of radio communication, transmitters and receivers
- Show understanding of the basic TV system and process transmission and reception
- Explain basic principles of radar systems and multiplexing broadband communication systems.
- Show understanding of fiber optic technology.
- Show understanding of information theory, coding and data communication

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations, Life-long Learning.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Electronic Communication Systems	George Kennedy	McGraw Hill	5th Edition, 2011

Reference Books

1	Electronic Communications Systems: Fundamentals Through Advanced	Wayne Tomasi	Pearson	5th Edition, 2009
2	Communication Systems	V. Chandrasekar	Oxford	1st Edition, 2012
3	Communication Systems	P Ramakrishna Rao	McGraw Hill	1st Edition, 2013

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - V			
PROGRAMMABLE LOGIC CONTROLLERS (Open Elective)			
Subject Code	15EE562	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To explain advantages and disadvantages, main parts and their functions, basic sequence of operation of PLC.
- To describe the hardware components: I/O modules, CPU, memory devices, other support devices and the functions of PLC memory map.
- To describe program scan sequence, the communication of information to the PLC using different languages, internal relay instruction.
- To explain identification of common operating modes found in PLCs, writing and entering the ladder logic programs.
- To define the functions of Relays, Contactors, Motor Starters, Switches, Sensors, Output Control Devices, Seal-In Circuits and Latching Relays.
- To explain conversion of relay schematics into PLC ladder logic programs and writing PLC programs directly from narrative descriptions.
- To explain the functions of PLC counter instructions, applying combinations of counters and timers to control systems.
- To describe the function of selectable timed interrupt and fault routine files and use of temporary end instruction.
- To explain the execution of data transfer instructions, interruption of data transfer and data compare instructions.
- To explain the basic operation of PLC closed-loop control system, various forms of mechanical sequencers and their operations.
- To describe the operation of bit and word shift registers and develop programs that use shift registers.
- To discuss the operation of various processes, structures of control systems and the method of communication between different industrial processes. ■

Module-1		**Teaching Hours**
Programmable Logic Controllers: Introduction, Parts of a PLC, Principles of Operation, Modifying the Operation, PLCs versus Computers, PLC Size and Application. **PLC Hardware Components:** The I/O Section, Discrete I/O Modules, Analog I/O Modules, Special I/O Modules, I/O Specifications, The Central Processing Unit (CPU), Memory Design, Memory Types, Programming Terminal Devices, Recording and Retrieving Data, Human Machine Interfaces (HMIs). **Basics of PLC Programming:** Processor Memory Organization, Program Scan, PLC Programming Languages, Relay-Type Instructions, Instruction Addressing, Branch Instructions, Internal Relay Instructions, Programming Examine If Closed and Examine If Open Instructions, Entering the Ladder Diagram, Modes of Operation■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding,	
Module-2		
Developing Fundamental PLC Wiring Diagrams and Ladder Logic Programs: Electromagnetic Control Relays, Contactors, Motor Starters, Manually Operated Switches, Mechanically Operated Switches, Sensors, Output Control Devices, Seal-In Circuits, Latching Relays, Converting Relay Schematics into PLC Ladder Programs, Writing a Ladder Logic Program Directly from a Narrative Description. **Programming Timers:** Mechanical Timing Relays, Timer Instructions, On-Delay Timer Instruction, Off-Delay Timer Instruction, Retentive Timer, Cascading Timers.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding,.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - V		
15EE562 PROGRAMMABLE LOGIC CONTROLLERS (Open Elective) (continued)		
Module-3		**Teaching Hours**
Programming Counters: Counter Instructions, Up-Counter, Down-Counter, Cascading Counters, Incremental Encoder-Counter Applications, Combining Counter and Timer Functions. **Program Control Instructions:** Master Control Reset Instruction, Jump Instruction, Subroutine Functions, Immediate Input and Immediate Output Instructions, Forcing External I/O Addresses, Safety Circuitry, Selectable Timed Interrupt, Fault Routine, Temporary End Instruction, Suspend Instruction. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding,.	
Module-4		
Data Manipulation Instructions: Data Manipulation, Data Transfer Operations, Data Compare Instructions, Data Manipulation Programs, Numerical Data I/O Interfaces, Closed-Loop Control. **Math Instructions:** Math Instructions, Addition Instruction, Subtraction Instruction, Multiplication Instruction, Division Instruction, Other Word-Level Math Instructions, File Arithmetic Operations. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Sequencer and Shift Register Instructions: Mechanical Sequencers, Sequencer Instructions, Sequencer Programs, Bit Shift Registers, Word Shift Operations. **Process Control, Network Systems, and SCADA:** Types of Processes, Structure of Control Systems, On/Off Control, PID Control, Motion Control, Data Communications, Supervisory Control and Data Acquisition (SCADA). ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss history of PLC, its sequence of operation, advantages and disadvantages, main parts and their functions.
- Describe the hardware components of PLC: I/O modules, CPU, memory devices, other support devices, operating modes and PLC programming.
- Describe field devices Relays, Contactors, Motor Starters, Switches, Sensors, Output Control Devices, Seal-In Circuits, and Latching Relays commonly used with I/O module.
- Convert relay schematics and narrative descriptions into PLC ladder logic programs
- Analyze PLC timer and counter ladder logic programs
- Describe the operation of different program control instructions
- Discuss the execution of data transfer instructions, data compare instructions and the basic operation of PLC closed-loop control system.
- Describe the operation of mechanical sequencers, bit and word shift registers, processes and structure of control systems and communication between the processes.■

Graduate Attributes (As per NBA)

Engineering Knowledge

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.

Students will have to answer 5 full questions, selecting one full question from each module.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - V				
15EE562 PROGRAMMABLE LOGIC CONTROLLERS (Open Elective) (continued)				
Textbook				
1	Programmable Logic Controllers	Frank D Petruzella	McGraw Hill,	4th Edition, 2011
Reference Book				
1	Programmable Logic Controllers an Engineer's Guide,	E A Parr	Newnes	3rd Edition, 2013
2	Introduction Programmable Logic Controllers	Gary Dunning	Cengage	3rd Edition, 2006

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - V

RENEWABLE ENERGY RESOURCES(Open Elective)

Subject Code	15EE563	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80

Credits - 03

Course objectives:

- To discuss causes of energy scarcity and its solution, energy resources and availability of renewable energy.
- To explain sun – earth geometric relationship, Earth – Sun Angles and their Relationships
- To discuss about solar energy reaching the Earth's surface and solar thermal energy applications.
- To discuss types of solar collectors, their configurations and their applications
- To explain the components of a solar cell system, equivalent circuit of a solar cell, its characteristics and applications.
- To discus benefits of hydrogen energy, production of hydrogen energy, storage its advantages and disadvantages.
- To discuss wind turbines, wind resources, site selection for wind turbine
- To discuss geothermal systems, their classification and geothermal based electric power generation
- To discuss waste recovery management systems, advantages and disadvantages
- To discuss biomass production, types of biomass gasifiers, properties of producer gas.
- To discuss biogas, its composition, production, benefits.
- To discuss tidal energy resources, energy availability, power generation.
- To explain motion in the sea wave, power associated with sea wave and energy availability and the devices for harnessing wave energy.
- To discuss principles of ocean thermal energy conversion and production of electricity. ■

Module-1		Teaching Hours
Introduction: Causes of Energy Scarcity, Solution to Energy Scarcity, Factors Affecting Energy Resource Development, Energy Resources and Classification, Renewable Energy – Worldwide Renewable Energy Availability, Renewable Energy in India. **Energy from Sun:** Sun- earth Geometric Relationship, Layer of the Sun, Earth – Sun Angles and their Relationships, Solar Energy Reaching the Earth's Surface, Solar Thermal Energy Applications. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Solar Thermal Energy Collectors: Types of Solar Collectors, Configurations of Certain Practical Solar Thermal Collectors, Material Aspects ofSolar Collectors, Concentrating Collectors, Parabolic Dish – Stirling Engine System, Working of Stirling or Brayton Heat Engine, Solar Collector Systems into Building Services, Solar Water Heating Systems, Passive Solar Water Heating Systems, Applications of Solar Water Heating Systems, Active Solar Space Cooling, Solar Air Heating, Solar Dryers, Crop Drying, Space Cooing, Solar Cookers, Solar pond. **Solar Cells:** Components of Solar Cell System, Elements of Silicon Solar Cell, Solar Cell materials, Practical Solar Cells, I – V Characteristics of Solar Cells, Efficiency of Solar Cells, Photovoltaic Panels, Applications of Solar Cell Systems. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Hydrogen Energy: Benefits of Hydrogen Energy, Hydrogen Production Technologies, Hydrogen Energy Storage, Use of Hydrogen Energy, Advantages and Disadvantages of Hydrogen Energy, Problems Associated with Hydrogen Energy. **Wind Energy:** Windmills, Wind Turbines, Wind Resources, Wind Turbine Site Selection. **Geothermal Energy:** Geothermal Systems, Classifications, Geothermal Resource Utilization, Resource Exploration, Geothermal Based Electric Power Generation, Associated Problems, environmental Effects.		**08**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - V		
15EE563 RENEWABLE ENERGY RESOURCES(Open Elective) (continued)		
Module-3 (continued)		**Teaching Hours**
Solid waste and Agricultural Refuse: Waste is Wealth, Key Issues, Waste Recovery Management Scheme, Advantages and Disadvantages of Waste Recycling, Sources and Types of Waste, Recycling of Plastics. ■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Biomass Energy: Biomass Production, Energy Plantation,Biomass Gasification, Theory of Gasification, Gasifier and Their Classifications, Chemistry of Reaction Process in Gasification, Updraft, Downdraft and Cross-draft Gasifiers, Fluidized Bed Gasification, Use of Biomass Gasifier, Gasifier Biomass Feed Characteristics, Applications of Biomass Gasifier, Cooling and Cleaning of Gasifiers. **Biogas Energy:** Introduction, Biogas and its Composition, Anaerobic Digestion, Biogas Production, Benefits of Biogas, Factors Affecting the Selection of a Particular Model of a Biogas Plant, Biogas Plant Feeds and their Characteristics. **Tidal Energy:**Introduction, Tidal Energy Resource, Tidal Energy Availability, Tidal Power Generation in India, Leading Country in Tidal Power Plant Installation, Energy Availability in Tides, Tidal Power Basin, Turbines for Tidal Power, Advantages and Disadvantages of Tidal Power, Problems Faced in Exploiting Tidal Energy.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
Sea Wave Energy: Introduction, Motion in the sea Waves, Power Associated with Sea Waves, Wave Energy Availability, Devices for Harnessing Wave Energy, Advantages and Disadvantages of Wave Power. **Ocean Thermal Energy:**Introduction,Principles of Ocean Thermal Energy Conversion (OTEC), Ocean Thermal Energy Conversion plants, Basic Rankine Cycle and its Working, Closed Cycle, Open Cycle and Hybrid Cycle, Carnot Cycle, Application of OTEC in Addition to Produce Electricity, Advantages, Disadvantages and Benefits of OTEC. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss causes of energy scarcity and its solution, energy resources and availability of renewable energy.
- Discuss energy from sun, energy reaching the Earth's surface and solar thermal energy applications.
- Discuss types of solar collectors, their configurations, solar cell system, its characteristics and their applications.
- Discus generation of energy from hydrogen, wind, geothermal system, solid waste and agriculture refuse.
- Discuss production of energy from biomass, biogas.
- Discuss tidal energy resources, energy availability and power generation.
- Discuss power generation sea wave energy and ocean thermal energy. ■

Graduate Attributes (As per NBA)

Engineering Knowledge,Problem Analysis,Modern tool usage,Ethics.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) **CHOICE BASED CREDIT SYSTEM (CBCS)** **SEMESTER - V**				
15EE563 RENEWABLE ENERGY RESOURCES(Open Elective) (continued)				
Textbook				
1	Nonconventional Energy Resources	ShobhNath Singh	Pearson	1^{st} Edition, 2015
Reference Books				
1	Nonconventional Energy Resources	B.H. Khan	McGraw Hill	3^{rd} Edition,
2	Renewable Energy; Power for a sustainable Future	Godfrey Boyle	Oxford	3^{rd} Edition, 2012
3	Renewable Energy Sources: Their Impact on global Warming and Pollution	TasneemAbbasi S.A. Abbasi	PHI	1^{st} Edition, 2011

<table>
<tr><td colspan="5" align="center">B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -V</td></tr>
<tr><td colspan="5" align="center">BUSINESS COMMUNICATION (Open Elective)</td></tr>
<tr><td>Subject Code</td><td>15EE564</td><td>IA Marks</td><td colspan="2">20</td></tr>
<tr><td>Number of Lecture Hours/Week</td><td>03</td><td>Exam Hours</td><td colspan="2">03</td></tr>
<tr><td>Total Number of Lecture Hours</td><td>40</td><td>Exam Marks</td><td colspan="2">80</td></tr>
<tr><td colspan="5" align="center">Credits - 03</td></tr>
<tr><td colspan="5">Course objectives:
• To discuss analysing audiences, and choose the most effective structure and style for delivering strategically sound written and spoken messages.
• To discuss how to organize the talk, handling audience response.
• To discuss how to communicate with managers, co-workers, customers and suppliers.
• To discuss how engineers can use written and oral skills, computer, graphics and other engineering tools to communicate with other engineers and management.■</td></tr>
<tr><td colspan="4">Module-1</td><td>Teaching Hours</td></tr>
<tr><td colspan="4">Analyse Communication Purpose and Audience: How to Learn, How Engineers Are Persuaded, Speak or Write: Select the Right Communication Channel, Consider Your Communication Purpose and Audience.
Projecting the Image of the Engineering Profession: Overcome Anxiety, Primary Impact: Nonverbal Body Language, Secondary Impact: Control Vocal Quality, Volume, And Pace, Optimize Presentation Environment.
Presentation Aids: Engineering: The Real da Vinci Code, Speaking Visually—Guidelines for Using Presentation Aids, Choosing among Options, Creating Visuals with Impact, Delivering with Visuals. ■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td colspan="3">L_1 – Remembering, L_2 – Understanding, L_3 – Applying.</td></tr>
<tr><td colspan="5">Module-2</td></tr>
<tr><td colspan="4">Organize Your Talk: Planning Your Talk, Conducting an Audience Analysis: 39Questions, Organizing Your Talking Seven Easy Stages, Getting Attention and Keeping Interest, Five Minutes Early – Time Management for Your Presentation, Delivering Your Introduction, Presenting Your Conclusion.
Handling Audience Response: Create the Environment, Handle with C.A.R.E, Deal with Hostile Questions, Deal with Other Types of Questions, Control the Q&A Session, Thinking on Your Feet.
Organizing for Emphasis: Make our Bottom Line the Top Line, Purpose Statement and Blueprints, Open Long Reports with a Summary, Use More Topic Sentences, Develop Headings, Structure Vertical Lists. ■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td colspan="3">L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.</td></tr>
<tr><td colspan="5">Module-3</td></tr>
<tr><td colspan="4">Write As If Talking to Your Engineering Associates: Use Personal Pronouns, Relyon Everyday Words, Use Short Spoken Transitions, Keep Sentences Short, Reach Out to Your Engineering Readers by Asking Questions, 5Whys-ATechnique for Engineering Problem Solving.
Trim Your Expressions: Introduction, Prune Wordy Expressions, Use Strong Verbs, Cut Doublings and Noun Strings, Eliminate Unnecessary Determiners and Modifiers, Change Phrases into Single Words, Change Unnecessary Clauses into Phrases or Single Words, Avoid Over using "Itis" and "Thereis", Eight Steps for Lean Writing.
Write Actively—Engineering is about Actions: Active Voice:"Albert Einstein Wrote the Theory of Relativity", How to Recognize the Passive Voice, How to Write Actively – Use Three Cures, Write Passively for Good Reasons Only, Theory of Completed Staff Work. ■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td colspan="3">L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.</td></tr>
<tr><td colspan="5">Module-4</td></tr>
<tr><td colspan="4">Every day Engineering Communications -E-Mails, Phone Calls, and Memos: Effective E-mail Writing: Seven Things to Remember, How to Be Productive on the Phone, "Memos Solve Problems".</td><td>08</td></tr>
<tr><td colspan="5"></td></tr>
</table>

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -V		
15EE564 BUSINESS COMMUNICATION (Open Elective) (continued)		
Module-4 (continued)		**Teaching Hours**
Visuals for Engineering Presentation - Engineers Think in Pictures: Optimize Slide Layout, Display Engineering Data Effectively, How to Develop Effective Graphics. **Write Winning Grant Proposals:** Know Your Audience, Understand Your Goal and Marketing Strategy, Select the Correct Writing Style, Organize Your Proposal around the FourPs, A Brief Checklist before Submitting Your Proposal. ■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
How to Effectively Prepare Engineering Reports: Writing an Effective Progress Report, Develop Informative Design Reports. **Listening Interactive Communication about Engineering Risk:** Listening – A Forgotten Risk Communication Skill Listening – Harder Than Speaking and Writing, How to Listen to Voice of Customers about Risk, Listen Attentively: Understanding What Drives Perceived Risk, Thirteen Questions about Risk Communication. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Apply business communication strategies and principles to prepare effective communication for domestic and international business situations.
- Utilize analytical and problem solving skills appropriate to business communication.
- Participate in team activities that lead to the development of collaborative work skills.
- Select appropriate organizational formats and channels used in developing and presenting business messages.
- Compose and revise accurate business documents using computer technology.
- Communicate via electronic mail, Internet, and other technologies.
- Deliver an effective oral business presentation. ■

Graduate Attributes (As per NBA)

Engineering Knowledge

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module. ■

Text Book

1	What Every Engineer Should Know AboutBusinessCommunication	John X. Wang	CRC	2008

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -V			
MICROCONTROLLER LABORATORY - 1			
Subject Code	15EEL57	IA Marks	20
Number of Practical Hours/Week	03	Exam Hours	03
Total Number of Practical Hours	42	Exam Marks	80
Credits - 02			

Course objectives:

- To explain writing assembly language programs for data transfer, arithmetic, Boolean and logical instructions.
- To explain writing assembly language programs for code conversions.
- To explain writing assembly language programs using subroutines for generation of delays, counters, configuration of SFRs for serial communication and timers.
- To perform interfacing of stepper motor and dc motor for controlling the speed.
- To explain generation of different waveforms using DAC interface. ■

Sl. NO	Experiments
Note: For the experiments 1 to 6, 8051 assembly programming is to be used.	
1	Data transfer – Program for block data movement, sorting, exchanging, finding largest element in an array.
2	Arithmetic instructions: Addition, subtraction, multiplication and division. Square and cube operations for 16 bit numbers.
3	Counters
4	Boolean and logical instructions (bit manipulation).
5	Conditional call and return instructions.
6	Code conversion programs – BCD to ASCII, ASCII to BCD, ASCII to decimal, Decimal to ASCII, Hexa decimal to and Decimal to Hexa.
7	Programs to generate delay, Programs using serial port and on-chip timer/counters.
Note: Single chip solution for interfacing 8051 is to be with C Programs for the following experiments.	
8	Stepper motor interface.
9	DC motor interface for direction and speed control using PWM.
10	Alphanumerical LCD panel interface.
11	Generate different waveforms: Sine, Square, Triangular, Ramp using DAC interface.
12	External ADC and Temperature control interface.
13	Elevator interface.

Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating.

Course outcomes:

At the end of the course the student will be able to:

- Write assembly language programs for data transfer, arithmetic, Boolean and logical instructions.
- Write ALP for code conversions.
- Write ALP using subroutines for generation of delays, counters, configuration of SFRs for serial communication and timers.
- Perform interfacing of stepper motor and dc motor for controlling the speed.
- Generate different waveforms using DAC interface.
- Work with a small team to carryout experiments using microcontroller concepts and prepare reports that present lab work. ■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Individual and Team work, Modern tool usage, Communication.

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -V
15EEL57 MICROCONTROLLER LABORATORY – 1(continued)
Conduct of Practical Examination: 1. All laboratory experiments are to be included for practical examination. 2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners. 3. Students can pick one experiment from the questions lot prepared by the examiners. 4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.■
Learning beyond the syllabus: To acquire a wide variety of skills and to develop society friendly applications mini projects can be practiced by referring to “Microcontroller Based Projects” Second Edition, An EFY (Electronics For You) Enterprise Pvt Ltd, 2013.

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - V

POWER ELECTRONICS LABORATORY

Subject Code	15EEL58	IA Marks	20
Number of Practical Hours/Week	03	Exam Hours	03
Total Number of Practical Hours	42	Exam Marks	80

Credits - 02

Course objectives:

- To conduct experiments on semiconductor devices to obtain their static characteristics.
- To study different methods of triggering the SCR
- To study the performance of single phase controlled full wave rectifier and AC voltage controller with R and RL loads.
- To control the speed of a dc motor, universal motor and stepper motors.
- To study single phase full bridge inverter connected to resistive load.
- To study commutation of SCR. ■

Sl. No	Experiments
1	Static Characteristics of SCR.
2	Static Characteristics of MOSFET and IGBT.
3	Characteristic of TRIAC.
4	SCR turn on circuit using synchronized UJT relaxation oscillator.
5	SCR digital triggering circuit for a single phase controlled rectifier and ac voltage regulator.
6	Single phase controlled full wave rectifier with R and R –L loads.
7	AC voltage controller using TRIAC and DIAC combination connected to R and RL loads.
8	Speed control of dc motor using single semi converter.
9	Speed control of stepper motor.
10	Speed control of universal motor using ac voltage regulator.
11	Speed control of a separately excited D.C. Motor using an IGBT or MOSFET chopper.
12	Design of Snubber circuit.
Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating

Course outcomes:

At the end of the course the student will be able to:

- Obtain static characteristics of semiconductor devices to discuss their performance.
- Trigger the SCR by different methods
- Verify the performance of single phase controlled full wave rectifier and AC voltage controller with R and RL loads.
- Control the speed of a dc motor, universal motor and stepper motors.
- Verify the performance of single phase full bridge inverter connected to resistive load.
- Perform commutation of SCR by different methods.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Individual and Team work, Communication.

Conduct of Practical Examination:

1. All laboratory experiments are to be included for practical examination.
2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners.
3. Students can pick one experiment from the questions lot prepared by the examiners.
4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.■

****** END ******

VI SEMESTER DETAILED SYLLABUS

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VI			
CONTROL SYSTEMS (Core Subject)			
Subject Code	15EE61	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80
Credits - 04			

Course objectives:

- To define a control system
- To explain the necessity of feedback and types of feedback control systems.
- To introduce the concept of transfer function and its application to the modeling of linear systems.
- To demonstrate mathematical modeling of control systems.
- To obtain transfer function of systems through block diagram manipulation and reduction
- To use Mason's gain formula for finding transfer function of a system
- To discuss transient and steady state time response of a simple control system.
- To discuss the stability of linear time invariant systems and Routh - Hurwitz criterion
- To investigate the trajectories of the roots of the characteristic equation when a system parameter is varied.
- To conduct the control system analysis in the frequency domain.
- To analyze stability of a control system using Nyquist plot.
- To discuss stability analysis using Bode plots.
- To determine the controller or compensator configuration and parameter values relative to how it is connected to the controlled process given the design specifications.■

Module-1		Teaching Hours
Introduction to control systems: Introduction, classification of control systems. **Mathematical models of physical systems:** Modelling of mechanical system elements, electrical systems, Analogous systems, Transfer function, Single input single output systems, Procedure for deriving transfer functions, servomotors, synchros, gear trains.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-2		
Block diagram: Block diagram of a closed loop system, procedure for drawing block diagram and block diagram reduction to find transfer function. **Signal flow graphs:** Construction of signal flow graphs, basic properties of signal flow graph, signal flow graph algebra, construction of signal flow graph for control systems.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Time Domain Analysis: Standard test signals, time response of first order systems, time response of second order systems, steady state errors and error constants, types of control systems. **Routh Stability criterion:** BIBO stability, Necessary conditions for stability, Routh stability criterion, difficulties in formulation of Routh table, application of Routh stability criterion to linear feedback systems, relative stability analysis. ■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	
Module-4		
Root locus technique: Introduction, root locus concepts, construction of root loci, rules for the construction of root locus. **Frequency Response analysis:** Co-relation between time and frequency response – 2nd order systems only. **Bode plots:** Basic factors G(iw)/H(jw), General procedure for constructing bode plots, computation of gain margin and phase margin. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VI		
15EE61 CONTROL SYSTEMS (Core Subject) (continued)		
Module-5		**Teaching Hours**
Nyquist plot: Principle of argument, Nyquist stability criterion, assessment of relative stability using Nyquist criterion. **Design of Control Systems:** Introduction, Design with the PD Controller, Design with the PI Controller, Design with the PID Controller, Design with Phase-Lead Controller, Design with Phase - Lag Controller, Design with Lead-Lag Controller.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss the effects of feedback and types of feedback control systems.
- Evaluate the transfer function of a linear time invariant system.
- Evaluate the stability of linear time invariant systems.
- Apply block diagram manipulation and signal flow graph methods to obtain transfer function of systems.
- Demonstrate the knowledge of mathematical modeling of control systems and components
- Determine transient and steady state time response of a simple control system.
- Investigate the performance of a given system in time and frequency domains.
- Discuss stability analysis using Root locus, Bode plots and Nyquist plots.
- Determine the controller or compensator configuration and parameter values relative to how it is connec ted to the controlled process given the design specifications.

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem analysis, Modern Tool Usage, Life-long Learning.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks. Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module. ■

Textbook

1	Control Systems	Anand Kumar	PHI	2nd Edition, 2014

ReferenceBooks

1	Automatic Control Systems	FaridGolnaraghi, Benjamin C. Kuo	Wiley	9th Edition, 2010
2	Control Systems Engineering	Norman S. Nise	Wiley	4th Edition, 2004
3	Modern Control Systems	Richard C Dorf et al	Pearson	11th Edition, 2008
4	Control Systems, Principles and Design	M.Gopal	McGaw Hill	4th Edition, 2012
5	Control Systems Engineering	S. Salivahanan et al	Pearson	1st Edition, 2015

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI

POWER SYSTEM ANALYSIS – 1 (Core Subject)

Subject Code	15EE62	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To introduce the per unit system and explain its advantages and computation.
- To explain the concept of one line diagram and its implementation in problems.
- To explain the necessity and conduction of short circuit analysis.
- To explain analysis of three phase symmetrical faults on synchronous machine and simple power systems.
- To discuss selection of circuit breaker.
- To explain symmetrical components, their advantages and the calculation of symmetrical components of voltages and currents in un-balanced three phase circuits.
- To explain the concept of sequence impedance and its analysis in three phase unbalanced circuits.
- To explain the concept of sequence networks and sequence impedances of an unloaded synchronous generator, transformers and transmission lines.
- To explain the analysis of synchronous machine and simple power systems for different unsymmetrical faults using symmetrical components.
- To discuss the dynamics of synchronous machine and derive the power angle equation for a synchronous machine
- Discuss stability and types of stability for a power system and the equal area criterion for the evaluation of stability of a simple system.■

Module-1		Teaching Hours
Representation of Power System Components: Introduction, Single-phase Representation of Balanced Three Phase Networks, One-Line Diagram and Impedance or Reactance Diagram, Per Unit (PU) System, Steady State Model of Synchronous Machine, Power Transformer, Transmission of electrical Power, Representation of Loads. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-2		
Symmetrical Fault Analysis: Introduction, Transient on a Transmission Line, Short Circuit of a Synchronous Machine(On No Load), Short Circuit of a Loaded Synchronous Machine, Selection of Circuit Breakers.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Symmetrical Components: Introduction, Symmetrical Component Transformation, Phase Shift in Star-Delta Transformers, Sequence Impedances of Transmission Lines, Sequence Impedances and Sequence Network of Power System, Sequence Impedances and Networks of Synchronous Machine, Sequence Impedances of Transmission Lines, Sequence Impedances and Networks of Transformers, Construction of Sequence Networks of a Power System, Measurement of sequence Impedance of Synchronous Generator. ■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	
Module-4		
Unsymmetrical Fault Analysis: Introduction, Symmetrical Component Analysis of Unsymmetrical Faults, Single Line-To-Ground (LG) Fault, Line-To-Line (LL) Fault, Double Line-To-Ground (LLG) Fault, Open Conductor Faults.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI

15EE62 POWER SYSTEM ANALYSIS – 1 (Core Subject) (continued)

Module-5		**Teaching Hours**
Power System Stability: Introduction, Dynamics of a Synchronous Machine, Power Angle Equation Salient and Non – Salient pole Synchronous Machines, Simple Systems, Steady State Stability, Transient Stability, Equal Area Criterion, Factors Affecting Transient Stability. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Show understanding of per unit system, its advantages and computation.
- Show the concept of one line diagram and its implementation in problems
- Perform short circuit analysis on a synchronous machine and simple power system to select a circuit breaker for the system.
- Evaluate symmetrical components of voltages and currents in un-balanced three phase circuits.
- Explain the concept of sequence impedance and sequence networks of power system components and power system.
- Analyze three phase synchronous machine and simple power systems for different unsymmetrical faults using symmetrical components.
- Discuss the dynamics of synchronous machine, stability and types of stability.
- Discuss equal area criterion for the evaluation of stability of a simple system under different fault conditions. ■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem analysis, The Engineer and Society, Ethics

Question paper pattern:

- The question paper will have ten full questions carrying equal marks. Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module. ■

Textbook

1.	Modern Power System	D. P. Kothari	McGraw Hill	4th Edition, 2011

ReferenceBooks

1	Elements of Power System	William D. StevensonJr	McGraw Hill	4th Edition, 1982
2	Power System Analysis and Design	J.Duncan Glover et al	Cengage	4th Edition, 2008
3	Power System Analysis	Hadi Sadat	McGraw Hill	1st Edition, 2002

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI

DIGITAL SIGNAL PROCESSING (Core Subject)

Subject Code	15EE63	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To define Discrete Fourier transform and its properties.
- To evaluate DFT of various signals using properties of DFT.
- To explain different linear filtering techniques**.**
- To explain the evaluation of DFT and inverse DFT using fast and efficient algorithms
- To discuss impulse invariant transformation, bilinear transformation techniques and their properties.
- To design infinite impulse response Butterworth digital filters using impulse invariant and bilinear transformation techniques.
- To design infinite impulse response Chebyshev digital filters using impulse invariant and bilinear transformation techniques.
- To discuss direct, cascade, parallel and ladder methods of realizing a digital IIR filter.
- To discuss window functions used for the design of FIR filters.
- To discuss windowing technique of designing FIR filter.
- To discuss frequency sampling technique of designing FIR filter.
- To discuss direct, cascade and linear phase form of realizing a digital FIR filter. ■

Module-1		**Teaching Hours**
Discrete Fourier Transforms: Definitions, properties-linearity, shift, symmetry Properties- circular convolution – periodic convolution, use of tabular arrays, circular arrays, Stock ham's method, linear convolution – two finite duration sequence, one finite & one infinite duration, overlap add and save methods**.** ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering,L_2 – Understanding,L_3 – Applying,L_4 – Analysing. L_5 – Evaluating	
Module-2		
Fast Fourier Transforms Algorithms: Introduction, decimation in time algorithm, first decomposition, number of computations, continuation of decomposition, number of multiplications, computational efficiency, decimation in frequency algorithms, Inverse radix – 2 algorithms.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing. L_5– Evaluating	
Module-3		
Design of IIR Digital Filters: Introduction, impulse invariant transformation, bilinear transformations, All pole analog filters- Butterworth & Chebyshev filters, design of digital Butterworth filter by impulse invariant transformation and bilinear transformation, Frequency transformations. ■		**10**
Revised Bloom's Taxonomy Level	L1- Remembering, L2 – Understanding, L3 – Applying. L4 – Analysing. L5 – Evaluating	
Module-4		
Design of IIR Digital Filters (Continued): Design of digital Chebyshev –type 1filter by impulse invariant transformation and bilinear transformation, Frequency transformations. **Realization of IIR digital systems:** direct form, cascade form and parallel form, Ladder structures for equal degree polynomial.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5– Evaluating	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI

15EE63 DIGITAL SIGNAL PROCESSING (Core Subject) (continued)

Module-5		Teaching Hours
Design of FIR Digital Filters: Introduction, windowing, rectangular, modified rectangular. Hamming, Hanning, Blackman window, **design of FIR digital filters by use of windows, Design of FIR digital filters-**frequency sampling techniques. **Realization of FIR systems:** direct form, cascade form, linear phase form■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5– Evaluating	

Course outcomes:

At the end of the course the student will be able to:

- Compute the DFT of various signals using its properties and linear filtering of two sequences.
- Apply fast and efficient algorithms for computing DFT and inverse DFT of a given sequence
- Design infinite impulse response Butterworth digital filters using impulse invariant / bilinear transformation technique.
- Design infinite impulse response Chebyshev digital filters using impulse invariant or bilinear transformation technique.
- Realize a digital IIR filter by direct, cascade, parallel and ladder methods of realization.
- Discuss different window functions and frequency sampling method used for design of FIR filters.
- Design FIR filters by use of window function or by frequency sampling method.
- Realize a digital FIR filter by direct, cascade, and linear phase form. ■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem analysis, Design/ Development of Solutions, Modern Tool Usage.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks.Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module. ■

Textbook

1	Introduction to Digital Signal Processing	Jhonny R. Jhonson	Pearson	1st Edition, 2016

Reference Books

1.	Digital Signal Processing – Principles, Algorithms, and Applications	Jhon G. Proakis Dimitris G. Manolakis	Pearson	4th Edition, 2007.
2.	Digital Signal Processing	A.NagoorKani	McGraw Hill	2nd Edition, 2012
3	Digital Signal Processing	Shaila D. Apte	Wiley	2nd Edition, 2009
4	Digital Signal Processing	Ashok Amberdar	Cengage	1st Edition, 2007
5	Digital Signal Processing	Tarun Kumar Rawat	Oxford	1st Edition, 2015

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI

ELECTRICAL MACHINE DESIGN (Core Course)

Subject Code	15EE64	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To discuss design factors, limitations in design and modern trends in design and manufacturing of electrical machines.
- To discuss the properties of electrical, magnetic and insulating materials used in the design of electrical machines.
- To derive the output equation of DC machine, single phase, three phase transformers, induction motor and synchronous machines.
- To discuss the selection of specific loadings, for various machines.
- To discuss separation of main dimensions for different electrical machines
- To discuss design of field windings for DC machines and synchronous machines.
- To evaluate the performance parameters of transformer, induction motor.
- To design of cooling tubes for the transformer for a given temperature rise.
- To explain design of rotor of squirrel cage rotor and slip ring rotor.
- To define short circuit ratio and discuss its effect on machine performance.■

Module-1		Teaching Hours
Fundamental Aspects of Electrical Machine Design: Design of Machines, Design Factors, Limitations in design, Modern Trends in design, manufacturing Techniques. **Electrical Engineering Materials:** Desirabilities of Conducting Materials, Comparison of Aluminium and Copper wires. Ferromagnetic Materials: Soft Magnetic materials – Solid Core Materials, Electrical Sheet and Strip, Cold Rolled Grain Oriented Steel. Insulating Materials: Desirable Properties, Temperature Rise and Insulating Materials, Classification of Insulating materials based on Thermal Consideration.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_4 – Analysing.	
Module-2		
Design of DC Machines: Output Equation, Choice of Specific Loadings and Choice of Number of Poles, Main Dimensions of armature, Design of Armature Slot Dimensions, Commutator and Brushes. Estimation of Ampere Turns for the Magnetic Circuit. Dimensions of Yoke, Main Pole and Air Gap. Design of Shunt and Series Field Windings.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Design of Transformers: Output Equations of Single Phase and Three Phase Transformers, Choice of Specific Loadings, Expression for Volts/Turn, Determination of Main Dimensions of the Core, Estimation of Number of Turns and Conductor Cross Sectional area of Primary and Secondary Windings, No Load Current. Expression for the Leakage Reactance of core type transformer with concentric coils, and calculation of Voltage Regulation. Design of Tank and Cooling (Round and Rectangular) Tubes.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Design of Three Phase Induction Motors: Output Equation, Choice of Specific Loadings, Main Dimensions of Stator. Design of stator slots and Winding, Choice of Length Air Gap, Estimation of Number of Slots for Squirrel Cage Rotor. Design of Rotor Bars and End Ring. Design of Slip Ring rotor. Estimation of No Load Current and Leakage Reactance.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI

15EE64 ELECTRICAL MACHINE DESIGN (Core Course) (continued)

Module-5

Design of Three Phase Synchronous Machines: Output Equation, Choice of Specific Loadings, Short Circuit Ratio, Main Dimensions of Stator. Design of stator slots and Winding. Design of Salient and non- salient Pole Rotors. Magnetic Circuit and Field Winding.■		**10**
Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing. L_2 – Understanding, L_4 – Analysing.	

Course outcomes: At the end of the course the student will be able to:

- Discuss design factors, limitations, modern trends in design, manufacturing of electrical machines and properties of materials used in the electrical machines.
- Derive the output equations of transformer, DC machines and AC machines.
- Discuss selection of specific loadings and magnetic circuits of different electrical machines
- Design the field windings of DC machine and Synchronous machine.
- Design stator and rotor circuits of a DC and AC machines.
- Estimate the number of cooling tubes, no load current and leakage reactance of core type transformer.
- Discuss short circuit ratio and its effects on performance of synchronous machines.
- Design salient pole and non-salient pole alternators for given specifications. ■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Ethics
.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks.Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module. ■

Textbook

1	A course in Electrical Machine design	A.K.Sawhney	DhanpatRai	6th Edition, 2013

Reference Books

1	Performance and Design of Alternating Current Machines	M.G. Say	CBS Publisher	3rd Edition, 2002
2	Design Data Handbook	A. Sanmugasundaram Et al	New Age International	1st Edition, 2011

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VI			
COMPUTER AIDED ELECTRICAL DRAWING (Professional Elective)			
Subject Code	15EE651	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To discuss the terminology of DC and AC armature windings.
- To discuss design and procedure to draw armature winding diagrams for DC and AC machines.
- To discuss the substation equipment, their location in a substation and development of a layout for substation.
- To discuss different sectional views of transformers, DC machine, its parts and alternator and its parts.
- To explain development of sectional views of Transformers, DC machine and alternators using the design data, sketches.■

Suitable CAD software can be used for drawings

PART - A

Module-1		Teaching Hours
Winding Diagrams: (a) Developed Winding Diagrams of D.C. Machines: Simplex Double Layer Lap and Wave Windings. (b) Developed Winding Diagrams of A.C. Machines: (c)Integral and Fractional Slot Double Layer Three Phase Lap and Wave Windings. (d) Single Layer Windings – Un-Bifurcated 2 and 3 Tier Windings, Mush Windings, Bifurcated 3 Tier Windings. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Single Line Diagrams: Single Line Diagrams of Generating Stations and Substations Covering Incoming Circuits, Outgoing Circuits, Busbar Arrangements (Single, Sectionalised Single, Main and Transfer, Double Bus Double Breaker, Sectionalised Double Bus, One and a Half Circuit Breaker Arrangement, Ring Main),Power Transformers, Circuit Breakers, Isolators,Earthing Switches,Instrument Transformers, Surge or Lightning Arresters, Communication Devices (Power-Line Carrier) and Line Trap.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

PART - B

Module-3		
Electrical Machine Assembly Drawings Using Design Data, Sketches or Both: Transformers - Sectional Views Of Single And Three Phase Core And Shell Type Transformers. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Electrical Machine Assembly Drawings Using Design Data, Sketches or Both: D.C. Machine - Sectional Views of Yoke with Poles, Armature and Commutator dealt separately.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
Electrical Machine Assembly Drawings Using Design Data, Sketches or Both: Alternator – Sectional Views of Stator and Rotor dealt separately. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VI

15EE651 COMPUTER AIDED ELECTRICAL DRAWING (Professional Elective) (continued)

Course Outcomes: At the end of the course the student will be able to**:**

- Discuss the terminology and types of DC and AC armature windings.
- Develop armature winding diagram for DC and AC machines
- Develop a layout for substation using the standard symbols for substation equipment. .
- Draw sectional views of core and shell types transformers using the design data
- Draw sectional views of assembled DC machine or its parts using the design data or the sketches.
- Draw sectional views of assembled alternator or its parts using the design data or the sketches.■

Graduate Attributes (As per NBA)

Engineering Knowledge,Problem Analysis,Modern tool usage,Ethics.

Question paper pattern:

- The question paper will have two parts, PART – A and PART – B.
- Each part is for 40 marks.
- Part A is for Modules 1 and 2.
- Questions 1 and 2 of PART - A will be only on DC windings or only on AC windings. Students have to answer any one of them. The marks prescribed is 25.
- Question 3 of PART – A covering module 2 is compulsory. The marks prescribed is 15.
- Part B is for Modules 3, 4 and 5.
- Questions 4 and 5 will cover any two modules of modules 3, 4 and 5. Students have to answer any one of them. The marks prescribed is 40.■

Reference Books

1	A course in Electrical Machine design	A. K. Sawhney	DhanpatRai	6th Edition, 2013
2	Electrical Engineering Drawing	K. L. Narang	SatyaPrakashan	2014

<table>
<tr><td colspan="5" align="center">B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI</td></tr>
<tr><td colspan="5" align="center">ADVANCED POWER ELECTRONICS (Professional Elective)</td></tr>
<tr><td>Subject Code</td><td>15EE652</td><td>IA Marks</td><td colspan="2">20</td></tr>
<tr><td>Number of Lecture Hours/Week</td><td>03</td><td>Exam Hours</td><td colspan="2">03</td></tr>
<tr><td>Total Number of Lecture Hours</td><td>40</td><td>Exam Marks</td><td colspan="2">80</td></tr>
<tr><td colspan="5" align="center">Credits - 03</td></tr>
<tr><td colspan="5">Course objectives:
• To study switching mode regulators and Boost converters, Resonant Pulse Inverters and multilevel inverters
• To learn the techniques for design and analysis of dc dc converters, Resonant Pulse Inverters and multilevel inverters
• To explain the operation and frequency characteristics of resonant inverters and the techniques for zero-voltage and zero-current switching
• To study the performance parameters of resonant inverters
• To explain the techniques for analyzing and design of resonant inverters
To explain the operation and features of multilevel inverters, their advantages and disadvantages.
• To explain the control strategy to address capacitor voltage unbalancing.
• To discuss potential applications of multilevel inverters.
• To study the types and circuit topologies of power supplies and explain the operation and analysis of power supplies.
• To study the applications of power electronic devices.■</td></tr>
<tr><td colspan="4">Module-1</td><td>Teaching Hours</td></tr>
<tr><td colspan="4">DC–DC Converters: Switching-Mode Regulators, Comparison of Regulators, Multi-output Boost Converter, Diode Rectifier-Fed Boost Converter, Averaging Models of Converters, State–Space Analysis of Regulators, Design Considerations for Input Filter and Converters, Drive IC for Converters.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td colspan="3">L_1 – Remembering, L_2 – Understanding, L_4 – Analysing.</td></tr>
<tr><td colspan="5">Module-2</td></tr>
<tr><td colspan="4">Resonant Pulse Inverters: Introduction. Series Resonant Inverters, Frequency Response of Series Inverters, Parallel Resonant Inverters, Voltage Controlled Resonant Inverters, Class E Resonant Inverter, Class E Resonant Rectifier, Zero – Current Switching (ZCS) Resonant Converters, Zero Voltage Switching Resonant Converters (ZVS), Comparison between ZCS and ZVS Resonant Converters, Two Quadrant ZVS Resonant Converters, Resonant DC – Link Inverters.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td colspan="3">L_1 – Remembering, L_2 – Understanding, L_4 – Analysing.</td></tr>
<tr><td colspan="5">Module-3</td></tr>
<tr><td colspan="4">Multilevel Inverters: Introduction, Multilevel Concept, Types of Multilevel Inverters, Diode – Clamped Multilevel Inverter, Flying - Capacitors Multilevel Inverter. Cascaded Multilevel Inverter, Applications, Features of Multilevel Inverters, Comparison of Multilevel Converters.■</td><td>08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td colspan="3">L_1 – Remembering, L_2 – Understanding, L_4 – Analysing.</td><td></td></tr>
<tr><td colspan="5">Module-4</td></tr>
<tr><td colspan="4">Power Supplies: Introduction, DC Power Supplies, AC Power Supplies, Multistage Conversions, Control Circuits, Magnetic Design Considerations.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td colspan="3">L_1 – Remembering, L_2 – Understanding. L_4 – Analysing</td></tr>
<tr><td colspan="5"></td></tr>
</table>

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -VI		
15EE652 ADVANCED POWER ELECTRONICS (Professional Elective) (continued)		
Module-5		**Teaching Hours**
Residential and Industrial Applications: Introduction, Residential Applications, Industrial Applications. **Electrical Utility Applications:** Introduction, High Voltage DC Transmission, Static VAR Compensators, Interconnection of Renewable Energy Sources and Energy Storage systems to the Utility Grid, Active Filters.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding. L_4 – Analysing	

Course outcomes:

At the end of the course the student will be able to:

- Explain the types of switching – mode regulators, Resonant Pulse Inverters and multilevel inverters
- To discuss the techniques for design and analysis of dc dc converters, Resonant Pulse Inverters and multilevel inverters
- Evaluate the performance parameters of resonant inverters
  Explain the techniques for zero-voltage and zero-current switching of resonant pulse inverters
- Explain the control strategy to address capacitor voltage unbalancing in multilevel inverters.
- Discuss the types, topologies operation and analysis of power supplies.
- Discuss residential, Industrial and Electrical utility applications of power electronic devices. ■■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis Design/ Development of Solutions , Conduct investigations of complex problems, Ethics

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Power Electronics: Circuits Devices and Applications,	Mohammad H Rashid	Pearson	4th Edition, 2014
2	Power Electronics Converters, Applications and Design (For Module 5: Chapters 16 and 17)	Ned Mohan et al	Wiley	3rd Edition, 2014

Reference Books

1	Power Electronics	Daniel W Hart	McGraw Hill	1st Edition, 2011

<table>
<tr><td colspan="4">B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI</td></tr>
<tr><td colspan="4">ENERGY AUDIT AND DEMAND SIDE MANAGEMENT (Professional Elective)</td></tr>
<tr><td>Subject Code</td><td>15EE653</td><td>IA Marks</td><td>20</td></tr>
<tr><td>Number of Lecture Hours/Week</td><td>03</td><td>Exam Hours</td><td>03</td></tr>
<tr><td>Total Number of Lecture Hours</td><td>40</td><td>Exam Marks</td><td>80</td></tr>
<tr><td colspan="4">Credits - 03</td></tr>
<tr><td colspan="4">Course objectives:
• To explain the importance of energy audit, its types and energy audit methodology.
• To explain the parameters required for energy audit and the working of the instruments used in the measurement of the parameters.
• To explain the energy audit of different systems and equipment and buildings
• To explain electrical load management techniques, harmonics and their effects, electricity tariffs and power factor improvement.
• To explain the scope of demand side management, its concept and implementation issues and strategies.
• To discuss energy conservation ■</td></tr>
</table>

<table>
<tr><td colspan="2">Module-1</td><td>Teaching Hours</td></tr>
<tr><td colspan="2">Energy Scenarios: Energy Conservation, Energy Audit, Energy Scenarios, Energy Consumption, Energy Security, Energy Strategy, Clean Development Mechanism.
Types of Energy Audits and Energy-Audit Methodology: Definition of Energy Audit, Place of Audit, Energy – Audit Methodology, Financial Analysis, Sensitivity Analysis, Project Financing Options, Energy Monitoring and Training.
Survey Instrumentation: Electrical Measurement, Thermal Measurement, Light Measurement, Speed Measurement, Data Logger and Data – Acquisition System, Thermal Basis.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 - Remembering, L_2 - Understanding, L_3 - Applying, L_4 - Analysing.</td></tr>
<tr><td colspan="3">Module-2</td></tr>
<tr><td colspan="2">Energy Audit of Boilers: Classification of Boilers, Parts of Boiler, Efficiency of a Boiler, Role of excess Air in Boiler Efficiency, Energy Saving Methods.
Energy Audit of Furnaces: Parts of a Furnace, classification of Furnaces, Energy saving Measures in Furnaces, Furnace Efficiency.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 - Remembering, L_2 - Understanding, L_3 - Applying, L_4 - Analysing ,</td></tr>
<tr><td colspan="3">Module-3</td></tr>
<tr><td colspan="2">Energy Audit of HVAC Systems: Introduction to HVAC, Components of Air – Conditioning System, Types of Air – Conditioning Systems, Human Comfort Zone and Psychrometry, Vapour – Compression Refrigeration Cycle, Energy Use Indices, Impact of Refrigerants on Environment and Global Warming, Energy – Saving Measures in HVAC, Star Rating and Labelling by BEE.
Electrical-Load Management: Electrical Basics, Electrical Load Management, Variable- Frequency Drives, Harmonics and its Effects, Electricity Tariff, Power Factor, Transmission and Distribution Losses.■</td><td>08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 - Remembering, L_2 - Understanding, L_3 - Applying, L_4 - Analysing</td><td></td></tr>
<tr><td colspan="3">Module-4</td></tr>
<tr><td colspan="2">Energy Audit of Motors: Classification of Motors, Parameters related to Motors, Efficiency of a Motor, Energy Conservation in Motors, BEE Star Rating and Labelling. Energy Audit of Lighting Systems:Fundamentals of Lighting, Different Lighting Systems, Ballasts, Fixtures (Luminaries), Reflectors, Lenses and Louvres, Lighting Control Systems, Lighting System Audit, Energy Saving Opportunities. ■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 - Remembering, L_2 - Understanding, L_3 - Applying, L_4 - Analysing</td></tr>
<tr><td colspan="3"></td></tr>
</table>

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI

15EE653 ENERGY AUDIT AND DEMAND SIDE MANAGEMENT (Professional Elective)(continued)

Module-5		Teaching Hours
Energy Audit Applied to Buildings: Energy – Saving Measures in New Buildings, Water Audit, Method of Audit, General Energy – Savings Tips Applicable to New as well as Existing Buildings. **Demand side Management:** Scope of DSM, Evolution of DSM concept, DSM planning and Implementation, Load management as a DSM strategy, Applications of Load Control, End use energy conservation, Tariff options for DSM, customer acceptance, implementation issues, Implementation strategies, DSM and Environment. **Energy Conservation:** Motivation of energy conservation, Principles of Energy conservation, Energy conservation planning, Energy conservation in industries, EC in SSI, EC in electrical generation, transmission and distribution, EC in household and commercial sectors, EC in transport, EC in agriculture, EC legislation.■		**08**
Revised Bloom's Taxonomy Level	L_1 - Remembering, L_2 - Understanding, L_3 - Applying, L_4 - Analysing	

Course outcomes:

At the end of the course the student will be able to:

- Understand the need of energy audit and energy audit methodology.
- Explain audit parameters and working principles of measuring instruments used to measure the parameters.
- Conduct energy audit of boilers, furnaces, power plant, steam distribution system and compressed air systems.
- Conduct energy audit HVAC systems, motors, pumps, blowers and cooling towers.
- Explain load management techniques, effects of harmonics, electricity tariff, improvement of power factor and losses in transmission.
- Conduct energy audit of lighting systems and buildings.
- Show an understanding of demand side management and energy conservation.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Conduct investigations of complex Problems, Environment and sustainability, Ethics, Individual and Team work, Communication

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Handbook on Energy Audit	Sonal Desai	McGraw Hill	1st Edition, 2015
2.	Generation of Electrical Energy	B R Gupta	S. Chand	1stEdition, 1983

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER –VI			
SOLAR AND WIND ENERGY (Professional Elective)			
Subject Code	15EE654	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits – 03			

Course objectives:

- To discuss the importance of energy in human life, relationship among economy and environment with energy use.
- To discuss the increasing role of renewable energy, energy management, energy audit, energy efficiency, energy intensity.
- To discuss energy consumption status in India, energy saving potential and energy conservation efforts in India.
- To explain the concept of energy storage and the principles of energy storage devices.
- To discuss the characteristics and distribution of solar radiation, measurement of components of solar radiation and analysis of collected solar radiation data.
- To explain availability of solar radiation at a location and the effect of tilting the surface of collector with respect to horizontal surface.
- To describe the process of harnessing solar energy in the form of heat and working of solar collectors.
- To discuss applications of solar energy including heating and cooling.
- To discuss the operation of solar cell and the environmental effects on electrical characteristics of solar cell
- To discuss sizing and design of typical solar PV systems and their applications.
- To discuss basic Principles of Wind Energy Conversion and to compute the power available in the wind.
- To discuss forces on the Blades, Wind Energy Conversion, collection of Wind Data, energy estimation and site selection.
- To discuss classification of WEC Systems, its advantages and disadvantages of WECS, and Types of Wind Machines (Wind Energy Collectors).
- To evaluate the performance of Wind-machines, Generating Systems.
- To discuss energy storage, applications of Wind Energy and Environmental Aspects.■

Module-1		**Teaching Hours**
Fundamentals of Energy Science and Technology: Introduction, Energy, Economy and Social Development, Classification of Energy Sources, Importance of Non -conventional Energy Sources, Salient features of Non-conventional Energy Sources, World Energy Status, Energy Status in India. **Energy Conservation and Efficiency:** Introduction, Important Terms and Definitions, Important Aspects of Energy Conservation, Global Efforts, Achievements and Future Planning, Energy Conservation/Efficiency Scenario in India, Energy Audit, Energy Conservation Opportunities. **Energy Storage:** Introduction, Necessity of Energy Storage, Specifications of Energy Storage Devices. **Solar Energy-Basic Concepts:** Introduction, The Sun as Source of Energy, The Earth, Sun, Earth Radiation Spectrum, Extraterrestrial and Terrestrial Radiations, Spectral Power Distribution of Solar Radiation, Depletion of Solar Radiation.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Solar Energy-Basic Concepts (continued): Measurement of Solar Radiation, Solar Radiation Data, Solar Time, Solar Radiation Geometry, Solar Day Length, Extraterrestrial Radiation on Horizontal Surface, Empirical Equations for Estimating Terrestrial Solar Radiation on Horizontal Surface, Solar Radiation on Inclined Plane Surface. **Solar Thermal Systems:** Introduction, Solar Collectors, Solar Water Heater, Solar Passive Space Heating and Cooling Systems, Solar Industrial Heating Systems, Solar Refrigeration and Air Conditioning Systems, Solar Cookers.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) **CHOICE BASED CREDIT SYSTEM (CBCS)** **SEMESTER – VI**		
15EE654 SOLAR AND WIND ENERGY (Professional Elective) (continued)		
Module-3		**Teaching Hours**
Solar Photovoltaic Systems: Introduction, Solar Cell Fundamentals, Solar Cell Characteristics, Solar Cell Classification, Solar Cell Technologies, Solar Cell, Module, and Array Construction, Maximizing the Solar PV Output and Load Matching. Maximum Power Point Tracker. Balance of System Components, Solar PV Systems, Solar PV Applications.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Wind Energy: Introduction, Basic Principles of Wind Energy Conversion, History of Wind Energy, Wind Energy Scenario – World and India. The Nature of the Wind, The Power in the Wind, Forces on the Blades, Wind Energy Conversion, Wind Data and Energy Estimation, Site Selection Considerations **Wind energy systems:** Environment and Economics Environmental benefits and problems of wind energy, Economics of wind energy, Factors influence the cost of energy generation, machine parameters, Life cycle cost analysis ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
Basic Components of a Wind Energy Conversion(WEC) System: Classification of WEC systems, Advantages and Disadvantages of WECS, Types of Wind Machines (Wind Energy Collectors), Analysis of Aerodynamic Forces Acting on the Blade, Performance of Wind-machines, Generating Systems, Energy Storage, Applications of Wind Energy, Environmental Aspects.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss the importance of energy in human life, relationship among economy and environment with energy use and the increasing role of renewable energy.
- Explain the concept of energy storage and the principles of energy storage devices.
- To discuss solar radiation on horizontal and tilted surface, its characteristics, measurement and analysis of radiation data.
- Describe the process of harnessing solar energy and its applications in heating and cooling.
- Discuss fabrication, operation of solar cell, electrical characteristics, sizing and design of solar PV systems and their applications.
- Explain basic Principles of Wind Energy Conversion, collection of wind data, energy estimation and site selection.
- Discuss the performance of Wind-machines, energy storage, applications of Wind Energy and environmental aspects.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Design/ Development of Solutions, The Engineer and Society, Environment and Sustainability, Ethics, Project Management and Finance.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – VI

15EE654 SOLAR AND WIND ENERGY(Professional Elective) (continued)

Textbook

1	Non-Conventional Energy Resources	B. H. Khan	McGraw Hill	2nd Edition 2017
2	Non-Conventional Sources of Energy	Rai, G. D	Khanna Publishers	4th Edition, 2009

Reference Books

1	Non-Conventional Energy Resources	ShobhNath Singh	Pearson	1st Edition, 2015
2	Solar Energy – Principles of Thermal Collections and Storage	S.P. Sukhatme J.K.Nayak	McGraw Hill	3rd Edition, 2008
3	Wind Turbine Technology	Ahmad Hemami	Cengage	1st Edition, 2012

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -VI			
ARTIFICIAL NEURAL NETWORKS & FUZZY LOGIC (Open Elective)			
Subject Code	15EE661	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To expose the students to the concepts of feed forward neural networks.
- To provide adequate knowledge about feedback networks.
- To teach about the concept of fuzziness involved in various systems.
- To provide adequate knowledge about fuzzy set theory. ■

Module		Teaching Hours
Module-1		
Fundamentals of Neural Networks: Basic concepts of Neural networks, Human Brain, Model of an Artificial Neuron, Neural network architectures, Characteristics of Neural Networks, Learning methods, Taxonomy of Neural Network Architectures, Early Neural Network Architectures. **Back propagation Networks**: Architecture of a Back propagation network, the Perceptron Model, The solution, Single layer Artificial Neural Network, Model for Multilayer Perceptron, Back propagation Learning, Illustration, Applications.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Back propagation Networks (continued): Effect of Tuning Parameters of the Back propagation Neural Network, Selection of Various Parameters in BPN, Variations of Standard Back propagation Algorithm. **Associative Memory:** Auto correlators, Hetero correlators: Kosko's Discrete BAM, Wang et al.'s Multiple Training Encoding Strategy, Exponential BAM, Associative Memory for Real-coded Pattern Pairs, Applications, Recent Trends. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-3		
Adaptive Resonance Theory: Introduction, ART l, ART 2, Applications, Sensitivities of Ordering of Data.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-4		
Fuzzy Set Theory: Fuzzy versus Crisp, Crisp sets, Fuzzy Sets, Crisp Relations, FuzzyRelations. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding. L_3 – Applying.	
Module-5		
Fuzzy Logic And Inference: Crisp Logic, Predicate Logic, Fuzzy Logic, Fuzzy Rule based System, Defuzzification Methods, Applications. **Type – 2 Fuzzy Sets**: Representation of Type – 2 Fuzzy Sets, Operations on Type – 2 Fuzzy Sets, Interval Type – 2 Fuzzy Sets.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding. L_3 – Applying.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – VI

15EE661 ARTIFICIAL NEURAL NETWORKS & FUZZY LOGIC (Open Elective) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Show an understanding of Organization of the Brain, Biological and Artificial Neuron Models
- Show an understanding of Back propagation network architecture, Perceptron Model, Single layer Artificial Neural Network, Model for Multilayer Perceptron, Back propagation Learning,
- Show an understanding of Back propagation training and summary of Back propagation Algorithm
- Show an understanding Bidirectional Associative Memory (BAM) Architecture
- Show an understanding adaptive resonance theory architecture and its applications
- Differentiate between crisp logic, predicate logic and fuzzy logic.
- Explain fuzzy rule based system
- Show an understanding of Defuzzification methods. ■

Graduate Attributes (As per NBA)

Engineering Knowledge

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Neural Networks, Fuzzy Systems and Evolutionary Algorithms: Synthesis and Applications.	S. Rajasekaran, G.A. VijayalakshmiPai	PHI Learning	2^{nd} Edition, 2017

Reference Books

1	Neural Networks – A comprehensive foundation	Simon Haykin	Prentice Hall	3rd Edition, 2004.
2	Fuzzy Logic With Engineering Applications	Timothy J Ross	Wiley	3rd Edition, 2014
3.	Fuzzy sets and Fuzzy Logic: Theory and Applications	Klir, G.J. Yuan Bo	Prentice Hall	2005.

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – VI

SENSORS AND TRANSDUCERS(Open Elective)

Subject Code	15EE662	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80

Credits – 03

Course objectives:

- To discuss need of transducers, their classification, advantages and disadvantages.
- To discuss working of different types of transducers and sensors..
- To discuss recent trends in sensor technology and their selection.
- To discuss basics of signal conditioning and signal conditioning equipment.
- To discuss configuration of Data Acquisition System and data conversion.
- To discuss the basics of Data transmission and telemetry.
- To explain measurement of various non-electrical quantities.■

Module-1		Teaching Hours
Sensors and Transducers: Introduction, Classification of Transducers**,** Advantages and Disadvantages of Electrical Transducers, Transducers Actuating Mechanisms, Resistance Transducers, Variable Inductance Transducers, Capacitive Transducers, Piezoelectric Transducers, Hall Effect Transducers, Thermoelectric Transducers, Photoelectric Transducers.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-2		
Sensors and Transducers (continued): Stain Gages, Load Cells, Proximity Sensors, Pneumatic Sensors, Light Sensors, Tactile Sensors, Fiber Optic Transducers, Digital Transducers, Recent Trends – Smart Pressure Transmitters, Selection of Sensors, Rotary – Variable Differential Transformer, Synchros and Resolvers, Induction Potentiometers, Micro Electromechanical Systems.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-3		
Signal Condition: Introduction, Functions of Signal Conditioning Equipment, Amplification, Types of Amplifiers, Mechanical Amplifiers Fluid Amplifiers, Optical Amplifiers, Electrical and electronic Amplifiers. **Data Acquisition Systems and Conversion:**Introduction, Objectives and Configuration of Data Acquisition System, Data Acquisition Systems, Data Conversion.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-4		
Data Transmission and Telemetry: Data/Signal Transmission, Telemetry. **Measurement of Non – Electrical Quantities:**Pressure Measurement■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Measurement of Non – Electrical Quantities (continued): Temperature Measurement, Flow Measurement – Introduction, Electromagnetic Flow meters, Ultrasonic Flow Meters, Thermal Metes, Wire Anemometers. Measurement of Displacement, Measurement of Velocity/ Speed, Measurement of Acceleration, Measurement of Force, Measurement of Torque, Measurement of Shaft Power, Measurement of Liquid Level, Measurement of Viscosity.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER – VI

15EE662 SENSORS AND TRANSDUCERS(Open Elective) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Discuss need of transducers, their classification, advantages and disadvantages.
- Show an understanding of working of various transducers and sensors.
- Discuss recent trends in sensor technology and their selection.
- Discuss basics of signal conditioning and signal conditioning equipment.
- Discuss configuration of Data Acquisition System and data conversion.
- Show knowledge of data transmission and telemetry.
- Explain measurement of non-electrical quantities -temperature, flow, speed, force, torque, power and viscosity. ■

Graduate Attributes (As per NBA)

Engineering Knowledge

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module. ■

Textbook

1	Electrical and Electronic Measurements and instrumentation	R.K Rajput	S. Chand	3rd Edition, 2013.

Reference Books

1	A Course in Electronics and Electrical Measurements and Instruments	J.B. Gupta	Katson Books	13th Edition, 2008
2	A Course in Electrical and Electronic Measurements and Instrumentation	A. K. Sawheny	DhanpatRai	2015

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VI

BATTERIES AND FUEL CELLS FOR COMMERCIAL, MILITARY AND SPACE APPLICATIONS
(Open Elective)

Subject Code	15EE663	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80

Credits - 03

Course objectives:

- To discuss the current status of various rechargeable batteries and fuel cells for various applications.
- To discuss the performance capabilities and limitations of batteries and fuel cells.
- To discuss the performance requirements for next-generation high-power rechargeable lithium-based batteries and sealed nickel-cadmium and lead-acid batteries.
- To discuss fuel cells that are best suited for applications where electrical power requirements vary between several kilowatts (kW) to a few megawatts (MW)
- To describe the high-power batteries currently used by EVs and HEVs and various next-generation rechargeable batteries best suited for all-electric cars, EVs, and HEVs.
- To discuss low-power battery configurations that are best suited for compact commercial, industrial, and medical applications.
- To identify the design aspects and performance characteristics of micro- and nano-batteries best suited for detection, sensing, and monitoring devices.■

Module-1		Teaching Hours
Current Status of Rechargeable Batteries and Fuel Cells: Rechargeable Batteries, Fundamental Aspects of a Rechargeable Battery, Rechargeable Batteries Irrespective of Power Capability, Rechargeable Batteries for Commercial and Military Applications, Batteries for Low-Power Applications, Fuel Cells. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Batteries for Aerospace and Communications Satellites: Introduction, On-board Electrical Power System, Battery Power Requirements and Associated Critical Components, Cost-Effective Design Criterion for Battery-Type Power Systems for Spacecraft, Spacecraft Power System Reliability, Ideal Batteries for Aerospace and Communications Satellites, Performance Capabilities and Battery Power Requirements for the Latest Commercial and Military Satellite Systems, Military Satellites for Communications, Surveillance, Reconnaissance, and Target Tracking, Batteries Best Suited to Power Satellite Communications Satellites.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Fuel Cell Technology: Introduction, Performance Capabilities of Fuel Cells Based on Electrolytes, Low-Temperature Fuel Cells Using Various Electrolytes, Fuel Cells Using a Combination of Fuels, Fuel Cell Designs for Multiple Applications, Ion-Exchange Membrane Fuel Cells, Potential Applications of Fuel Cells, Fuel Cells for Aircraft Applications, Fuel Cells for Commercial, Military, and Space Applications, Fuel Cells Capable of Operating in Ultra-High-Temperature Environments, Fuel Cell Requirements for Electric Power Plant Applications.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Batteries for Electric and Hybrid Vehicles: Introduction, Chronological Development History of Early Electric Vehicles and Their Performance Parameters, Electric and Hybrid Electric Vehicles		**08**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VI

15EE663 BATTERIES & FUEL CELLS FOR COMMERCIAL, MILITARY & SPACE APPLICATIONS(Open Elective) (continued)

Module-4(continued)		Teaching Hours
Batteries for Electric and Hybrid Vehicles (continued): Developed Earlier by Various Companies and Their Performance Specifications, Development History of the Latest Electric and Hybrid Electric Vehicle Types and Their Performance Capabilities and Limitations, Performance Requirements of Various Rechargeable Batteries, Materials for Rechargeable Batteries, Critical Role of Rare Earth Materials in the Development of EVs and HEVs.■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Low-Power Rechargeable Batteries for Commercial, Space, and Medical Applications: Introduction, Low-Power Battery Configurations, Characteristics, Batteries for Miniaturized Electronic System Applications, for Embedded-System Applications, Batteries for Medical Applications, Selection Criteria for Primary and Secondary (Rechargeable) Batteries for Specific Applications. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:
At the end of the course the student will be able to:

- Discuss the current status, the performance capabilities and limitations of rechargeable batteries and fuel cells for various applications.
- To discuss the performance requirements for next-generation high-power rechargeable lithium-based batteries and sealed nickel-cadmium and lead-acid batteries.
- Discuss fuel cells that are best suited for applications where electrical power requirements vary between several kilowatts (kW) to a few megawatts (MW)
- Describe the high-power batteries currently used by EVs and HEVs and various next-generation rechargeable batteries best suited for all-electric cars, EVs, and HEVs.
- Discuss low-power battery configurations that are best suited for compact commercial, industrial, and medical applications.
- Explain the design aspects and performance characteristics of micro- and nano-batteries best suited for detection, sensing, and monitoring devices.■

Graduate Attributes (As per NBA)
Engineering Knowledge

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Next-Generation Batteries and Fuel Cells for Commercial, Military, and Space Applications	A.R. JHA	CRC Press	1st Edition, 2012

Reference Books

1	Electrochemical Power Sources: Batteries, Fuel Cells, and Supercapacitors.	Vladimir S. Bagotsky	John Wiley	1st Edition,2015
2	Modelling and Control of Fuel Cells: Distributed Generation Applications	M. HashemNehrir Caisheng Wang	Wiley	1st Edition,2009

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VI			
INDUSTRIAL SERVO CONTROL SYSTEMS(Professional Elective)			
Subject Code	15EE664	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To explain the evolution and classification of servos, with descriptions of servo drive actuators, amplifiers, feedback transducers, performance, and troubleshooting techniques.
- To discuss system analogs and vectors, with a review of differential equations.
- To discuss the concept of transfer functions for the representation of differential equations.
- To discuss mathematical equations for electric servo motors, both DC and brushless DC servo motors.
- To represent servo drive components by their transfer function, to combine the servo drive building blocks into system block diagrams.
- To determine the frequency response techniques for proper servo compensation.
- To explain perform indices and performance criteria for servo systems.
- To discuss the mechanical considerations of servo systems.■

Module-1		**Teaching Hours**
Servos: Introduction, Benefits of Servo Systems, Types of Servos - Evolution of Servo Drives, Classification of Drives, Components of Servos - Hydraulic/Electric Circuit Equations,Actuators—Electric,Actuators—Hydraulic,Amplifiers—Electric,Amplifiers—Hydraulic,Transducers (Feedback).■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Machine Servo Drives: Types of Drives, Feed Drive Performance. **Troubleshooting Techniques:** Techniques by Drive, Problems: Their Causes and Cures. **Machine Feed Drives:** Advances in Technology, Parameters for making ApplicationChoices. **Application of Industrial Servo Drives:** Introduction ,Physical System Analogs, Quantities and Vectors,Differential Equations for PhysicalSystems,Electric Servo Motor TransferFunctions and Time Constants, Transport Lag Transfer Function,Hydraulic Servo Motor Characteristics,General Transfer Characteristics.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Generalized Control Theory: Servo Block Diagrams,Frequency-Response Characteristics and Construction of Approximate (Bode) Frequency Charts,Nichols Charts, Servo Analysis Techniques,Servo Compensation. **Indexes of Performance:** Definition of Indexes of Performance for Servo Drives,Indexes of Performance for Electric and Hydraulic Drives.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Performance Criteria: Percent Regulation,Servo System Responses. **Servo Plant CompensationTechniques:** Dead-Zone Nonlinearity,Change-in-Gain Nonlinearity,Structural Resonances,Frequency Selective Feedback,Feedforward Control. **Machine Considerations:**Machine feed drive Considerations,Ball Screw Mechanical Resonances and Reflected Inertias for Machine Drives.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VI

15EE664 INDUSTRIAL SERVO CONTROL SYSTEMS (Open Elective) (continued)

Module-5		**Teaching Hours**
Machine Considerations: Drive Stiffness, Drive Resolution, Drive Acceleration, Drive Speed Considerations, Drive Ratio Considerations, Drive Thrust/Torque And Friction Considerations, Drive Duty Cycles.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Explain the evolution and classification of servos, with descriptions of servo drive actuators, amplifiers, feedback transducers, performance, and troubleshooting techniques.
- Discuss system analogs and vectors, with a review of differential equations.
- Discuss the concept of transfer functions for the representation of differential equations.
- Discuss mathematical equations for electric servo motors, both DC and brushless DC servo motors.
- Represent servo drive components by their transfer function, to combine the servo drive building blocks into system block diagrams.
- Determine the frequency response techniques for proper servo compensation.
- Explain perform indices and performance criteria for servo systems.
- Discuss the mechanical considerations of servo systems.■

Graduate Attributes (As per NBA)

Engineering Knowledge

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text Book

1	Industrial Servo Control SystemsFundamentals andApplications	George W. Younkin	Marcel Dekker	1st Edition, 2003

Reference Books

1	Servo Motors and Industrial Control Theory	RiazollahFiroozian	Springer	2nd Edition, 2014
2	DC SERVOS Application and Design with MATLAB	Stephen M. Tobin	CRC	1st Edition, 2011

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -VI			
CONTROL SYSTEM LABORATORY			
Subject Code	15EEL67	IA Marks	20
Number of Practical Hours/Week	03	Exam Hours	03
Total Number of Practical Hours	42	Exam Marks	80
Credits - 02			

Course objectives:

- To determine the time and frequency domain reposes of a given second order system using software package or discrete components.
- To design and analyze Lead, Lag and Lag – Lead compensators for given specifications.
- To draw the performance characteristics of ac and dc servomotors and synchro-transmitter receiver pair.
- To simulate the DC position and feedback control system to study the effect of P, PI, PD and PID controller and Lead compensator on the step response of the system.
- To write a script files to plot root locus, bode plot, Nyquist plots to study the stability of the system using a software package.■

Sl. NO	Experiments
1	Experiment to draw the speed torque characteristics of (i) AC servo motor (ii) DC servo motor
2	Experiment to draw synchro pair characteristics
3	Experiment to determine frequency response of a second order system
4	(a) To design a passive RC lead compensating network for the given specifications, viz, the maximum phase lead and the frequency at which it occurs and to obtain the frequency response. (b) To determine experimentally the transfer function of the lead compensating network.
5	(a)To design a passive RC lag compensating network for the given specifications, viz, the maximum phase lag and the frequency at which it occurs and to obtain the frequency response. (b) To determine experimentally the transfer function of the lag compensating network
6	Experiment to draw the frequency response characteristics of the lag – lead compensator network and determination of its transfer function.
	Experiments 7 to 11 must be done using MATLAB/SCILAB only.
7	(a) To simulate a typical second order system and determine step response and evaluate time response specifications. (b) To evaluate the effect of additional poles and zeros on time response of second order system. (c) To evaluate the effect of pole location on stability (d) To evaluate the effect of loop gain of a negative feedback system on stability.
8	To simulate a second order system and study the effect of (a) P, (b) PI, (c) PD and (d) PID controller on the step response.
9	(a) To simulate a D.C. Position control system and obtain its step response. (b) To verify the effect of input waveform, loop gain and system type on steady state errors. (c) To perform trade-off study for lead compensator. (d) To design PI controller and study its effect on steady state error.
10	(a) To examine the relationship between open-loop frequency response and stability, open-loop frequency and closed loop transient response (b) To study the effect of open loop gain on transient response of closed loop system using root locus.
11	(a) To study the effect of open loop poles and zeros on root locus contour (b) To estimate the effect of open loop gain on the transient response of closed loop system using root locus. (c) Comparative study of Bode, Nyquist and root locus with respect to stability.

Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding. L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VI

15EEL67 CONTROL SYSTEM LABORATORY(continued)

Course outcomes: At the end of the course the student will be able to:

- Use software package or discrete components in assessing the time and frequency domain reposes of a given second order system.
- Design and analyze Lead, Lag and Lag – Lead compensators for given specifications.
- Determine the performance characteristics of ac and dc servomotors and synchro-transmitter receiver pair used in control systems.
- Simulate the DC position and feedback control system to study the effect of P, PI, PD and PID controller and Lead compensator on the step response of the system.
- Write a script files to plot root locus, bode plot, Nyquist plots to study the stability of the system using a software package.
- Work with a small team to carryout experiments and prepare reports that present lab work. ■

Graduate Attributes (As per NBA)
Engineering Knowledge,Problem Analysis, Individual and Team work, Modern tool usage, Communication.

Conduct of Practical Examination:
1. All laboratory experiments are to be included for practical examination.
2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners.
3. Students can pick one experiment from the questions lot prepared by the examiners.
4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VI			
DIGITAL SIGNAL PROCESSING LABORATORY			
Subject Code	15EEL68	IA Marks	20
Number of Practical Hours/Week	03	Exam Hours	03
Total Number of Practical Hours	42	Exam Marks	80
Credits - 02			

Course objectives:

- To explain the use of MATLAB software in evaluating the DFT and IDFT of given sequence
- To verify the convolution property of the DFT
- To design and implementation of IIR and FIR filters for given frequency specifications.
- To realize IIR and FIR filters.
- To help the students in developing software skills.■

Sl. No	Experiments
1	Verification of Sampling Theorem both in time and frequency domains
2	Evaluation of impulse response of a system
3	To perform linear convolution of given sequences
4	To perform circular convolution of given sequences using (a) the convolution summation formula (b) the matrix method and (c) Linear convolution from circular convolution with zero padding.
5	Computation of N – point DFT and to plot the magnitude and phase spectrum.
6	Linear and circular convolution by DFT and IDFT method.
7	Solution of a given difference equation.
8	Calculation of DFT and IDFT by FFT
9	Design and implementation of IIR filters to meet given specification (Low pass, high pass, band pass and band reject filters)
10	Design and implementation of FIR filters to meet given specification (Low pass, high pass, band pass and band reject filters) using different window functions
11	Design and implementation of FIR filters to meet given specification (Low pass, high pass, band pass and band reject filters) using frequency sampling technique.
12	Realization of IIR and FIR filters

Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding. L_3 – Applying, L_4 – Analysing, L_5 – Evaluating,

Course outcomes: At the end of the course the student will be able to:

- Give physical interpretation of sampling theorem in time and frequency domains.
- Evaluate the impulse response of a system.
- Perform convolution of given sequences to evaluate the response of a system.
- Compute DFT and IDFT of a given sequence using the basic definition and/or fast methods.
- Provide a solution for a given difference equation.
- Design and implement IIR and FIR filters
- Conduct experiments using software and prepare reports that present lab work■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Individual and Team work, Communication.

Conduct of Practical Examination:

1. All laboratory experiments are to be included for practical examination.
2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners.
3. Students can pick one experiment from the questions lot prepared by the examiners.
4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.■

****** END ******

VII SEMESTER DETAILED SYLLABUS

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -VII			
POWER SYSTEM ANALYSIS – 2(Core Course)			
Subject Code	15EE71	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80
Credits - 04			

Course objectives:

- To explain formulation of network models and bus admittance matrix for solving load flow problems.
- To discuss solution of nonlinear static load flow equations by different numerical techniques and methods to control voltage profile.
- To discuss optimal operation of generators on a bus bar, optimal unit commitment, reliability considerations and optimum generation scheduling.
- To discuss optimal power flow solution, scheduling of hydro-thermal system, power system security and reliability.
- To explain formulation of bus impedance matrix for the use in short circuit studies on power systems.
- To explain numerical solution of swing equation for multi-machine stability. ■

Module-1		Teaching Hours
Load Flow Studies: Introduction, Network Model Formulation, Formation of Y_{bus} by Singular Transformation, Load Flow Problem, Gauss-Seidel Method. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying L_4 – Analysing.	
Module-2		
Load Flow Studies (continued): Newton-Raphson Method, Decoupled Load Flow Methods, Comparison of Load Flow Methods, Control of Voltage Profile. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying L_4 – Analysing.	
Module-3		
Optimal System Operation: Introduction, Optimal Operation of Generators on a Bus Bar, Optimal Unit Commitment, Reliability Considerations, Optimum Generation Scheduling. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying L_4 – Analysing.	
Module-4		
Optimal System Operation (continued): Optimal Load Flow Solution, Optimal Scheduling of Hydrothermal System, Power System Security, Maintenance Scheduling, Power System Reliability. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying L_4 – Analysing.	
Module-5		
SymmetricalFault Analysis: Algorithm for Short Circuit Studies, Z_{bus} Formulation. **Power System Stability:** Numerical Solution of Swing Equation, Multimachine Stability. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Formulate network matrices and models for solving load flow problems.
- Perform steady state power flow analysis of power systems using numerical iterative techniques.
- Suggest a method to control voltage profile.
- Show knowledge of optimal operation of generators on a bus bar, optimal unit commitment,

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
15EE71POWER SYSTEM ANALYSIS – 2(Core Subject) (continued)
CHOICE BASED CREDIT SYSTEM (CBCS)

Course outcomes(continued):

- Discuss optimal scheduling for hydro-thermal system, power system security and reliability.
- Analyze short circuit faults in power system networks using bus impedance matrix.
- Perform numerical solution of swing equation for multi-machine stability■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations of complex problems, Modern Tool Usage, Ethics, Individual and Team Work, Communication, Life-long Learning.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks.Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module.

Textbook

1	Modern Power System Analysis	D. P. Kothari	McGraw Hill	4th Edition, 2011

Reference Books

1	Computer Methods in Power Systems Analysis	Glenn W Stagg Ahmed H Ei - Abiad	McGraw Hill	1stEdition, 1968
2	Computer Techniques in Power System Analysis	M.A. Pai	McGraw Hill	2ndEdition, 2006
3	Power System Analysis	HadiSaadat	McGraw Hill	2ndEdition, 2002

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VII

POWER SYSTEM PROTECTION(Core Subject)

Subject Code	15EE72	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To discuss performance of protective relays, components of protection scheme and relay terminology.
- To explain relay construction and operating principles.
  To explain Overcurrent protection using electromagnetic and static relays and Overcurrent protective schemes.
- To discuss types of electromagnetic and static distance relays, effect of arc resistance, power swings, line length and source impedance on performance of distance relays.
- To discuss pilot protection; wire pilot relaying and carrier pilot relaying.
- To discuss construction, operating principles and performance of various differential relays for differential protection.
- To discuss protection of generators, motors, Transformer and Bus Zone Protection.
- To explain the principle of circuit interruption and different types of circuit breakers.
- To describe the construction and operating principle of different types of fuses and to give the definitions of different terminologies related to a fuse.
- To discuss protection Against Overvoltages and Gas Insulated Substation (GIS). ■

Module-1		Teaching Hours
Introduction to Power System Protection: Need for protective schemes, Nature and Cause of Faults, Types of Fault,Effects of Faults, Fault Statistics, Zones of Protection, Primary and Backup Protection, Essential Qualities of Protection,Performance of Protective Relaying, Classification of Protective Relays, Automatic Reclosing, Current Transformers for protection, Voltage Transformers for Protection. **Relay Construction and Operating Principles:** Introduction, Electromechanical Relays, Static Relays – Merits and Demerits of Static Relays, Numerical Relays, Comparison between Electromechanical Relays and Numerical Relays. **Overcurrent Protection:**Introduction, Time – current Characteristics, Current Setting, Time Setting.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-2		
Overcurrent Protection (continued): Overcurrent Protective Schemes, Reverse Power or Directional Relay, Protection of Parallel Feeders, Protection of Ring Mains, Earth Fault and Phase Fault Protection, Combined Earth Fault and Phase Fault Protective Scheme, Phase Fault Protective Scheme, Directional Earth Fault Relay, Static Overcurrent Relays, Numerical Overcurrent Relays. **Distance Protection:** Introduction, Impedance Relay, Reactance Relay, Mho Relay, Angle Impedance Relay, Effect of Arc Resistance on the Performance of Distance Relays, Reach of Distance Relays. Effect of Power Surges(Power Swings) on Performance of Distance Relays, Effect of Line Length and Source Impedance on Performance of Distance Relays.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Pilot Relaying Schemes: Introduction, Wire Pilot Protection, Carrier Current Protection **Differential Protection**: Introduction, Differential Relays, Simple Differential Protection, Percentage or Biased Differential Relay, Differential Protection of 3 Phase Circuits, Balanced (Opposed) Voltage Differential Protection. **Rotating Machines Protection:** Introduction, Protection of Generators. **Transformer and Buszone Protection:** Introduction, Transformer Protection, Buszone Protection, Frame Leakage Protection.■		**10**
Revised Bloom's Taxonomy Level	L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VII		
15EE72 POWER SYSTEM PROTECTION (Core Course) (continued)		
Module-4		**Teaching Hours**
Circuit Breakers: Introduction, Fault Clearing Time of a Circuit Breaker, Arc Voltage, Arc Interruption, Restriking Voltage and Recovery Voltage, Current Chopping, Interruption of Capacitive Current, Classification of Circuit Breakers, Air – Break Circuit Breakers, Oil Circuit Breakers, Air – Blast Circuit Breakers, SF_6 Circuit Breakers, Vacuum Circuit Breakers, High Voltage Direct Current Circuit Breakers, Rating of Circuit Breakers, Testing of Circuit Breakers.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5		
Fuses: Introductions, Definitions, Fuse Characteristics, Types of Fuses, Applications of HRC Fuses, Selection of Fuses, Discrimination. **Protection against Overvoltages:** Causes of Overvoltages, Lightning phenomena, Wave Shape of Voltage due to Lightning, Over Voltage due to Lightning, Klydonograph and Magnetic Link, Protection of Transmission Lines against Direct Lightning Strokes, Protection of Stations and Sub – Stations from Direct Strokes, Protection against Travelling Waves, Insulation Coordination, Basic Impulse Insulation Level (BIL). **Modern Trends in Power System Protection:** Introduction, gas insulated substation/switchgear (GIS).■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss performance of protective relays, components of protection scheme and relay terminology overcurrent protection.
- Explain the working of distance relays and the effects ofarc resistance, power swings, line length and source impedance on performance of distance relays.
- Discuss pilot protection; wire pilot relaying and carrier pilot relaying.
- Discuss construction, operating principles and performance of differential relays for differential protection.
- Discuss protection of generators, motors, Transformer and Bus Zone Protection.
- Explain the principle of circuit interruption in different types of circuit breakers.
- Describe the construction and operating principle of different types of fuses and to give the definitions of different terminologies related to a fuse.
- Discuss protection against Overvoltages and Gas Insulated Substation (GIS).■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Ethics, Communication, Life-long Learning.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks.Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module. ■

Textbook

1	Power System Protection and Switchgear	Badri Ram, D.N. Vishwakarma	McGraw Hill	2nd Edition
2	Power System Protection and Switchgear(For additional study on gapless arrester, Refer to pages 458 to 461)	BhuvaneshOza et al	McGraw Hill	1st Edition, 2010

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VII				
15EE72 POWER SYSTEM PROTECTION (Core Course) (continued)				
Reference Books				
1	Protection and Switchgear	Bhavesh et al	Oxford	1st Edition, 2011
2	Power System Switchgear and Protection	N. Veerappan S.R. Krishnamurthy	S. Chand	1st Edition, 2009
3	Fundamentals of Power System Protection	Y.G.Paithankar S.R. Bhide	PHI	1st Edition, 2009

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VII

HIGH VOLTAGE ENGINEERING (Core Course)

Subject Code	15EE73	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To discuss conduction and breakdown in gases, liquid dielectrics.
- To discuss breakdown in solid dielectrics.
  To discuss generation of high voltages and currents and their measurement.
- To discuss overvoltage phenomenon and insulation coordination in electric power systems.
- To discuss non-destructive testing of materials and electric apparatus.
- To discuss high-voltage testing of electric apparatus■

Module-1		Teaching Hours
Conduction and Breakdown in Gases: Gases as Insulating Media, Collision Process, Ionization Processes, Townsend's Current Growth Equation, Current Growth in the Presence of Secondary Processes, Townsend's Criterion for Breakdown, Experimental Determination of Coefficients α and γ, Breakdown in Electronegative Gases, Time Lags for Breakdown, Streamer Theory of Breakdown in Gases, Paschen's Law, Breakdown in Non-Uniform Fields and Corona Discharges. **Conduction and Breakdown in Liquid Dielectrics:** Liquids as Insulators, Pure Liquids and Commercial Liquids, Conduction and Breakdown in Pure Liquids, Conduction and Breakdown in Commercial Liquids. **Breakdown in Solid Dielectrics:** Introduction, Intrinsic Breakdown, Electromechanical Breakdown, Thermal Breakdown.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-2		
Generation of High Voltages and Currents: Generation of High Direct Current Voltages, Generation of High Alternating Voltages, Generation of Impulse Voltages, Generation of Impulse Currents, Tripping and Control of Impulse Generators.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering , L_2 – Understanding L_3 – Applying.	
Module-3		
Measurement of High Voltages and Currents: Measurement of High Direct Current Voltages, Measurement of High AC and Impulse Voltages, Measurement of High Currents – Direct, Alternating and Impulse, Cathode Ray Oscillographs for Impulse Voltage and Current Measurements.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering , L_2 – Understanding L_3 – Applying.	
Module-4		
Overvoltage Phenomenon and Insulation Coordination in Electric Power Systems: National Causes for Overvoltages - Lightning Phenomenon, Overvoltage due to Switching Surges, System Faults and Other Abnormal, Principles of Insulation Coordination on High Voltage and Extra High Voltage Power Systems.■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Non-Destructive Testing of Materials and Electrical Apparatus: Introduction, Measurement of Dielectric Constant and Loss Factor, Partial Discharge Measurements.		**10**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

15EE73 HIGH VOLTAGE ENGINEERING (Core Course) (continued)

Module-5 (continued)		**Teaching Hours**
High Voltage Testing of Electrical Apparatus: Testing of Insulators and Bushings, Testing of Isolators and Circuit Breakers, Testing of Cables, Testing of Transformers, Testing of Surge Arrestors, Radio Interference Measurements, Testing of HVDC Valves and Equipment.■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:
At the end of the course the student will be able to:
- Explain conduction and breakdown phenomenon in gases, liquid dielectrics.
- Explain breakdown phenomenon in solid dielectrics.
- Explain generation of high voltages and currents
  Discuss measurement techniques for high voltages and currents.
- Discuss overvoltage phenomenon and insulation coordination in electric power systems.
- Discuss non-destructive testing of materials and electric apparatus andhigh-voltage testing of electric apparatus■

Graduate Attributes (As per NBA)
Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Modern Tool Usage, Ethics, Individual and Team Work, Communication, Life-long Learning.

Question paper pattern:
- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	High Voltage Engineering	M.S. Naidu, V.Kamaraju	McGraw Hill	5th Edition, 2013.

Reference Books

1	High Voltage Engineering Fundamentals	E. Kuffel, W.S. Zaengl, J. Kuffel	Newnes	2nd Edition, 2000
2	High Voltage Engineering	Wadhwa C.L.	New Age International	3rd Edition, 2012
3	High-Voltage Test and Measuring Techniques	Wolfgang Hauschild • Eberhard Lemke	Springer	1st Edition2014
4	High Voltage Engineering	Farouk A.M. Rizk	CRC Press	1st Edition2014

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VII			
ADVANCED CONTROL SYSTEMS(Professional Elective)			
Subject Code	15EE741	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To introduce state variable approach for linear time invariant systems in both the continuous and discrete time systems
- To explain development of state models for linear continuous – time and discrete – time systems
- To explain application of vector and matrix algebra to find the solution of state equations for linear continuous – time and discrete – time systems
- To define controllability and observability of a system and testing techniques for controllability and observability of a given system
- To explain design techniques of pole assignment and state observer using state feedback.
- To explain about inherent and intentional nonlinearities that can occur in control system and developing the describing function for the nonlinearities.
- To explain stability analysis of nonlinear systems using describing function analysis.
- To explain the analysis of nonlinear systems using Lyapunov function and design of Lyapunov function for stable systems.■

Module-1		**Teaching Hours**
State Variable Analysis and Design: Introduction, Concept of State, State Variables and State Model, State Modelsfor Linear Continuous – Time Systems, State Variables and Linear Discrete – Time Systems.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 –Analysing, L_5 –Evaluating.	
Module-2		
State Variable Analysis and Design (continued): Diagonalization, Solution of State Equations, Concepts of Controllability and Observability. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 –Analysing, L_5 –Evaluating.	
Module-3		
Pole Placement Design and State Observers: Introduction, Stability Improvements by State Feedback, Necessary and Sufficient Conditions for Arbitrary Pole Placement, State Regulator Design, Design of State Observer, Compensator Design by the Separation Principle. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 –Analysing, L_5 –Evaluating.	
Module-4		
Non-linear systems Analysis: Introduction, Common Nonlinear System Behaviours, Common Nonlinearities in Control Systems, Fundamentals, Describing Functions of Common Nonlinearities, Stability Analysis by Describing Function Method, Concept of Phase Plane Analysis, Construction of Phase Portraits, System Analysis on the Phase Plane.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 –Analysing, L_5 –Evaluating.	
Module-5		
Non-linear systems Analysis (continued): Simple Variable Structure Systems, Lyapunov Stability Definitions, Lyapunov Stability Theorems, Lyapunov Functions for Nonlinear Systems.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 –Analysing, L_5 –Evaluating.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

15EE741 ADVANCED CONTROL SYSTEMS(Professional Elective) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Discuss state variable approach for linear time invariant systems in both the continuous and discrete time systems.
- Develop of state models for linear continuous – time and discrete – time systems.
- Apply vector and matrix algebra to find the solution of state equations for linear continuous – time and discrete – time systems.
- Define controllability and observability of a system and test for controllability and observability of a given system.
- Design pole assignment and state observer using state feedback.
- Develop the describing function for the nonlinearity present to assess the stability of the system.
- Develop Lyapunov function for the stability analysis of nonlinear systems. ■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations of complex problems, Modern Tool Usage, Ethics, Individual and Team Work, Life-long Learning.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.

Textbook

1	Control Systems Engineering (For the Modules 1 and 2)	I.J. Nagarath and M.Gopal	New Age	5th Edition, 2007
2	Digital Control and State Variable Methods: Conventional and Intelligent Control Systems (For the Modules 3,4 and 5)	M.Gopal	McGraw Hill	3rd Edition, 2008

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VII

UTILIZATION OF ELECTRICAL POWER(Professional Elective)

Subject Code	15EE742	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To discuss electric heating, air-conditioning and electric welding.
- To explain laws of electrolysis, extraction and refining of metals and electro deposition.
- To explain the terminology of illumination, laws of illumination, construction and working of electric lamps.
- To explain design of interior and exterior lighting systems- illumination levels for various purposes light fittings- factory lighting- flood lighting-street lighting
- To discuss systems of electric traction, speed time curves and mechanics of train movement.
- To discuss motors used for electric traction and their control.
- To discuss braking of electric motors, traction systems and power supply and other traction systems.
- Give awareness of technology of electric and hybrid electric vehicles.■

Module-1		Teaching Hours
Heating and welding: Electric Heating, Resistance ovens, Radiant Heating, Induction Heating, High frequency Eddy Current Heating, Dielectric Heating, The Arc Furnace, Heating of Buildings, Air – Conditioning, Electric Welding, Modern Welding Techniques. **Electrolytic Electro – Metallurgical Process:**Ionization, Faraday's Laws of Electrolysis, Definitions, Extraction of Metals, Refining of Metals, Electro Deposition.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Illumination: Introduction, Radiant Energy, Definitions, Laws of Illumination, Polar Curves, Photometry, Measurement of Mean Spherical Candle Power by Integrating Sphere, Illumination Photometer, Energy Radiation and luminous Efficiency, electric Lamps, Cold Cathode Lamp, Lighting Fittings, Illumination for Different Purposes, Requirements of Good Lighting.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Electric Traction Speed - Time Curves and Mechanics of Train Movement: Introduction, Systems of Traction, Systems of electric Traction, Speed - Time Curves for Train Movement,Mechanics of Train Movement, Train Resistance, Adhesive Weight, Coefficient of Adhesion. **Motors for Electric traction:**Introduction, Series and Shunt Motors for Traction Services, Two Similar Motors (Series Type) are used to drive a Motor Car, Tractive Effort and Horse Power, AC Series Motor, Three Phase Induction Motor. **Control of motors:** Control of DC Motors,Tapped Field Control or Control by Field Weakening, Multiple Unit Control, Control of Single Phase Motors, Control of Three Phase Motors. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Braking: Introduction, Regenerative Braking with Three Phase Induction Motors, Braking with Single Phase Series Motors, Mechanical braking, Magnetic Track Brake, Electro – Mechanical Drum Brakes. **Electric Traction Systems and Power Supply:** System of Electric Traction, AC Electrification, Transmission Lines to Sub - Stations, Sub – Stations, Feeding and Distribution System of AC		**08**

<table>
<tr><td colspan="5">B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII</td></tr>
<tr><td colspan="5">15EE742 UTILIZATION OF ELECTRICAL POWER(Professional Elective) (continued)</td></tr>
<tr><td colspan="4">Module-4 (continued)</td><td>Teaching Hours</td></tr>
<tr><td colspan="4">Traction,Feeding and Distribution System for Dc Tramways, Electrolysis by Currents through Earth, Negative Booster, System of Current Collection, Trolley Wires.
Trams, Trolley Buses and Diesel – Electric Traction: Tramways, The Trolley – Bus, Diesel Electric Traction.■</td><td rowspan="2"></td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td colspan="3">L_1 – Remembering, L_2 – Understanding.</td></tr>
<tr><td colspan="5">Module-5</td></tr>
<tr><td colspan="4">Electric Vehicles: Configurations of Electric Vehicles, Performance of Electric Vehicles, Tractive Effort in Normal Driving, Energy Consumption.
Hybrid Electric Vehicles: Concept of Hybrid Electric Drive Trains, Architectures of Hybrid Electric Drive Trains. ■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td colspan="3">L_1 – Remembering, L_2 – Understanding.</td></tr>
<tr><td colspan="5"></td></tr>
<tr><td colspan="5">Course outcomes:
At the end of the course the student will be able to:
Discuss electric heating, air-conditioning and electric welding.
Explain laws of electrolysis, extraction and refining of metals and electro deposition.
Explain the terminology of illumination, laws of illumination, construction and working of electric lamps.
Design interior and exterior lighting systems- illumination levels for factory lighting- flood lighting- street lighting.
Discuss systems of electric traction, speed time curves and mechanics of train movement.
Explain the motors used for electric traction and their control.
Discuss braking of electric motors, traction systems and power supply and other traction systems.
Explain the working of electric and hybrid electric vehicles. ■</td></tr>
<tr><td colspan="5">Graduate Attributes (As per NBA)
Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations of complex problems, The Engineer and Society, Ethics, Individual and Team Work.</td></tr>
<tr><td colspan="5">Question paper pattern:
The question paper will have ten questions.
Each full question is for 16 marks.
There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
Each full question with sub questions will cover the contents under a module.
Students will have to answer 5 full questions, selecting one full question from each module.■</td></tr>
<tr><td colspan="5">Textbook</td></tr>
<tr><td>1</td><td>A Textbook on Power System Engineering</td><td>A. Chakrabarti et al</td><td>DhanpatRai and Co</td><td>2nd Edition, 2010</td></tr>
<tr><td>2</td><td>Modern Electric,Hybrid Electric, and Fuel Cell Vehicles: Fundamentals Theory, and Design (Chapters 04 and 05 for module 5)</td><td>MehrdadEhsani et al</td><td>CRC Press</td><td>1stEdition, 2005</td></tr>
<tr><td colspan="5">Reference Books</td></tr>
<tr><td>1</td><td>Utilization, Generation and Conservation of Electrical Energy</td><td>Sunil S Rao</td><td>Khanna Publishers</td><td>1stEdition, 2011</td></tr>
<tr><td>2</td><td>Utilization of Electric Power and Electric Traction</td><td>G.C. Garg</td><td>Khanna Publishers</td><td>9thEdition, 2014</td></tr>
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B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

CARBON CAPTURE AND STORAGE(Professional Elective)

Subject Code	15EE743	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80

Credits - 03

Course objectives:

- To provide an overview of carbon capture and carbon storage and explain the fundamentals of power generation.
- To explain carbon capture from power generation, industrial processes, using solvent absorption and other technologies including membranes, adsorbents, chemical looping, cryogenics and gas hydrate technology.
- To explain different geological storage methods including storage in coal seams, depleted gas reservoirs and saline formations.
- To explain Carbon dioxide compression and pipeline transport.■

<table>
<tr><td colspan="2">Module-1</td><td>Teaching Hours</td></tr>
<tr><td colspan="2">Introduction: The Carbon Cycle, Mitigating Growth of The Atmospheric Carbon Inventory, The Process of Technology Innovation.
Overview of carbon capture and storage: Carbon Capture, Carbon Storage.
Power generation fundamentals: Physical and Chemical Fundamentals, Fossil-Fueled Power Plant, Combined Cycle Power Generation, Future Developments in Power-Generation Technology.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding, L_3 – Applying.</td></tr>
<tr><td colspan="3">Module-2</td></tr>
<tr><td colspan="2">Carbon capture from power generation: Introduction, Pre-combustion Capture, Post-combustion Capture, Oxy- fuel Combustion Capture, Chemical Looping Capture Systems, Capture-Ready and Retrofit Power Plant, Approaches to Zero-Emission Power Generation.
Carbon capture from industrial processes:Cement Production, Steel Production, Oil Refining, Natural Gas Processing.
Absorption capture systems:Chemical and Physical Fundamentals, Absorption Applications in Post Combustion Capture, Absorption Technology RD&D Status.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.</td></tr>
<tr><td colspan="3">Module-3</td></tr>
<tr><td colspan="2">Adsorption capture systems: Physical and Chemical Fundamentals, Adsorption Process Applications, Adsorption Technology RD&D Status. References and Resources.
Membrane separation systems:Physical and Chemical Fundamentals, Membrane Configuration and Preparation and Module Construction, Membrane Technology RD&D Status, Membrane Applications in Pre-combustion Capture, Membrane and Molecular Sieve Applications in Oxy-fuel Combustion, Membrane Applications in Post-combustion CO_2 Separation, Membrane Applications in Natural Gas Processing.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.</td></tr>
<tr><td colspan="3">Module-4</td></tr>
<tr><td colspan="2">Cryogenic and distillation systems: Physical Fundamentals, Distillation column configuration and operation, Cryogenic oxygen production for oxy-fuel combustion, Ryan–Holmes process for CO_2 – CH_4 separation, RD&D in cryogenic and distillation technologies.
Mineral carbonation: Physical and chemical fundamentals, Current state of technology development, Demonstration and deployment outlook.
Geological storage: Introduction, Geological and engineering fundamentals, Enhanced oil recovery, Saline aquifer storage, Other geological storage options.■</td><td rowspan="2">08</td></tr>
<tr><td>Revised Bloom's Taxonomy Level</td><td>L_1 – Remembering, L_2 – Understanding.</td></tr>
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B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

15EE743 CARBON CAPTURE AND STORAGE(Professional Elective) (continued)

Module-5		Teaching Hours
Ocean storage: Introduction, Physical, chemical, and biological fundamentals, Direct CO_2 injection, Chemical sequestration, Biological sequestration, **Storage in terrestrial ecosystems:** Introduction, Biological and chemical fundamentals, Terrestrial carbon storage options, Full GHG accounting for terrestrial storage, Current R&D focus in terrestrial storage. **Other sequestration and use options:** Enhanced industrial usage, Algal biofuel production.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss the impacts of climate change and the measures that can be taken to reduce emissions.
- Discuss carbon capture and carbon storage.
- Explain the fundamentals of power generation.
- Explain methods of carbon capture from power generation and industrial processes.
- Explain different carbon storage methods: storage in coal seams, depleted gas reservoirs and saline formations.
- Explain Carbon dioxide compression and pipeline transport. ■

Graduate Attributes (As per NBA)

Engineering Knowledge

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Carbon Capture and Storage	Stephen A. Rackley	Elsevier	2010

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -VII			
POWER SYSTEM PLANNING (Professional Elective)			
Subject Code	15EE744	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To discuss primary components of power system planning namely load furcating, evaluation of energy resources, provisions of electricity Act and Energy Conservation Act.
- To explain planning methodology for optimum power system expansion, various types of generation, transmission and distribution
- To explain forecasting of anticipated future load requirements of both demand and energy by deterministic and statistical techniques using forecasting tools.
- To discuss methods to mobilize resources to meet the investment requirement for the power sector
- To perform economic appraisal to allocate the resources efficiently and take proper investment decisions
- To discuss expansion of power generation and planning for system energy in the country
- To discuss evaluation of operating states of transmission system, their associated contingencies and determination of the stability of the system for worst case conditions
- To discuss principles of distribution planning, supply rules, network development and the system studies
- To discuss reliability criteria for generation, transmission, distribution and reliability evaluation and analysis.
- To discuss grid reliability, voltage disturbances and their remedies.
- To discuss planning and implementation of electric –utility activities designed to influence consumer uses of electricity.
- To discuss market principles and the norms framed by CERC for online trading and exchange in the interstate power market. ■

Module-1		**Teaching Hours**
Power System: Power Systems, Planning Principles, Planning Process, Project Planning, Power Development, Power Growth, National and Regional Planning, Enterprise Resources Planning, Structure of a Power System, Power Resources, Planning Tools, Power Planning Organisation, Regulation, Scenario Planning. **Electricity Forecasting:** Load Requirement, System Load, Electricity Forecasting, Forecasting Techniques, Forecasting Modelling, Spatial – Load Forecasting, Peak Load - Forecast, Reactive – Load Forecast, Unloading of a System.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-2		
Power-System Economics: Financial Planning, Techno – Economic Viability, Private Participation, Financial Analysis, Economic Analysis, Economic Characteristics – Generation Units, Transmission, Rural Electrification Investment, Total System Analysis, Credit - Risk Assessment, Optimum Investment, Tariffs. **Generation Expansion:** Generation Capacity and Energy, Generation Mix, Conventional Generation Resources, Nuclear Energy, Clean Coal Technologies. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Generation Expansion (continued): Distributed Power Generation, Renovation and Modernisation of Power Plants. **Transmission Planning:** Transmission Planning Criteria, Right – of – Way, Network Studies, High – Voltage Transmission, Conductors, Sub – Stations, Power Grid, Reactive Power Planning, Energy Storage.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-4		
Distribution: Distribution Deregulation, Planning Principles, Electricity – Supply Rules, Criteria and Standards, Sub – Transmission, Basic Network, Low Voltage Direct Current Electricity,		**08**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

15EE744 POWER SYSTEM PLANNING (Professional Elective) (continued)

Module-4(continued)		**Teaching Hours**
Distribution(continued): Upgradation of Existing Lines and Sub – Stations, Network Development, System Studies, Urban Distribution, Rural Electrification, Villages Self – Sufficiency in Energy, Community Power, Self – Generation. **Reliability and Quality:** Reliability Models, System Reliability, Reliability and Quality Planning, Functional Zones, Generation Reliability Planning Criteria, Transmission Reliability Criteria, Distribution Reliability, Reliability Evaluation, Grid Reliability, Reliability Target, Security Requirement, Disaster Management, Quality of Supply, Reliability and Quality Roadmap.■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Demand-Side Planning: Demand Response, Demand – Response Programmes, Demand– Response Technologies, Energy Efficiency, Energy - Economical Products, Efficient – Energy Users, Supply – Side Efficiency, Energy Audit. **Electricity Market:** Market Principles, Power Pool, Independent System Operator, Distribution System Operator, Power Balancing, Market Participants, Power Markets, Market Rules, Bidding, Trading, Settlement System, Locational Marginal Pricing, Transmission Charges, Merchant Power, Differential Electricity, Congestion Management, Ancillary Services, Hedging, Smart Power Market.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss primary components of power system planning, planning methodology for optimum power system expansion, various types of generation, transmission and distribution.
- Show knowledge of forecasting of future load requirements of both demand and energy by deterministic and statistical techniques using forecasting tools.
- Discuss methods to mobilize resources to meet the investment requirement for the power sector
- Understand economic appraisal to allocate the resources efficiently and appreciate the investment decisions
- Discuss expansion of power generation and planning for system energy in the country, evaluation of operating states of transmission system, their associated contingencies and the stability of the system.
- Discuss principles of distribution planning, supply rules, network development and the system studies
- Discuss reliability criteria for generation, transmission, distribution and reliability evaluation and analysis, grid reliability, voltage disturbances and their remedies
- Discuss planning and implementation of electric –utility activities, market principles and the norms framed by CERC for online trading and exchange in the interstate power market.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations of complex problems, Modern Tool Usage, The Engineer and Society, Ethics, Individual and Team Work, Communication, Life-long Learning.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Electric Power Planning	A. S. Pabla	McGraw Hill,	2nd Edition, 2016

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -VII			
FACTS AND HVDC TRANSMISSION (Professional Elective)			
Subject Code	15EE751	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To discuss transmission interconnections, flow of Power in an AC System, limits of the loading capability, dynamic stability considerations of a transmission interconnection and controllable parameters.
- To explain the basic concepts, definitions of flexible ac transmission systems and benefits from FACTS technology.
- To describe shunt controllers, Static Var Compensator and Static Compensator for injecting reactive power in the transmission system in enhancing the controllability and power transfer capability.
- To describe series Controllers Thyristor-Controlled Series Capacitor (TCSC) and the Static Synchronous Series Compensator (SSSC) for control of the transmission line current.
- To explain advantages of HVDC power transmission, overview and organization of HVDC system.
- To describe the basic components of a converter, the methods for compensating the reactive power demanded by the converter.
- Explain converter control for HVDC systems, commutation failure, control functions.■

Module-1		Teaching Hours
FACTS Concept and General System Considerations: Transmission Interconnections, Flow of Power in an AC System, What Limits the Loading Capability? Power Flow and Dynamic Stability Considerations of a Transmission Interconnection, Relative Importance of Controllable Parameters, Basic Types of FACTS Controllers, Brief Description and Definitions of FACTS Controllers, Checklist of Possible Benefits from FACTS Technology, In Perspective: HVDC or FACTS.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-2		
Static Shunt Compensators: Objectives of Shunt Compensation - Midpoint Voltage Regulation for Line Segmentation, End of Line Voltage Support to Prevent Voltage Instability, Improvement of Transient Stability. Methods of Controllable Var Generation –Thyristor controlled Reactor (TCR) and Thyristor Switched Reactor (TSR), Thyristor Switched Capacitor (TSC).Operation of Single Phase TSC – TSR. Switching Converter Type Var Generators, Basic Operating Principles, Basic Control Approaches. Static VAR Compensators: SVC and STATCOM, the Regulation Slope. Comparison between STATCOM and SVC, V –I and V –Q Characteristics, Transient stability, Response Time.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Static Series Compensators: Objectives of Series Compensation, Concept of Series Capacitive Compensation, Voltage Stability, Improvement of Transient Stability. GTO Thyristor-Controlled Series Capacitor, Thyristor-Switched Series Capacitor, Thyristor-Controlled Series Capacitor, The Static synchronous Series Compensator, Transmitted Power Versus Transmission AngleCharacteristic.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Development of HVDC Technology: Introduction, Advantages of HVDC Systems, HVDC System Costs, Overview and Organization of HVDC Systems, HVDC Characteristics and Economic Aspects. **Power Conversion:**3-Phase Converter, 3-Phase Full Bridge Converter, 12-Pulse Converter.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

15EE751 FACTS AND HVDC TRANSMISSION (Professional Elective) (continued)

Module-5		**Teaching Hours**
Control of HVDC Converter and System: Converter Control for an HVDC System, Commutation Failure, HVDC Control and Design, HVDC Control Functions, Reactive Power and Voltage Stability.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss transmission interconnections, flow of Power in an AC System, limits of the loading capability, dynamic stability considerations of a transmission interconnection and controllable parameters.
- Explain the basic concepts, definitions of flexible ac transmission systems and benefits from FACTS technology.
- Describe shunt controllers, Static Var Compensator and Static Compensator for injecting reactive power in the transmission system in enhancing the controllability and power transfer capability.
- Describe series Controllers Thyristor-Controlled Series Capacitor (TCSC) and the Static Synchronous Series Compensator (SSSC) for control of the transmission line current.
- Explain advantages of HVDC power transmission, overview and organization of HVDC system.
- Describe the basic components of a converter, the methods for compensating the reactive power demanded by the converter.
- Explain converter control for HVDC systems, commutation failure, control functions■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations of complex problems, Modern Tool Usage, Ethics, Individual and Team Work, Communication, Life-long Learning.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbooks

1	Understanding FACTS: Concepts and Technology of Flexible AC Transmission Systems	Narain G Hingorani, Laszlo Gyugyi	Wiley	1st Edition, 2000
2	HVDC Transmission: Power Conversion Applicationsin Power Systems	Chan-Ki Kim et al	Wiley	1st Edition, 2009

Reference Books

1	Thyristor Based FACTS Controllers for Electrical Transmission Systems	R. Mohan Mathur, Rajiv K. Varma	Wiley	1st Edition, 2002

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VII

TESTING AND COMMISSIONING OF POWER SYSTEM APPARATUS(Professional Elective)

Subject Code	15EE752	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80

Credits - 03

Course objectives:

- Describe the process to plan, control and implement commissioning of electrical equipment's.
- Differentiate the performance specifications of transformer and induction motor.
- Demonstrate the routine tests for synchronous machine, induction motor, transformer & switchgears.
- Identification of tools and equipment's used for installation and maintenance of electrical equipment.
- Explain the operation of an electrical equipment's such as isolators, circuit breakers, insulators and switchgears.■

Module-1	Teaching Hours
Electrical Tools, accessories: Tools, Accessories and Instruments required for Installation, Maintenance and Repair Work, India Electricity Rules, Safely Codes Causes and Prevention of Accidents, Artificial Respiration, Workmen's Safety Devices. **Transformers:** Installation, Location Site Selection, Foundation Details, Code of Practice for Terminal Plates, Polarity and Phase Sequence, Oil Tanks, Drying of Winding sand General Inspection. Commissioning Tests As Per National and International Standards - Volts Ratio Earth Resistance, Oil Strength, Insulation Tests, Impulse Tests Polarizing Index, Load Temperature Rise Tests. Specific Tests for Determination of Performance Curves like Efficiencies, Regulation Etc., Determination Mechanical Stress Under Normal and Abnormal Conditions.■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding.	
Module-2	
Synchronous Machines: Specifications as per BIS Standards. Installation - Physical Inspection, Foundation Details, Alignments, Excitation Systems, Cooling and Control Gear, Drying Out. Commissioning Tests - Insulation, Resistance Measurement of Armature and Field Windings, Wave Form and Telephone Interference Tests, Line Charging Capacitance. Performance Tests -Various Tests to Estimate the Performance of Generator Operations, Slip Test, Maximum Lagging Current, Maximum Reluctance Power Tests, Sudden Short Circuit Tests, Transient Sub Transient Parameters, Measurement of Sequence Impedances, Capacitive Reactance, and Separation Of Losses, Temperature Rise Test, and Retardation Tests. Factory Tests -Gap Length, Magnetic Eccentricity, Balancing Vibrations, Bearing Performance.■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-3	
Induction Motor: Specifications. Installation- Location of Motors and its Control Apparatus, Shaft Alignment for Various Coupling, Fitting of Pulleys and Coupling, Drying of Windings. Commissioning Tests -Mechanical Tests For Alignment, Air Gap Symmetry, Tests for Bearings, Vibrations and Balancing. Specific Tests -Performance and Temperature Raise Tests, Stray Load Losses, Shaft Alignment, Re-Writing and Special Duty Capability, Site Test■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 –Analysing, L_5 –Evaluating.	
Module-4	
Laying of Underground Cables: Inspection, Storage, Transportation and Handling of Cables, Cable Handing Equipment, Cable Laying Depths and Clearances from other Services such as Water Sewerage, Gas, Heating and other Mains, Series of Power and Telecommunication Cables and Coordination with these Services, Excavation of Trenches, Cable Jointing and Terminations Testing and Commissioning. Location of Faults using Megger, Effect of Open or Loose Neutral Connections, Provision of Proper Fuses on Service Lines and Their Effect on System, Causes and Dim, and Flickering Lights■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 –Analysing, L_5 –Evaluating.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

15EE752 TESTING AND COMMISSIONING OF POWER SYSTEM APPARATUS
(Professional Elective) (continued)

Module-5		**Teaching Hours**
Switchgear and Protective Devices: Standards, Types, Specification, Installation, Commissioning Tests, Maintenance Schedule, Type and Routine Tests. **Domestic Installation:** Introduction, Testing of Electrical Installation of a Building, Testing of Insulation Resistance to Earth, Testing of Insulation and Resistance between Conductors Continuity or Open Circuit Test, Short Circuit Test, Testing of Earthing Continuity, Location of Faults, IE Rules for Domestic Installation■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 –Analysing, L_5 –Evaluating.	

Course outcomes:

At the end of the course the student will be able to:

- Describe the process to plan, control and implement commissioning of electrical equipment's.
- Differentiate the performance specifications of transformer and induction motor.
- Demonstrate the routine tests for synchronous machine, induction motor, transformer & switchgears.
- Describe corrective and preventive maintenance of electrical equipment's.
- Explain the operation of an electrical equipment's such as isolators, circuit breakers, induction motor and synchronous machines.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Conduct investigations of complex problems, Modern Tool Usage, Ethics, Individual and Team Work, Communication, Life-long Learning.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Text/ Reference Books

1	Testing, Commissioning, Operation and Maintenance of Electrical Equipment	S. Rao	Khanna Publishers	6th Edition, 19th Reprint, 2015
2	Testing and Commissioning of Electrical Equipment	R.L.Chakrasali	Prism Books Pvt Ltd	1st Edition,2014
3	Preventive Maintenance of Electrical Apparatus	S.K.Sharotri	Katson Publishing House	1st Edition, 1980
4	Handbook of Switchgears	BHEL	McGraw Hill	1st Edition, 2005
5	Transformers	BHEL	McGraw Hill	1st Edition, 2003
6	TheJ&P Transformer Book	Martin J. Heathcote	Newnes	12th Edition, 1998

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -VII			
SPACECRAFT POWER TECHNOLOGIES(Professional Elective)			
Subject Code	15EE753	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To discuss the increasing demand for space craft power systems and to give an overview of electrical power system and its technology.
- To discuss near – earth environmental factors that will affect the design of space craft power systems.
- To describe the elements of a space photovoltaic power system, the status of solar cell technologies presently in use.
- To discuss advances in both cell and array technology, and solar thermo photovoltaic energy conversion.
- To discusses, space-qualified components, the array of chemical storage technologies including both batteries and fuel cells.
- To describe components and techniques for achieving the various Power Management and Distribution functions and examples of several PMAD configurations. ■

Module-1		Teaching Hours
Spacecraft: Introduction, the Beginnings, the Electrical Power System. **Environmental Factors:** Introduction, Orbital Considerations, The Near-earth Space Environment. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-2		
Solar Energy Conversion: Introduction, Solar Cell Fundamentals, Space Solar Cell Calibration and Performance Measurements, Silicon Space Solar Cells, III-V Compound Semiconductor Solar Cells, Thin Film Solar Cells. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Solar Energy Conversion (continued): Space Solar Cell Arrays, Space Thermo photovoltaic Power Systems. **Chemical Storage and Generation Systems:** Introduction, Inventions, Evolution of Batteries in Space, Fundamentals of Electrochemistry, Cell and Battery Mechanical Design, Performance Metrics. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Chemical Storage and Generation Systems (continued): Electrochemical Cell Types, Fuel Cell Systems.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Power Management and Distribution (PMAD): Introduction, Functions of PMAD, Components and Packaging, System Examples. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss the increasing demand for space craft power systems and to give an overview of electrical power system and its technology.
- Discuss near – earth environmental factors that will affect the design of space craft power systems.

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

15EE753 SPACECRAFT POWER TECHNOLOGIES(Professional Elective)(continued)

Course outcomes(continued):

- Describe the elements of a space photovoltaic power system, the status of solar cell technologies presently in use.
- Discuss advances in both cell and array technology, and solar thermo photovoltaic energy conversion.
- Discusses, space-qualified components, the array of chemical storage technologies including both batteries and fuel cells.
- Describe components and techniques for achieving the various Power Management and Distribution functions and examples of several PMAD configurations.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations of complex problems, Modern Tool Usage, Ethics, Individual and Team Work, Communication, Life-long Learning.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Spacecraft Power Technologies	A.K. Hyder et al	Imperial College Press	1st Edition, 2000

Reference Books

1	Spacecraft Power Systems	Mukund R. Patel	CRC Press	1st Edition, 2004

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

INDUSTRIAL HEATING (Professional Elective)

Subject Code	15EE754	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80

Credits - 03

Course objectives:

- To explain construction, classification of industrial furnaces and the methods of heat transfer in them
- To discuss heating capacity of batch furnaces
- To discuss heating capacity of continuous furnaces
- To discuss methods of saving energy in industrial furnace systems and fuel consumption calculation.
- To explain operation and control of industrial furnaces.■

Module	Teaching Hours
Module-1	
Industrial Heating Processes: Industrial Process Heating Furnaces, Classifications of Furnaces, Elements of Furnace Construction. **Heat Transfer in Industrial Furnaces:** Heat Required for Load and Furnace, Flow of Heat Within the Charged Load, Heat Transfer to the Charged Load Surface, Determining Furnace Gas Exit Temperature, Thermal Interaction in Furnaces, Temperature Uniformity, Turndown.■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2	
Heating Capacity of Batch Furnaces: Definition of Heating Capacity, Effect of Rate of Heat Liberation, Effect of Rate of Heat Absorption by the Load, Effect of Load Arrangement, Effect of Load Thickness, Vertical Heating, Batch Indirect-Fired Furnaces, Batch Furnace Heating Capacity Practice, Controlled Cooling in or After Batch Furnaces.■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3	
Heating Capacity of Continuous Furnaces: Continuous Furnaces Compared to Batch Furnaces, Continuous Dryers, Ovens, and Furnaces for <1400 F (<760 C), Continuous Midrange Furnaces, 1200 to 1800 F (650 to 980 C), Sintering and Pelletizing Furnaces, Axial Continuous Furnaces for Above 2000 F (1260 C), Continuous Furnaces for 1900 to 2500 F (1038 to 1370 C), Continuous Liquid Heating Furnaces.■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4	
Saving Energy in Industrial Furnace Systems: Furnace Efficiency, Methods for Saving Heat, Heat Distribution in a Furnace, Furnace, Kiln, and Oven Heat Losses, Heat Saving in Direct-Fired Low-Temperature Ovens, Saving Fuel in Batch Furnaces, Saving Fuel in Continuous Furnaces, Effect of Load Thickness on Fuel Economy, Saving Fuel in Reheat Furnaces, Fuel Consumption Calculation, Fuel Consumption Data for Various Furnace Types, Energy Conservation by Heat Recovery from Flue Gases, Energy Costs of Pollution Control.■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-5	**08**
Operation and Control of Industrial Furnaces: Burner and Flame Types, Location, Flame Fitting, Unwanted NOx Formation, Controls and Sensors- Care, Location, Zones, Air/Fuel Ratio Control, Furnace Pressure Control Turndown Ratio, Furnace Control Data Needs, Soaking Pit Heating Control, Uniformity Control in Forge Furnaces, Continuous Reheat Furnace Control. ■	
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VII
15EE754 INDUSTRIAL HEATING (Professional Elective) (continued)

Course outcomes:

At the end of the course the student will be able to:

- Explain construction, classification of industrial furnaces
- Discuss the methods of heat transfer in industrial furnaces.
- Discuss heating capacity of batch furnaces and continuous furnaces
- Discuss methods of saving energy in industrial furnace systems and fuel consumption calculation.
- Explain operation and control of industrial furnaces.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations of complex problems.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Industrial Furnaces	W. Trinks	Wiley	6^{th} Edition, 2004

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VII

POWER SYSTEM SIMULATION LABORATORY

Subject Code	15EEL76	IA Marks	20
Number ofPracticalHours/Week	03	Exam Hours	03
Total Number of PracticalHours	42	Exam Marks	80

Credits - 02

Course objectives:

- To explain the use of MATLAB package to assess the performance of medium and long transmission lines.
- To explain the use of MATLAB package to obtain the power angle characteristics of salient and non-salient pole alternator.
- To explain the use of MATLAB package to study transient stability of radial power systems under three phase fault conditions.
- To explain the use of MATLAB package to develop admittance and impedance matrices of interconnected power systems.
- To explain the use of Mi-Power package to solve power flow problem for simple power systems.
- To explain the use of Mi-Power package to perform fault studies for simple radial power systems.
- To explain the use of Mi-Power package to study optimal generation scheduling problems for thermal power plants. ■

Sl. No		Experiments
1	Use of MATLAB package	Formation for symmetric π /T configuration for Verification of $AD - BC = 1,$ Determination of Efficiency and Regulation.
2		Determination of Power Angle Diagrams, Reluctance Power, Excitation, Emf and Regulation for Salient and Non-Salient Pole Synchronous Machines.
3		To obtain Swing Curve and to Determine Critical Clearing Time, Regulation, Inertia Constant/Line Parameters /Fault Location/Clearing Time/Pre-Fault Electrical Output for a Single Machine connected to Infinite Bus through a Pair of identical Transmission Lines Under 3-Phase Fault On One of the two Lines.
4		Y Bus Formation for Power Systems with and without Mutual Coupling, by Singular Transformation and Inspection Method.
5		Formation of Z Bus(without mutual coupling) using Z-Bus Building Algorithm.
6		Determination of Bus Currents, Bus Power and Line Flow for a Specified System Voltage (Bus) Profile.
7	Use of Mi-Power package	Formation of Jacobian for a System not Exceeding 4 Buses (No PV Buses) in Polar Coordinates.
8		Load Flow Analysis using Gauss Siedel Method, NR Method and Fast Decoupled Method for Both PQand PV Buses.
9		To Determine Fault Currents and Voltages in a Single Transmission Line System with Star-Delta Transformers at a Specified Location for LG and LLG faults by simulation.
10		Optimal Generation Scheduling for Thermal power plants by simulation.

Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating.

Course outcomes:

At the end of the course the student will be able to:

- Develop a program in MATLAB to assess the performance of medium and long transmission lines.
- Develop a program in MATLAB to obtain the power angle characteristics of salient and non-salient pole alternator.
- Develop a program in MATLAB to assess the transient stability under three phase fault at different locations in a of radial power systems.
- Develop programs in MATLAB to formulate bus admittance and bus impedance matrices of interconnected power systems.
- Use Mi-Power package to solve power flow problem for simple power systems.
- Use Mi-Power package to study unsymmetrical faults at different locations in radial power systems
- Use of Mi-Power package to study optimal generation scheduling problems for thermal power plants. ■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VII
15EEL76POWER SYSTEM SIMULATION LABORATORY (continued)
Graduate Attributes (As per NBA) Engineering Knowledge, Problem Analysis, Individual and Team work, Communication.
Conduct of Practical Examination: 1. All laboratory experiments are to be included for practical examination. 2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners. 3. Students can pick one experiment from the questions lot prepared by the examiners. 4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) **CHOICE BASED CREDIT SYSTEM (CBCS)** **SEMESTER -VII**			
RELY AND HIGH VOLTAGE LABORATORY			
Subject Code	15EEL77	IA Marks	20
Number ofPracticalHours/Week	03	Exam Hours	03
Total Number of PracticalHours	42	Exam Marks	80
Credits - 02			

Course objectives:

- To conduct experiments to verify the characteristics of over current, over voltage, under voltage relays both electromagnetic and static type.
- To verify the operation of negative sequence relay.
- To conduct experiments to verify the characteristics of microprocessor based over current, over voltage, under voltage relays and distance relay.
- To conduct experiments on generator, motor and feeder protection.
- To conduct experiments to study the sparkover characteristics for both uniform and non-uniform configurations using High AC and DC voltages.
- To measure high AC and DC voltages
- To experimentally measure the breakdown strength of transformer oil**.**
- To experimentally measure the capacitance of different electrode configuration models using Electrolytic Tank. To generate standard lightning impulse voltage and determine efficiency, energy of impulse generator and 50% probability flashover voltage for air insulation.■

Sl. NO		Experiments
Total of Six experiments are to be conducted by selecting Two experiments from each Part – A, Part – B and Part – C. The experiments under Part – D is compulsory.		
1	**Part - A**	Over Current Relay: (a)Inverse Definite Minimum Time(IDMT)Non-Directional Characteristics (b) Directional Features (c) IDMT Directional.
2		IDMT Characteristics of Over Voltage or Under Voltage Relay (Solid State or Electromechanical type).
3		Operation of Negative Sequence Relay.
4	**Part - B**	Operating Characteristics of Microprocessor Based (Numeric) Over –Current Relay.
5		Operating Characteristics of Microprocessor Based (Numeric) Distance Relay.
6		Operating Characteristics of Microprocessor Based (Numeric) Over/Under Voltage Relay.
7	**Part - C**	Generation Protection: Merz Price Scheme.
8		Feeder Protection against Faults.
9		Motor Protection against Faults.
10	**Part - D**	Spark Over Characteristics of Air subjected to High Voltage AC with Spark Voltage Corrected to Standard Temperature and Pressure for Uniform [as per IS1876: 2005]and Non-uniform [as per IS2071(Part 1) : 1993] Configurations: Sphere – Sphere, Point –Plane, Point – Point and Plane – Plane.
11		Spark Over Characteristics of Air subjected to High voltage DC.
12		Measurement of HVAC and HVDC using Standard Spheres as per IS 1876 :2005
13		Measurement of Breakdown Strength of Transformer Oil as per IS 1876 :2005
14		Field Mapping using Electrolytic Tank for any one of the following Models: Cable/ Capacitor/ Transmission Line/ Sphere Gap.
15		(a) Generation of standard lightning impulse voltage and to determine efficiency and energy of impulse generator. (b) To determine 50% probability flashover voltage for air insulation subjected to impulse voltage.

Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -VII
15EEL77 RELY AND HIGH VOLTAGE LABORATORY (continued)

Course outcomes:

At the end of the course the student will be able to:

- Experimentally verify the characteristics of over current, over voltage, under voltage and negative sequence relays both electromagnetic and static type.
- Experimentally verify the characteristics of microprocessor based over current, over voltage, under voltage relays and distance relay.
- Show knowledge of protecting generator, motor and feeders.
- Analyze the spark over characteristics for both uniform and non-uniform configurations using High AC and DC voltages.
- Measure high AC and DC voltages and breakdown strength of transformer oil**.**
- **Draw electric field and** measure the capacitance of different electrode configuration models.
- Show knowledge of generating standard lightning impulse voltage to determine efficiency, energy of impulse generator and 50% probability flashover voltage for air insulation.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Individual and Team work, Communication.

Conduct of Practical Examination:

1. All laboratory experiments are to be included for practical examination.
2. Breakup of marks and the instructions printed on the cover page of answer script to be strictly adhered by the examiners.
3. Students can pick one experiment from the questions lot prepared by the examiners.
4. Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VII			
PROJECT PHASE – I AND SEMINAR			
Subject Code	15EEP78	IA Marks	100
Number ofPracticalHours/Week	--	Exam Hours	--
Total Number of PracticalHours	--	Exam Marks	--
Credits - 02			

Course objectives:

- Support independent learning.
- Guide to select and utilize adequate information from varied resources maintaining ethics.
- Guide to organize the work in the appropriate manner and present information (acknowledging the sources) clearly.
- Develop interactive, communication, organisation, time management, and presentation skills.
- Impart flexibility and adaptability.
- Inspire independent and team working.
- Expand intellectual capacity, credibility, judgement, intuition.
- Adhere to punctuality, setting and meeting deadlines.
- Instil responsibilities to oneself and others.
- Train students to present the topic of project work in a seminar without any fear, face audience confidently, enhance communication skill, involve in group discussion to present and exchange ideas. ■

Project Phase-1 Students in consultation with the guide/s shall carry out literature survey/ visit industries to finalize the topic of the Project. Subsequently, the students shall collect the material required for the selected project, prepare synopsis and narrate the methodology to carry out the project work

Seminar: Each student, under the guidance of a Faculty, is required to

- Present the seminar on the selected project orally and/or through power point slides.
- Answer the queries and involve in debate/discussion.
- Submit two copies of the typed report with a list of references.

The participants shall take part in discussion to foster friendly and stimulating environment in which the students are motivated to reach high standards and become self-confident. ■

Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating.

Course outcomes:

At the end of the course the student will be able to:

- Demonstrate a sound technical knowledge of their selected project topic.
- Undertake problem identification, formulation and solution.
- Design engineering solutions to complex problems utilising a systems approach.
- Communicate with engineers and the community at large in written an oral forms.
- Demonstrate the knowledge, skills and attitudes of a professional engineer.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Individual and Team work, Communication.

Continuous Internal Evaluation

CIE marks for the project report (50 marks)and seminar (50 marks) shall be awarded (based on the quality of report and presentation skill, participation in the question and answer session by the student) by the committee constituted for the purpose by the Head of the Department. The committee shall consist of three faculty from the department with the senior most acting as the Chairman. ■

****** END ******

VIII SEMESTER DETAILED SYLLABUS

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VIII

POWER SYSTEM OPERATION AND CONTROL(Core Course)

Subject Code	15EE81	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To describe various levels of controls in power systems and the vulnerability of the system.
- To explain components, architecture and configuration of SCADA.
- To define unit commitment and explain various constraints in unit commitment and the solution methods
- To explain issues of hydrothermal scheduling and solutions to hydro thermal problems
- To explain basic generator control loops, functions of Automatic generation control, speed governors and mathematical models of Automatic Load Frequency Control
- To explain automatic generation control, voltage and reactive power control in an interconnected power system.
- To explain reliability and contingency analysis, state estimation and related issues. ■

Module	Teaching Hours
Module-1	
Introduction: Operating States of Power System, Objectives of Control, Key Concepts of Reliable Operation, Preventive and Emergency Controls, Energy Management Centres. **Supervisory Control and Data acquisition (SCADA):** Introduction to SCADA and its Components, Standard SCADA Configurations, Users of Power Systems SCADA, Remote Terminal Unit for Power System SCADA, Common Communication Channels for SCADA in Power Systems, Challenges for Implementation of SCADA. **Unit Commitment:** Introduction, SimpleEnumeration Constraints, Priority List Method, DynamicProgramming Method for Unit Commitment.■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_4 – Analysing.	
Module-2	
Hydro-thermal Scheduling: Introduction, Scheduling Hydro Systems, Discrete Time Interval Method, Short Term Hydro Thermal Scheduling Using $\gamma - \lambda$ Iterations, Short Term Hydro Thermal Scheduling Using Penalty Factors. **Automatic Generation Control (AGC):** Introductions, Basic Generator Control Loops, Commonly used Terms in AGC, Functions of AGC, Speed Governors.■	**10**
Revised Bloom's Taxonomy Level: L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3	
Automatic Generation Control (continued): Mathematical Model of Automatic Load Frequency Control, AGC Controller, Proportional Integral Controller. **Automatic Generation Control in interconnected Power system:** Introductions, Tie - Line Control with Primary Speed Control, Frequency Bias Tie - Line Control, State-Space Models.■	**10**
Revised Bloom's Taxonomy Level: L_3 – Applying.	
Module-4	
Automatic Generation Control in interconnected Power system (continued): State-Space Model for Two - Area System, Tie-Line Oscillations, Related Issues in Implementation of AGC. **Voltage and Reactive Power Control:** Introduction, Production and Absorption of Reactive Power, Methods of Voltage Control, Dependence of Voltage on Reactive Power , Sensitivity of Voltage to Changes in P And Q, Cost Saving, Methods of Voltage Control by Reactive Power Injection, Voltage Control Using Transformers, Voltage Stability. ■	**10**
Revised Bloom's Taxonomy Level: L_3 – Applying.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VIII

15EE81POWER SYSTEM OPERATION AND CONTROL(Core Course) (continued)

Module-5	**Teaching Hours**
Power System Reliability and Security: Introduction, Security Levels of System, Reliability Cost, Adequacy Indices, Functions of System Security, Contingency Analysis, Linear Sensitivity Factors, Contingency Selection and Ranking. **State estimation of Power Systems:** Introduction, Linear Least Square Estimation, DC State Estimator, Other Issues in State Estimation.■	**10**
Revised Bloom's Taxonomy Level: L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Describe various levels of controls in power systems, the vulnerability of the system,components, architecture and configuration of SCADA.
- Solve unit commitment problems
- Explain issues of hydrothermal scheduling and solutions to hydro thermal problems
- Explain basic generator control loops, functions of Automatic generation control, speed governors
- Develop and analyze mathematical models of Automatic Load Frequency Control
- Explain automatic generation control, voltage and reactive power control in an interconnected power system.
- Explain reliability, security, contingency analysis, state estimation and related issues of power systems.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Conduct investigations of complex problems, Modern Tool Usage, Communication, Life-long Learning.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks. Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module.

Textbook

1	Power System Operation and Control	K. Uma Rao	Wiley	1st Edition, 2012

Reference Books

1	Power Generation Operation and Control	Allen J Wood etal	Wiley	2nd Edition,2003
2	Power System Stability and Control	Kundur	McGraw Hill	8th Reprint, 2009

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VIII

INDUSTRIAL DRIVES AND APPLICATIONS(Core Course)

Subject Code	15EE82	IA Marks	20
Number of Lecture Hours/Week	04	Exam Hours	03
Total Number of Lecture Hours	50	Exam Marks	80

Credits - 04

Course objectives:

- To define electric drive, its parts, advantages and explain choice of electric drive.
- To explain dynamics and modes of operation of electric drives.
- To explain selection of motor power ratings and control of dc motor using rectifiers.
- To analyze the performance of induction motor drives under different conditions.
- To explain the control of induction motor, synchronous motor and stepper motor drives.
- To discuss typical applications electrical drives in the industry. ■

Module-1	Teaching Hours
Electrical Drives: Electrical Drives, Advantages of Electrical Drives. Parts of Electrical Drives, Choice of Electrical Drives, Status of dc and ac Drives. **Dynamics of Electrical Drives:** Fundamental Torque Equations, Speed TorqueConventions and Multiquadrant Operation. Equivalent Values of DriveParameters, Components of Load Torques, Nature and Classification of LoadTorques, Calculation of Time and Energy Loss in Transient Operations, SteadyState Stability, Load Equalization. **Control Electrical Drives:** Modes of Operation, Speed Control and Drive Classifications, Closed loop Control of Drives. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Module-2	
Selection of Motor Power Ratings: Thermal Model of Motor for Heating and Cooling, Classes of Motor Duty, Determination of Motor Rating. **Direct Current Motor Drives:** Controlled Rectifier Fed dc Drives, Single Phase Fully Controlled Rectifier Control of dc Separately Excited Motor,SinglePhase Half Controlled Rectifier Control of dc Separately Excited Motor, Three Phase Fully Controlled Rectifier Control of dc Separately Excited Motor,Three Phase Half Controlled Rectifier Control of dc Separately Excited Motor, Multiquadrant Operation of dc Separately Excited Motor Fed Form Fully Controlled Rectifier,Rectifier Control of dc Series Motor, Supply Harmonics, Power Factor and Ripple in Motor Current,Chopper Control of Separately Excited dcMotor, Chopper Control of Series Motor. ■	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Module-3	
Induction Motor Drives: Analysis and Performance ofThree Phase Induction Motors, Operation with Unbalanced Source Voltage and Single Phasing,Operation with Unbalanced Rotor Impedances,Analysis of Induction Motor Fed From Non-Sinusoidal Voltage Supply,Starting, Braking, Transient Analysis.Speed Control Techniques-Stator Voltage Control, Variable Voltage Frequency Control from Voltage Sources. ■	**10**
Revised Bloom's Taxonomy Level: L_2 – Understanding, L_3 – Applying, L_4 – Analysing, L_5 – Evaluating.	

Module-4	
Induction Motor Drives (continued): Voltage Source Inverter (VSI) Control, Cycloconverter Control, Closed Loop Speed Control and Converter Rating for VSI and Cycloconverter Induction Motor Drives, Variable Frequency Control from a Current Source, Current Source (CSI) Control,current regulated voltage source inverter control, speed control of single phase induction motors. **Synchronous Motor Drives:** Operation from fixed frequency supply-starting, synchronous motor	**10**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER -VIII		
15EE82 INDUSTRIAL DRIVES AND APPLICATIONS(Core Course) (continued)		
Module-5		**Teaching Hours**
Synchronous Motor Drives (continued): Self-controlled synchronous motor drive employing load commutated thruster inverter, Starting Large Synchronous Machines, Permanent Magnet ac (PMAC) Motor Drives, Sinusoidal PMAC Motor Drives, Brushless dc Motor Drives. **Stepper Motor Drives**: Variable Reluctance, Permanent Magnet, Important Features of Stepper Motors, Torque Versus Stepping rate Characteristics, Drive Circuits for Stepper Motor. **Industrial Drives:** Textile Mills, Steel Rolling Mills, Cranes and Hoists, Machine Tools. ■		**10**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	

Course outcomes:

At the end of the course the student will be able to:

- Explain the advantages and choice of electric drive.
- Explain dynamics and different modes of operation of electric drives.
- Suggest a motor for a drive and control of dc motor using controlled rectifiers.
- Analyze the performance of induction motor drives under different conditions.
- Control induction motor, synchronous motor and stepper motor drives.
- Suggest a suitable electrical drive for specific application in the industry. ■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Modern Tool Usage.

Question paper pattern:

- The question paper will have ten full questions carrying equal marks. Each full question consisting of 16 marks.
- There will be two full questions (with a maximum of four sub questions) from each module.
- Each full question will have sub question covering all the topics under a module.
- The students will have to answer five full questions, selecting one full question from each module.

Textbook

1	Fundamentals of Electrical Drives	Gopal K. Dubey	Narosa Publishing House	2nd Edition, 2001
2	Electrical Drives: Concepts and Applications (Refer to chapter 07 for Industrial Drives under module 5.)	VedumSubrahmanyam	McGraw Hill	2nd Edition, 2011

Reference Books

1	Electric Drives	N.K De,P.K. Sen	PHI Learning	1st Edition, 2009

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER –VIII			
SMART GRID(Professional Elective)			
Subject Code	15EE831	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To define smart grid and discuss the progress made by different stakeholders in the design and development of smart grid.
- To explain the measurement techniques using PMUs and smart meters.
- To discuss tools for the analysis of smart grid and design, operation and performance.
- To discuss incorporating performance tools such as voltage and angle stability and state estimation into smart grid.
- To discuss classical optimization techniques and computational methods for smart grid design, planning and operation.
- To discuss the development of predictive grid management and control technology for enhancing the smart grid performance.
- To discuss development of cleaner, more environmentally responsible technologies for the electric system.
- To discuss the fundamental tools and techniques essential to the design of the smart grid.
- To describe methods to promote smart grid awareness and enhancement.
- To discuss methods to make the existing transmission system smarter by investing in new technology.■

Module-1		Teaching Hours
Smart Grid Architectural Designs: Introduction, Today's Grid versus the Smart Grid, Energy Independence and Security Act of 2007: Rationale for the Smart Grid, Computational Intelligence, Power System Enhancement, Communication and Standards, Environment and Economics, General View of the Smart Grid Market Drivers, Stakeholder Roles and Function, Working Definition of the Smart Grid Based on Performance Measures, Representative Architecture, Functions of Smart Grid Components. **Smart Grid Communications and Measurement Technology:** Communication and Measurement, Monitoring, PMU, Smart Meters, and Measurements Technologies, GIS and Google Mapping Tools, Multiagent Systems (MAS) Technology, Microgrid and Smart Grid Comparison. **Performance Analysis Tools for Smart Grid Design:** Introduction to Load Flow Studies, Challenges to Load Flow in Smart Grid and Weaknesses of the Present Load Flow Methods, Load Flow State of the Art: Classical, Extended Formulations, and Algorithms, Congestion Management Effect, Load Flow for Smart Grid Design, DSOPF Application to the Smart Grid, Static Security Assessment (SSA) and Contingencies, Contingencies and Their Classification, Contingency Studies for the Smart Grid.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Stability Analysis Tools for Smart Grid: Introduction to Stability, Strengths and Weaknesses of Existing Voltage Stability Analysis Tools, Voltage Stability Assessment, Voltage Stability Assessment Techniques, Voltage Stability Indexing, Analysis Techniques for Steady-State Voltage Stability Studies, Application and Implementation Plan of Voltage Stability, Optimizing Stability Constraint through Preventive Control of Voltage Stability, Angle Stability Assessment, State Estimation.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Computational Tools for Smart Grid Design: Introduction to Computational Tools, Decision Support Tools, Optimization Techniques, Classical Optimization Method, Heuristic Optimization, Evolutionary Computational Techniques, Adaptive Dynamic Programming Techniques, Pareto		**08**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER –VIII		
15EE831 SMART GRID(Professional Elective) (continued)		
Module-3 (continued)		**Teaching Hours**
Methods, Hybridizing Optimization Techniques and Applications to the Smart Grid, Computational Challenges. **Pathway for Designing Smart Grid:** Introduction to Smart Grid Pathway Design, Barriers and Solutions to Smart Grid Development, Solution Pathways for Designing Smart Grid Using Advanced Optimization and Control Techniques for Selection Functions, General Level Automation, Bulk Power Systems Automation of the Smart Grid at Transmission Level, Distribution System Automation Requirement of the Power Grid, End User/Appliance Level of the Smart Grid, Applications for Adaptive Control and Optimization.■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Renewable Energy and Storage: Renewable Energy Resources, Sustainable Energy Options for the Smart Grid, Penetration and Variability Issues Associated with Sustainable Energy Technology, Demand Response Issues, Electric Vehicles and Plug-in Hybrids, PHEV Technology, Environmental Implications, Storage Technologies, Tax Credits. **Interoperability, Standards, and Cyber Security:** Introduction, Interoperability, Standards, Smart Grid Cyber Security, Cyber Security and Possible Operation for Improving Methodology for Other Users.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Research, Education, and Training for the Smart Grid: Introduction, Research Areas for Smart Grid Development, Research Activities in the Smart Grid, Multidisciplinary Research Activities, Smart Grid Education, Training and Professional Development. **Case Studies and Test beds for the Smart Grid:** Introduction, Demonstration Projects, Advanced Metering, Microgrid with Renewable Energy, Power System Unit Commitment (UC) Problem,ADP for Optimal Network Reconfiguration in Distribution Automation, Case Study of RER Integration,Testbeds and Benchmark Systems, Challenges of Smart Transmission,Benefits of Smart Transmission.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss the progress made by different stakeholders in the design and development of smart grid.
- Explain measurement techniques using Phasor Measurement Units and smart meters
- Discuss tools for the analysis of smart grid and design, operation and performance
- Discuss classical optimization techniques and computational methods for smart grid design, planning and operation.
- Explain predictive grid management and control technology for enhancing the smart grid performance
- Develop cleaner, more environmentally responsible technologies for the electric system.
- Discuss the computational techniques, communication, measurement, and monitoring technology tools essential to the design of the smart grid.
- Explain methods to promote smart grid awareness and making the existing transmission system smarter by investing in new technology.■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations of complex problems, Modern Tool Usage, The Engineer and Society, , Ethics, Individual and Team Work, Communication, Life-long Learning.

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER –VIII				
15EE831 SMART GRID(Professional Elective) (continued)				
Question paper pattern: • The question paper will have ten questions. • Each full question is for 16 marks. • There will be 2full questions (with a maximum of four sub questions in one full question) from each module. • Each full question with sub questions will cover the contents under a module. • Students will have to answer 5 full questions, selecting one full question from each module.■				
Textbook				
1	Smart Grid, Fundamentals of Design and Analysis	James Momoh	Wiley	1st Edition, 2012

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VIII

OPERATION AND MAINTENANCE OF SOLAR ELECTRICSYSTEMS (Professional Elective)

Subject Code	15EE832	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80

Credits - 03

Course objectives:

- To discuss basics of solar resource data, its acquisition and usage.
- To discuss PV technology, buying the PV modules and connecting the modules to form arrays.
- To discuss inverters, system components, cabling used to connect the components and mounting methods of the PV system.
- To explain site assessment, design process of the grid connected system and its sizing.
- To explain installation, commissioning, operation and maintenance of PV systems.
- To explain the types of financial incentives available, calculation of payback time. ■

Module-1	Teaching Hours
Solar Resource and Radiation: Solar resources, Quantifying solar radiation, The effect of the Earth's atmosphere on solar radiation, Sun geometry, Geometry for installing solar arrays. **PV Industry and Technology:**Semiconductor devices,Mainstream technologies,Monocrystalline silicon,Multicrystalline/polycrystalline silicon,Thin film solar cells,Contacts,Buying solar modules,Standards,Certifications,Warranties,Emerging technologies,Dye-sensitized solar cells,Sliver cells,Heterojunction with intrinsic thin layer (HIT) photovoltaic cells,III-V Semiconductors,Solar concentrators. **PV Cells, Modules and Arrays:**Characteristics of PV cells,Graphic representations of PV cell performance,Connecting PV cells to create a module,Specification sheets,Creating a string of modules,Creating an array,Photovoltaic array performance,Irradiance,Temperature,Shading.■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2	
Inverters and Other System Components: Introduction, Inverters,Battery inverters,Grid-interactive inverters,Transformers,Mainstream inverter technologies,String inverters,Multi-string inverter,Central inverter,Modular inverters,Inverter protection systems,Self-protection,Grid protection,Balance of system equipment: System equipment excluding the PV array and inverter,Cabling,PV combiner box,Module junction box,Circuit breakers and fuses,PV main disconnects/isolators,Lightning and surge protection,System monitoring,Metering,Net metering,Gross metering. Mounting Systems:Roof mounting systems,Pitched roof mounts,Pitched roof mounts for tiled roofs,Pitched roof mounts for metal roofs,Rack mounts,Direct mounts,Building-integrated systems,Ground mounting systems,Ground rack mounts,Pole mounts,Sun-tracking systems,Wind loading,Lightning protection.■	**08**
Revised Bloom's Taxonomy Level: L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3	
Site Assessment: Location of the PV array,Roof specifications,Is the site shade-free?,Solar Pathfinder,SolmetricSuneye,HORIcatcher,iPhone apps,Software packages,Available area,Portrait installation,Landscape installation,Energy efficiency initiatives,Health, safety and environment (HSE) risks,Local environment,Locating balance of system equipment,Site plan. **Designing Grid-connected PV Systems:**Design brief,Existing system evaluation,Choosing system components,Modules,Mounting structure,Inverters,Cabling,Voltage sizing,Current sizing,Monitoring,System protection,Over-current protection,Fault-current protection,Lightning and surge protection,Grounding/earthing,Mechanical protection,Array protection,Sub-array protection,Extra low voltage (ELV) segmentation. **Sizing a PV System:**Introduction, Matching voltage specifications,Calculating maximum voltage,Calculating minimum voltage,Calculating the minimum number of modules in a string,Calculating the maximum voltage,Calculating the maximum number of modules in a string,Calculating the	**08**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) **CHOICE BASED CREDIT SYSTEM (CBCS)** **SEMESTER - VIII**		
15EE832 OPERATION AND MAINTENANCE OF SOLAR ELECTRICSYSTEMS **(Professional Elective)(continued)**		
Module-3 (continued)		**Teaching Hours**
minimum voltage,Calculating the minimum number of modules in a string,Matching current specifications,Matching modules to the inverter's power rating,Losses in utility-interactive PV systems,Temperature of the PV module,Dirt and soiling,Manufacturer's tolerance,Shading,Orientation and module tilt angle,Voltage drop,Inverter efficiency,Calculating system yield.■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-4		
Installing Grid-connected PV Systems:PV array installation, DC wiring, Cabling routes and required lengths,Cable sizing, PV combiner box,System grounding/earthing, Inverter installation, Installation checklist,Interconnection with the utility grid,Required information for installation,Safety. **System Commissioning:**Introduction, Final inspection of system installation,Testing,Commissioning, System documentation. **System Operation and Maintenance:**System maintenance, PV array maintenance, Inverter maintenance, System integrity, Troubleshooting, Identifying the problem, Troubleshooting PV arrays, Troubleshooting underperforming systems,Troubleshooting inverters,Other common problems. ■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Marketing and Economics of Grid-connected PV Systems:Introduction, PV system costing,Valuing a PV system,Simple payback and financial incentives,Simple payback,Feed-in tariffs,Rebates,Tax incentives,Loans,Renewable portfolio standards and renewable energy certificates,Marketing,Insurance. **Case Studies:**Case studies A to G.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Discuss basics of solar resource data, its acquisition and usage.
- Explain PV technology, buying the PV modules and connecting the modules to form arrays.
- Explain the use of inverters, other system components, cabling used to connect the components and mounting methods of the PV system.
- Assess the site for PV system installation.
- Design a grid connected system and compute its size.
- Explain installation, commissioning, operation and maintenance of PV systems.
- Explain the types of financial incentives available, calculation of payback time■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions, Conduct investigations of complex problems, Modern Tool Usage, The Engineer and Society, Environment and Sustainability, Ethics, Individual and Team Work, Communication, Project Management and Finance, Life-long Learning.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) **CHOICE BASED CREDIT SYSTEM (CBCS)** **SEMESTER - VIII**				
15EE832 OPERATION AND MAINTENANCE OF SOLAR ELECTRICSYSTEMS **(Professional Elective)(continued)**				
Textbook				
1	Grid-connected Solar Electric Systems, The Earthscan Expert Handbook for Planning, Design and Installation	Geoff Stapleton and Susan Neill	Earthscan	1st Edition, 2012

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER -VIII

INTEGRATION OF DISTRIBUTED GENERATION(Professional Elective)

Subject Code	15EE833	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80

Credits - 03

Course objectives:

- To explain power generation by alternate energy source like wind power and solar power.
- To explain selection of size of units and location for wind and solar systems.
- Discuss the effects of integration of distributed generation on the performance the system.
- To provide practical and useful information about grid integration of distributed generation.■

Module-1		Teaching Hours
Distributed Generation: Introduction,Sources of Energy - Wind Power, Solar Power, Combined Heat-and-Power, Hydropower, Tidal Power, Wave Power, Geothermal Power, Thermal Power Plants.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
Distributed Generation (continued): Interface with the Grid. **Power System Performance**: Impact of Distributed Generation on the Power System, Aims of the Power System, Hosting Capacity Approach, Power Quality, Voltage Quality and Design of Distributed Generation, Hosting Capacity Approach for Events, Increasing the Hosting Capacity. **Overloading and Losses**: Impact of Distributed Generation, Overloading: Radial Distribution Networks, Overloading: Redundancy and Meshed Operation, Losses.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Overloading and Losses(continued): Increasing the Hosting Capacity. **Voltage Magnitude Variations:** Impact of Distributed Generation, Voltage Margin and Hosting Capacity, Design of Distribution Feeders, A Numerical Approach to Voltage Variations, Tap Changers with Line-Drop Compensation, Probabilistic Methods for Design of Distribution Feeders.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
Voltage Magnitude Variations (continued): Statistical Approach to Hosting Capacity, Increasing the Hosting Capacity. **Power Quality Disturbances:** Impact of Distributed Generation, Fast Voltage Fluctuations, Voltage Unbalance.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Power Quality Disturbances (continued): Low-Frequency Harmonics, High-Frequency Distortion, Voltage Dips, Increasing the Hosting Capacity.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Explain energy generation by wind power and solar power.
- Discuss the variation in production capacity at different timescales, the size of individual units, and the flexibility in choosing locations with respect to of wind and solar systems.

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VIII
15EE833 INTEGRATION OF DISTRIBUTED GENERATION(Professional Elective)(continued)

Course outcomes (continued):

- Explain the performance of the system when distributed generation is integrated to the system.
- Discuss effects of the integration of DG: the increased risk of overload and increased losses.
- Discuss effects of the integration of DG: increased risk of overvoltages, increased levels of power quality disturbances.
- Discuss effects of the integration of DG: incorrect operation of the protection
- Discuss the impact the integration of DG on power system stability and operation. ■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions,Conduct investigations of complex problems, Modern Tool Usage, The Engineer and Society, Ethics, Individual and Team Work, Communication,Project Management and Finance, Life-long Learning.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Integration ofDistributedGeneration in thePower System	Math Bollen	Wiley	2011

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VIII			
POWER SYSTEM IN EMERGENCIES(Professional Elective)			
Subject Code	15EE834	IA Marks	20
Number of Lecture Hours/Week	03	Exam Hours	03
Total Number of Lecture Hours	40	Exam Marks	80
Credits - 03			

Course objectives:

- To discuss the disturbances that may occur in a power system and the impact of them on its viable operation.
- To give the definitions, concepts and standard terminology used in the literature on emergency control and to discuss the effect of system structure on the form of emergency control.
- To discuss the structure, function and alternatives for main transmission.
- To discuss standards of security and quality of supply in planning and operation,timescales and tasks in system operation and control.
- To discuss SCADA facilities - functions, structure, performance criteria, data and human - computer interface.
- To discuss energy management systems, communications, telemetry, telecommand and distributed generation.
- To discuss factors affecting the onset, severity and propagation of a disturbance, measures to minimize the risk.
- To discuss weather related disturbances that can occur in the power systems and aids to the restoration process and problems which hinder restoration.
- To discuss different simulators that can be used in training.
- To discuss facilities and characteristics for emergency control, qualitative and quantitative benefits of emergency control and emergency control in the future. ■

Module-1		**Teaching Hours**
Disturbances in Power Systems and their Effects: Sudden Disturbance, Predictable Disturbances, Forms of System Failure, Analysis Techniques, Trends in the Development of Analytical Techniques. **Some General Aspects of Emergency Control:** Definitions and Concepts used in Emergency Control, Some Standard Terminology, The Effects of Various Types of Fault or Disturbance on System Performance, Typical Pattern of the Development of a Sudden Disturbance, Conceptual Forms of Emergency Control, Effect of System Structure on the Need for and Implementation of Emergency Control, Design Criteria for Emergency Control Facilities.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying.	
Module-2		
The Power System and its Operational and Control Infrastructure: Structure, The Functions of Interconnection, The Alternatives for Main Transmission, Security and Quality of Supply in Planning and Operation, Timescales in System Operation and Control, SCADA, Energy Management Systems, Communications and Telemetry,Telecommand, Distributed Generation, Flexible AC Transmission Systems (FACTS).■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-3		
Measures to Minimize the Impact of Disturbances: Factors in Onset, Severity and Propagation of a Disturbance, Measures in the Planning Timescale to Minimize the Risk of a Disturbance, Measures in the Operational Timescale to Minimize the Risk and Impact of a Disturbance, Special Protection Schemes, Reduction in the Spread of Disturbances, Measures to Minimize the Impact of Predictable Disturbances, An Approach to Managing Resources, The Control Centre.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding, L_3 – Applying, L_4 – Analysing.	
Module-4		
The Natural Environment - Some Disturbances Reviewed: Introduction, Useful Sources of Information, Extreme Environmental Conditions, Noteworthy Disturbances,Incidents.		**08**

B.E ELECTRICAL AND ELECTRONICS ENGINEERING(EEE)
CHOICE BASED CREDIT SYSTEM (CBCS)
SEMESTER - VIII

15EE834 POWER SYSTEM IN EMERGENCIES(Professional Elective) (continued)

Module-4 (continued)		**Teaching Hours**
Restoration: Introduction, The Range of Disturbed System Conditions, Some General Issues in Restoration, Recovery from an Abnormal Operating Situation, Local Islanding or Localized Loss of Demand, The 'Black Start' Situation,Strategies for Restoration of the Whole System, Aides in Restoration Process, Problems Found in Restoration, Analysis, Simulation and Modelling in Blackstart, Restoration from a Foreseen Disturbance. **Training and Simulators for Emergency Control:** Introduction, Training in General, The Need for Operator Training, The Content of Training, Forms of Training, Training Simulators, The Use of Dispatch Training Simulators in Practice.■		
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	
Module-5		
Plant Characteristics and Control Facilities for Emergency Control and Benefits to be Obtained: Introduction, The Characteristics and Facilities Required for Emergency Control, The System and Demand, System Control Costs for Emergencies, Indirect Costs, The Benefits of Emergency Control, Quantitative Aspects,Is Emergency Control Worthwhile? **Systems and Emergency Control in the Future:** Introduction, Changes in Organization, Restructuring, Unbundling and Emergency Control, Facilities for Emergency Control in the Future, Superconductivity, Contingency Planning and Crisis.■		**08**
Revised Bloom's Taxonomy Level	L_1 – Remembering, L_2 – Understanding.	

Course outcomes:

At the end of the course the student will be able to:

- Explain disturbances that may occur in a power system and the impact of them on its operation.
- Give the definitions, concepts and standard terminology used in the literature on emergency control and discuss the effect of system structure on the form of emergency control
- Discuss the structure, function and alternatives for main transmission
- To discuss standards of security and quality of supply in planning and operation,timescales, tasks in system operation and control, SCADA facilities - functions, structure, performance criteria, data and human - computer interface
- To discuss energy management systems, communications, telemetry, telecommand and distributed generation.
- To discuss factors affecting the onset, severity and propagation of a disturbance, measures to minimize the risk
- To discuss weather related disturbances that can occur in the power systems and aids to the restoration process and problems which hinder restoration
- To discuss different simulators used in training, facilities and characteristics for emergency control, and benefits of emergency control and emergency control in the future. ■

Graduate Attributes (As per NBA)

Engineering Knowledge, Problem Analysis, Design/ Development of Solutions,Conduct investigations of complex problems, Modern Tool Usage, The Engineer and Society, Ethics, Individual and Team Work, Communication,Project Management and Finance, Life-long Learning.

Question paper pattern:

- The question paper will have ten questions.
- Each full question is for 16 marks.
- There will be 2full questions (with a maximum of four sub questions in one full question) from each module.
- Each full question with sub questions will cover the contents under a module.
- Students will have to answer 5 full questions, selecting one full question from each module.■

Textbook

1	Power Systems in Emergencies: From Contingency Planning to Crisis Management	U. G. Knight	Wiley	1st Edition, 2001

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VIII			
INTERNSHIP / PROFESSIONAL PRACTICE			
Subject Code	15EE84	IA Marks	50
Number of Practical Hours/Week	--	Exam Hours	--
Total Number of Practical Hours	--	Exam Marks	50
Credits - 02			

Course objectives:

Internship/Professional practice provide students the opportunity of hands-on experience that include personal training, time and stress management, interactive skills, presentations, budgeting, marketing, liability and risk management, paperwork, equipment ordering, maintenance, responding to emergencies etc. The objective are further,

- To put theory into practice.
- To expand thinking and broaden the knowledge and skills acquired through course work in the field.
- To relate to, interact with, and learn from current professionals in the field.
- To gain a greater understanding of the duties and responsibilities of a professional.
- To understand and adhere to professional standards in the field.
- To gain insight to professional communication including meetings, memos, reading, writing, public speaking, research, client interaction, input of ideas, and confidentiality.
- To identify personal strengths and weaknesses.
- To develop the initiative and motivation to be a self-starter and work independently.■

Internship/Professional practice: Students under the guidance ofinternal guide/s and external guide shall take part in all the activities regularly to acquire as much knowledge as possible without causing any inconvenience at the place of internship.

Seminar: Each student, is required to

- Present the seminar on the internship orally and/or through power point slides.
- Answer the queries and involve in debate/discussion.
- Submit the report duly certified by the external guide.

The participants shall take part in discussion to foster friendly and stimulating environment in which the students are motivated to reach high standards and become self-confident. ■

Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating

Course outcomes:

At the end of the course the student will be able to:

- Gain practical experience within industry in which the internship is done.
- Acquire knowledge of the industry in which the internship is done.
- Apply knowledge and skills learned to classroom work.
- Develop a greater understanding about career options while more clearly defining personal career goals.
- Experience the activities and functions of professionals.
- Develop and refine oral and written communication skills.
- Identify areas for future knowledge and skill development.
- Expand intellectual capacity, credibility, judgment, intuition.
- Acquire the knowledge of administration, marketing, finance and economics. ■

Graduate Attributes (As per NBA):

Engineering Knowledge, Problem Analysis, Design / development of solutions, Conduct investigations of complex Problems, Modern Tool Usage, Engineers and society, Environment and sustainability, Ethics, Individual and Team work, Communication.

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VIII
15EE84INTERNSHIP / PROFESSIONAL PRACTICE(continued)
Continuous Internal Evaluation CIE marks for the Internship/Professional practicereport (25 marks)and seminar (25 marks) shall be awarded (based on the quality of report and presentation skill, participation in the question and answer session by the student) by the committee constituted for the purpose by the Head of the Department. The committee shall consist of three faculty from the department with the senior most acting as the Chairman. ■ **Semester End Examination** SEE marks for the project report (25 marks)and seminar (25 marks) shall be awarded (based on the quality of report and presentation skill, participation in the question and answer session) by the examiners appointed by the University.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VIII			
PROJECT WORK PHASE -II			
Subject Code	15EEP85	IA Marks	100
Number of Practical Hours/Week	--	Exam Hours	--
Total Number of Practical Hours	--	Exam Marks	100
Credits - 06			

Course objectives:

- To support independent learning.
- To guide to select and utilize adequate information from varied resources maintaining ethics.
- To guide to organize the work in the appropriate manner and present information (acknowledging the sources) clearly.
- To develop interactive, communication, organisation, time management, and presentation skills.
- To impart flexibility and adaptability.
- To inspire independent and team working.
- To expand intellectual capacity, credibility, judgement, intuition.
- To adhere to punctuality, setting and meeting deadlines.
- To instil responsibilities to oneself and others.
- To train students to present the topic of project work in a seminar without any fear, face audience confidently, enhance communication skill, involve in group discussion to present and exchange ideas. ■

Project Work Phase - II: Each student of the project batch shall involve in carrying out the project work jointly in constant consultation with internal guide, co-guide, and external guide and prepare the project report as per the norms avoiding plagiarism.

Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating

Course outcomes:

At the end of the course the student will be able to:

- Present the project and be able to defend it.
- Make links across different areas of knowledge and to generate, develop and evaluate ideas and information so as to apply these skills to the project task.
- Habituated to critical thinking and use problem solving skills
- Communicate effectively and to present ideas clearly and coherently in both the written and oral forms.
- Work in a team to achieve common goal.
- Learn on their own, reflect on their learning and take appropriate actions to improve it. ■

Graduate Attributes (As per NBA):
Engineering Knowledge, Problem Analysis, Design / development of solutions, Conduct investigations of complex Problems, Modern Tool Usage, Engineers and society, Environment and sustainability, Ethics, Individual and Team work, Communication.

Evaluation Procedure:
The Internal marks evaluation shall be based on project report and presentation of the same in a seminar.
Project Report:50 marks. The basis for awarding the marks shall be the involvement of individual student of the project batch in carrying the project and preparation of project report. To be awarded by the internal guide in consultation with external guide if any.
Project Presentation:50 marks. Each student of the project batch shall present the topic of Project Work Phase -II orally and/or through power point slides.
The Project Presentation marks of the Project Work Phase -II shall be awarded by the committee constituted for the purpose by the Head of the Department. The committee shall consist of three faculty from the department with the senior most acting as the Chairman.
The student shall be evaluated based on:
Presentation skill for 30 marks and ability in the Question and Answer session for 20 marks.■

Semester End Examination

SEE marks for the project (100 marks)shall be awarded (based on the quality of report and presentation skill, participation in the question and answer session) as per the University norms by the examiners appointed VTU.■

B.E ELECTRICAL AND ELECTRONICS ENGINEERING (EEE) CHOICE BASED CREDIT SYSTEM (CBCS) SEMESTER - VIII			
SEMINAR			
Subject Code	15EES86	IA Marks	100
Number of Practical Hours/Week	--	Exam Hours	--
Total Number of Practical Hours	--	Exam Marks	--
Credits - 01			

Course objectives:

The objective of the seminar is to inculcate self-learning, face audience confidently, enhance communication skill, involve in group discussion and present and exchange ideas.
Each student, under the guidance of a Faculty, is required to
Choose, preferably, a recent topic of his/her interest relevant to the Course of Specialization.

- Carryout literature survey, organize the Course topics in a systematic order.
- Prepare the report with own sentences.
- Type the matter to acquaint with the use of Micro-soft equation and drawing tools or any such facilities.
- Present the seminar topic orally and/or through power point slides.
- Answer the queries and involve in debate/discussion.
- Submit typed report with a list of references.

The participants shall take part in discussion to foster friendly and stimulating environment in which the students are motivated to reach high standards and become self-confident. ■

Revised Bloom's Taxonomy Level	L_3 – Applying, L_4 – Analysing, L_5 – Evaluating, L_6 – Creating

Course outcomes:

At the end of the course the student will be able to:

- Attain, use and develop knowledge in the field of electrical and electronics engineering and other disciplines through independent learning and collaborative study.
- Identify, understand and discuss current, real-time issues
- Improve oral and written communication skills
- Explore an appreciation of the self in relation to its larger diverse social and academic contexts.
- Apply principles of ethics and respect in interaction with others.■

Graduate Attributes (As per NBA):
Engineering Knowledge, Problem Analysis, Design / development of solutions, Conduct investigations of complex Problems, Modern Tool Usage, Engineers and society, Environment and sustainability, Ethics, Individual and Team work, Communication.

Evaluation Procedure:
The CIE marks for the seminar shall be awarded (based on the relevance of the topic, presentation skill, participation in the question and answer session and quality of report) by the committee constituted for the purpose by the Head of the Department. The committee shall consist of three faculties from the department with the senior most acting as the Chairman.
Marks distribution for internal assessment of the course 15EES86 seminar:
Seminar Report: 30 marks
Presentation skill:50 marks
Question and Answer:20 marks.■

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering/ B.E. Information Science & Engineering

III SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15MAT31	Engineering Mathematics - III	04	--	03	80	20	100	4
2	15CS32	Analog and Digital Electronics	04	--	03	80	20	100	4
3	15CS33	Data Structures and Applications	04	--	03	80	20	100	4
4	15CS34	Computer Organization	04	--	03	80	20	100	4
5	15CS35	Unix and Shell Programming	04	--	03	80	20	100	4
6	15CS36	Discrete Mathematical Structures	04	--	03	80	20	100	4
7	15CSL37	Analog and Digital Electronics Laboratory	--	1I+2P	03	80	20	100	2
8	15CSL38	Data Structures Laboratory	--	1I+2P	03	80	20	100	2
		TOTAL	24	6	24	640	160	800	28

Note: 'I' Stands for Instruction Hours and 'P' for practical Hours

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering/ B.E. Information Science & Engineering

IV SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	15MAT41	**Engineering Mathematics - IV**	04	--	03	80	20	100	4
2	15CS 42	**Software Engineering**	04	--	03	80	20	100	4
3	15CS43	**Design and Analysis of Algorithms**	04	--	03	80	20	100	4
4	15CS 44	**Microprocessors and Microcontrollers**	04	--	03	80	20	100	4
5	15CS45	**Object Oriented Concepts**	04	--	03	80	20	100	4
6	15CS46	**Data Communication**	04	--	03	80	20	100	4
7	15CSL47	**Design and Analysis of Algorithm Laboratory**	--	1I+2P	03	80	20	100	2
8	15CSL48	**Microprocessors Laboratory**	--	1I+2P	03	80	20	100	2
TOTAL			**24**	**06**	**24**	**640**	**160**	**800**	**28**

Note: 'I' Stands for Instruction Hours and 'P' for practical Hours

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering

V SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	**15CS51**	Management and Entrepreneurship for IT Industry	04	--	03	80	20	100	4
2	**15CS52**	Computer Networks	04	--	03	80	20	100	4
3	**15CS53**	Database Management System	04	--	03	80	20	100	4
4	**15CS54**	Automata theory and Computability	04	--	03	80	20	100	4
5	**15CS55x**	Professional Elective 1	03	--	03	80	20	100	3
6	**15CS56x**	Open Elective 1	03	--	03	80	20	100	3
7	**15CSL57**	Computer Network Laboratory	--	1I+2P	03	80	20	100	2
8	**15CSL58**	DBMS Laboratory with mini project	--	1I+2P	03	80	20	100	2
TOTAL			**22**	**6**	**24**	**640**	**160**	**800**	**26**

Professional Elective 1	
15CS551	Object Oriented Modeling and Design
15CS552	Introduction to Software Testing
15CS553	Advanced JAVA and J2EE
15CS554	Advanced Algorithms

1. **Professional Elective: Electives relevant to chosen specialization / branch**
2. **Open Elective: Electives from other technical and/or emerging subject areas (Announced separately)**

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016
B.E. Computer Science & Engineering

VI SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	Theory/ Practical Marks	I.A. Marks	Total Marks	
1	**15CS61**	Cryptography, Network Security and Cyber Law	04	--	03	80	20	100	4
2	**15CS62**	Computer Graphics and Visualization	04	--	03	80	20	100	4
3	**15CS63**	System Software and Compiler Design	04	--	03	80	20	100	4
4	**15CS64**	Operating Systems	04	--	03	80	20	100	4
5	**15CS65x**	Professional Elective 2	03	--	03	80	20	100	3
6	**15CS66x**	Open Elective 2	03	--	03	80	20	100	3
7	**15CSL67**	System Software and Operating System Laboratory	--	1I+2P	03	80	20	100	2
8	**15CSL68**	Computer Graphics Laboratory with mini project	--	1I+2P	03	80	20	100	2
		TOTAL	**22**	**6**	**24**	**640**	**160**	**800**	**26**

Professional Elective 2	
15CS651	Data Mining and Data Warehousing
15CS652	Software Architecture and Design Patterns
15CS653	Operations research
15CS654	Distributed Computing system

1. **Professional Elective: Electives relevant to choosen specialization / branch**
2. **Open Elective: Electives from other technical and/or emerging subject areas (Announced separately)**

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering

VII SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15CS71	Web Technology and its applications	04	--	03	20	80	100	4
2	15CS72	Advanced Computer Architectures	04	--	03	20	80	100	4
3	15CS73	Machine Learning	04	--	03	20	80	100	4
4	15CS74x	Professional Elective 3	03	--	03	20	80	100	3
5	15CS75x	Professional Elective 4	03	--	03	20	80	100	3
6	15CSL76	Machine Learning Laboratory	--	1I+2P	03	20	80	100	2
7	15CSL77	Web Technology Laboratory with mini project	--	1I+2P	03	20	80	100	2
8	15CSP78	Project Phase 1 + Seminar	--	--	--	100	--	100	2
TOTAL			18	6	21	240	560	800	24

Professional Elective 3		Professional Elective 4	
15CS741	Natural Language Processing	15CS751	Soft and Evolutionary Computing
15CS742	Cloud Computing and its Applications	15CS752	Computer Vision and Robotics
15CS743	Information and Network Security	15CS753	Digital Image Processing
15CS744	Unix System Programming	15CS754	Storage Area Networks

1. Professional Elective: Electives relevant to choosen specialization / branch
2. Project Phase 1 + Seminar : Literature Survey, Problem Identification, Objectives and Methodology, Submission of Synopsis and Seminar

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2015-2016

B.E. Computer Science & Engineering

VIII SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credits
			Theory	Practical/ Drawing	Duration	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	15CS81	Internet of Things and Applications	4	--	3	20	80	100	4
2	15CS82	Big Data Analytics	4	--	3	20	80	100	4
3	15CS83x	Professional Elective 5	3	--	3	20	80	100	3
4	15CS84	Internship / Professional Practice	Industry Oriented		3	50	50	100	2
5	15CSP85	Project work phase II	--	6	3	100	100	200	5
6	15CSS86	Seminar	--	4	--	100	--	100	2
		TOTAL	11	10	15	310	390	700	20

Professional Elective 5	
15CS831	High Performance Computing
15CS832	User Interface Design
15CS833	Network management
15CS834	System Modeling and Simulation

1. Professional Elective: Electives relevant to chosen specialization / branch
2. Internship / Professional Practice: To be carried out between 6th and 7th semester vacation or 7th and 8th semester vacation period

Visvesvaraya Technological University, Belagavi

REGULATIONS GOVERNING

THE DEGREE OF BACHELOR OF ENGINEERING/ TECHNOLOGY (B.E/B.Tech)

UNDER CHOICE BASED CREDIT SYSTEM (CBCS)

Effective from the academic year 2017 – 18

AUGUST2017

Visvesvaraya Technological University, Belagavi

REGULATIONS GOVERNING
THE DEGREE OF BACHELOR OF ENGINEERING/ TECHNOLOGY (B.E/B.Tech)
UNDER CHOICE BASED CREDIT SYSTEM (CBCS)
Effective from the academic year 2017 – 18

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Visvesvaraya Technological University, Belagavi

REGULATIONS GOVERNING
THE DEGREE OF BACHELOR OF ENGINEERING/ TECHNOLOGY (B.E/B.Tech)
UNDER CHOICE BASED CREDIT SYSTEM (CBCS)
Effective from the academic year 2017 – 18

Definitions of Keywords

The following are the definitions/descriptions that have been followed for the different terms used in the Regulations of B.E/B.Tech. Programmes:

1) **Programme:** Is an educational programme in a particular stream/ branch of Engineering/branch of specialization leading to award of Degree. It involves events/activities, comprising of lectures/ tutorials/ laboratory work/ field work, outreach activities/ project work/ vocational training/ viva/ seminars/ Internship/ assignments/ presentations/ self-study etc., or a combination of some of these.
2) **Branch:** Means Specialization or discipline of B.E/B.Tech. Degree Programme, like Civil Engineering, Mechanical Engineering, Textile Engineering, etc.
3) **Semester:** Refers to one of the two sessions of an academic year (vide: serial number 4), each session being of sixteen weeks duration (with working days greater than or equal to ninety).The odd semester may be scheduled from August and even semester from February of the year.
4) **Academic Year:** Refers to the sessions of two consecutive semesters (odd followed by an even) including periods of vacation.
5) **Course:** Refers to usually referred to as 'papers' and is a component of a programme. All Courses need not carry the same weight. The Courses should define learning objectives and learning outcomes. A Course may be designed to comprise lectures/ tutorials/ laboratory work/ field work/ outreach activities/project work/ vocational training/ viva/ seminars/ term papers/assignments/ presentations/ self-study etc., or a combination of some of these.
6) **Credit:** Referstoa unit by which the Course work is measured. It determines the number of hours of instructions required per week. One credit is equivalent to one hour of lecture or two hours of laboratory/practical Courses/ tutorials/ fieldwork per week etc.
7) **Audit Courses:** Means Knowledge/ Skill enhancing Courses without the benefit of a grade or credit for a Course.
8) **Choice Based Credit System (CBCS):** Refers to customizing the Course work, through Core, Elective and soft skill Courses, to provide necessary support for the students to achieve their goals.
9) **Course Registration:** Refers to formal registration for the Courses of a semester (Credits) by every student under the supervision of a Faculty Advisor (also called Mentor, Counselor etc.,) in each Semester for the Institution to maintain proper record.
10) **Course Evaluation:** Means Continuous Internal Evaluation (CIE) and Semester End Examinations (SEE) to constitute the major evaluations prescribed for each Course. SEE and CIE to carry 60% and 40% respectively, to enable each Course to be evaluated for 100 marks, irrespective of its Credits.
11) **Continuous Internal Evaluation (CIE):** Refers to evaluation of students' achievement in the learning process. CIE shall be by the Course Instructor and includes tests, homework, problem solving, group discussion, quiz, mini-project and seminar throughout the Semester, with weightage for the different components being fixed at the University level.

12) **Semester end examinations (SEE):** Refers to examination conducted at the University level covering the entire Course Syllabus. For this purpose, Syllabi to be modularized and SEE questions to be set from each module, with a choice confined to the concerned module only. SEE is also termed as university examination.

13) **First Attempt:** Refers to a student who has completed all formalities and has become eligible to attend the SEE and has attended at least one head of passing, such attempt shall be considered as first attempt.

14) **Credit Based System (CBS):** Refers to quantification of Course work, after a student completes teaching – learning process, followed by passing in both CIE and SEE. Under CBS, the requirement for awarding degree is prescribed in terms of total number of credits to be earned by the students.

15) **Credit Representation:** Refers to Credit Values for different academic activities considered, as per the Table.1. Credits for seminar, project phases, project viva–voce and internship shall be as specified in the Scheme of Teaching and Examination (Annexure -1).

Table 1: Credit Values

Theory/Lectures (L) (hours/week/Semester)	Tutorials (T) (hours/week/Semester)	Laboratory/Practical (P) (hours/week/Semester)	Credits (L:T:P)	Total Credits
4	0	0	4:0:0	4
3	0	0	3:0:0	3
2	2	0	2:1:0	3
2	0	2	2:0:1	3
2	2	2	2:1:1	4
0	0	6	0:0:3	3

NOTE: Activities like, practical training, study tour and participation in Guest lectures not to carry Credits.

16) **Letter Grade:** It is an index of the performance of students in a said Course. Grades are denoted by letters S, A, B, C, D, E and F.

17) **Grading:** Grade refers to qualitative measure of achievement of a student in each Course, based on the percentage of marks secured in (CIE plus SEE). Grading is done by Absolute Grading [Refer: 17OB6.0]. The rubric attached to letter grades are as follows:
S – Outstanding, A – Excellent, B – Very Good, C – Good, D – Above Average, E – Average and F – Fail.

18) **Grade Point (GP):** Refers to a numerical weightage allotted to each letter grade on a 10-point scale as under.

Letter Grade and corresponding Grade Points on a typical 10 – Point scale

Letter Grade	S	A	B	C	D	E	F
Grade Point	10	09	08	07	06	04	00

19) **Passing Standards:** Refers to passing a Course only when getting GP greater than or equal to 04 (as per serial number 18).

20) **Credit Point:** Is the product of grade point (GP) and number of credits for a Course i.e.,
Credit Point (CrP) = GP $\times$Credits for the Course

21) **Semester Grade Point Average (SGPA):** Refers to a measure of academic performance of student/s in a semester. It is the ratio of total credit points secured by a student in various Courses of a semester and the total Course credits taken during that semester. [Refer:17OB6.0]

22) **Cumulative Grade Point Average (CGPA):** Is a measure of overall cumulative performance of a student over all semesters. The CGPA is the ratio of total credit points earned by a student in various Courses in all semesters and the sum of the total credits of all Courses in all the semesters. It is expressed up to two decimal places. [Refer: 17OB6.0]

23) **Transcript or Grade Card or Certificate:** Refers to a certificate showing the grades earned by a student. A grade certificate shall be issued to all the registered students after every semester. The grade certificate will display the programme details (Course code, title, number of credits, grades secured) along with SGPA of that semester and CGPA earned till that semester.

24) **University:** Visvesvaraya Technological University (VTU), Belagavi.

Visvesvaraya Technological University, Belagavi
Regulations Governing the Degree of Bachelor of Engineering/ Technology (B.E/B.Tech.)
Under Choice Based Credit System (CBCS)
(With effective from the academic year 2017 – 18)

17OB 1.0	**Title, Duration and Credits of the Programme of Study**
17OB 1.1	The programme of study shall be called the degree of Bachelor of Engineering (Subject of Specialization) /Bachelor of Technology (Subject of Specialization), abbreviated as B.E. / B.Tech. (Subject of Specialization).
17 OB1.2	The program to which students are admitted to I semester of the programme shall be of four academic year duration divided into eight semesters and each semester is of 16 weeks duration. The programme to which students are admitted to III semester of the programme under lateral entry shall be of three academic year duration divided into six semesters and each semester is of 16 weeks duration. The programme (conducted during evening) to which students are admitted to III semester of the programme under lateral entry shall be of three academic year duration divided into six semesters and each semester is of 16 weeks duration. The deficit contact hours of the programme, conducted during evening on all working days, shall be compensated on all Sundays (except on general holidays).
17 OB 1.3	The calendar of events in respect of the program of study shall be notified by the University in advance.
17 OB 1.4	The University examination in all programs of study shall be conducted at the end of each semester for all the eight semesters.
17 OB 1.5	**Maximum Duration for Programme Completion:** a) i. Students admitted to I year B.E/ B.Tech shall complete the programme within a period of eight academic years from the date of first admission, failing which they have to discontinue the Course. ii. Students admitted II Year B.E./B.Tech. under lateral entry scheme shall complete the Programme within a period of six academic years from the date of first admission, failing which he/she has to discontinue the Course. b) i. A student who has not obtained the eligibility for III semester even after three academic years from the date of admission to I semester shall discontinue the Programme or get readmitted to I semester of first year B.E./B.Tech. with a new University Seat Number but retaining the same year of admission. ii. A student (under lateral entry scheme) who has not obtained the eligibility for V semester even after three academic years from the date of admission to III semester shall discontinue the Programme or get readmitted to III semester of II year B.E./B.Tech. with a new University Seat Number but retaining the same year of admission.
17 OB 1.6	Prescribed Number of Credits for the Programme: (a) The number of credits to be completed by students admitted I semester of B.E./B.Tech. programme shall be 200 **(b)** The number of credits to be completed by students admitted to III semester of B.E./B.Tech. programme under lateral entry scheme shall be 152

17 OB2.0	**Eligibility for Admission(As per the Government orders issued from time to time)**
17 OB2.1	Admission to I year/ I semester Bachelor Degree in Engineering/ shall be open to the students who have passed the II PUC/ XII Standard/ Equivalent Examination with English as one of the Languages and obtained a Minimum of 45% of Marks in aggregate in Physics and Mathematics along with Chemistry / Bio-Technology / Biology / Electronics / Computer. In case of SC/ST, Category -1 and OBC (2A, 2B, 3A and 3B) category students from Karnataka (Karnataka candidates) the minimum marks for eligibility shall be 40 %. With regard to the qualification earned from foreign countries, Equivalence certificate from the Association of Indian Universities is Mandatory for admission to B.E./B.Tech. programme. In case of any dispute about the equivalence in qualification earned from foreign countries, the decision of the Equivalence committee shall be the final in establishing the eligibility of the student. Admission to II year/ III semester Bachelor Degree in Engineering/ Technology (Lateral Entry) shall be open to the Diploma holders and B.Sc. graduates.
17 OB2.2	**(i) Diploma Holders** (a) Must have passed diploma or equivalent examination as recognized by University and secured not less than forty five percentage (45%) marks in the final year examination (fifth and sixth semesters) in the appropriate branch of engineering. In case of SC/ST and OBC students from Karnataka the minimum marks for eligibility shall be forty percent (40%). (b) Those candidates who have completed Diploma from other than Karnataka state shall provide the Equivalence/ Eligibility Certificate from the Director of Technical Education, Karnataka. **(ii) B.Sc. Graduates** Must have passed B.Sc. degree from a recognized University under the UGC or equivalent qualification as recognized by University and secured not less than forty five percentage (45%) marks in aggregate (considering the marks of all six semesters). In case of SC/ST and OBC students from Karnataka (Karnataka candidates) the minimum marks for eligibility shall be forty percent (40%). Candidates must have studied Mathematics as subject of study at XII Standard.
	(i) Diploma Holders for the programme conducted during evening A candidate who has passed diploma examination or equivalent examination and obtained an aggregate minimum of 45 % marks taken together in all the subjects of the final year (fifth and sixth semesters) diploma exanimation is eligible to B.E Courses, and 40 % of marks in case of SC/ST and backward classes of Karnataka candidates. In addition to this a candidate after passing the diploma, must have minimum of two years full time professional experience as on first September of the year of admission, in a registered firm/company/industry/ educational / Government / Autonomous organizations in the branch of Engineering/ Technology, in which the candidates hold a diploma, and in which admission is sought by him/her.

17 OB2.2 (continued)	Further that employment shall be in an establishment situated within the 15 km from the place of the institution to which the candidate is seeking admission. Professional experience refers to the experience earned as an employee on regular basis in, (a) Government, Government Undertaking, Public Sector Undertaking, Corporation or, (b) In a private company registered under the Directorate of Industries and Commerce or the Directorate of Small Scale Industries or, (c) Government, Government recognized Institutions as technical staff. Provided that the period of apprenticeship undergone shall also be treated as professional experience, if sponsored by the Board of Apprenticeship Training, Southern Region, Chennai or by Government, Government undertakings and Public Sector undertakings. Further, those candidates who have completed Diploma from other than Karnataka state shall provide the Equivalence/ Eligibility Certificate from the Director of Technical Education, Karnataka.
17 OB2.3	Those students, who have passed a qualifying examination other than the PUC II examination of the Pre-University Education Board of Karnataka, have to obtain eligibility certificate for seeking admission to B.E./B.Tech. Degree Programme from Visvesvaraya Technological University, Belagavi.
17 OB3.0	**Courses**
17 OB3.1	There shall be the following types of Courses: a) Humanities and Social Sciences (HSS) including Management. These are mandatory for all disciplines. b) Basic Sciences (BS): Physics, Chemistry and Mathematics. These are mandatory for all disciplines. c) Engineering Sciences (ES): Materials, Workshop, Drawing, and Basics of Electrical/ Electronics/ Instrumentation/ Civil/ Mechanical/ Computer Engineering. These are mandatory for all disciplines. d) Professional Subjects (PS) - Core: Are the professional Core (PC) Courses, relevant to the chosen specialization/ branch. The core Courses are to be compulsorily studied by a student and are mandatory to complete them to fulfill the requirements of a programme. e) Professional Subjects (PS) - Elective: Are the professional Electives (PE), relevant to the chosen specialization/ branch and can be chosen from the pool of papers. It shall be supportive to the discipline providing extended scope/enabling an exposure to some other discipline /domain and nurturing student proficiency skills. f) Open Subjects - Electives (OE): Are from other technical areas and/ or from emerging fields. g) Mini project and Main Project: Carried out at the Institution or at an Industry. h) Seminar: Deliverable at the Institution under the supervision of a Faculty. i) Internship: Preferably at an industry/R and D organization/IT company/ Government organization of significant repute for a specified period mentioned in the Scheme of Teaching and Examination. j) Mandatory Courses (MC): These Courses are mandatory, without the benefit of a grade or credit, for students admitted to B.E./B.Tech. program. A pass in each mandatory Course is required to qualify for Degree award from the University.

17 OB3.1 (continued)	k) Audit Courses (AC): Knowledge/ skill enhancement Courses without the benefit of a grade or credit for a Course. i) The Audit Course/s (other than the Course/s considered for completing the prescribed program credits) can be any Course offered by the program to which the student is admitted to other programs offered in the institution, where the student is studying. ii) The students who are interested in audit Courses can register for one audit Course at a time during III to VIII semesters. Students, who have opted for audit Courses and considered on par with students registered for credit Courses, have to satisfy the attendance and CIE requirements. However, they need not have to appear for SEE. The number of registrations to an audit Course is restricted to 10 % of the AICTE intake. iii) Registration for any audit Course, in writing, shall be completed at the beginning of semesters. The Institution should intimate the Registrar (Evaluation) about the registration at the beginning of the semester and obtain a formal approval for inclusion of the audit Course/s in the Grade cards/ Transcripts issued to the students.
17 OB3.2	The minimum number of students registered to any Elective offered by the Departments shall be not less than ten.
17 OB3.3	A student shall exercise his option in respect of Elective Courses and register for the same at the beginning of the concerned semester. The student may be permitted to opt for a change of Elective Course within 15 days from the date of commencement of the semester as per the calendar of the University.
17 OB3.4	**Course Registration:** Every student shall register for the Courses of a semester (Credits) under the supervision of a Faculty Advisor (also called Mentor, Counselor etc.,) in each Semester for the Institution to maintain proper record.
17OB4.0	**Internship/Professional Practice**
17OB4.1	**Internship / Professional Practice:** 1) The Internship shall be completed during the period specified in the Scheme of Teaching and Examination. 2) The internship can be carried out in any industry/R and D Organization/Research Institute/ Educational institute of repute. 3) (a) The Department/college shall nominate staff member/s to facilitate, guide and supervise students under internship. (b) The Internal Guide has to visit place of internship at least once during the student's internship. 4) The students shall report the progress of the internship to the guide in regular intervals and seek his/her advice. 5) After the completion of Internship, students shall submit a report with completion and attendance certificates to the Head of the Department with the approval of both internal and external guides. 6) There will be 50 marks for CIE (Seminar: 25, Internship report: 25) and 50 marks for Viva – Voce conducted during SEE. The minimum requirement of CIE marks shall be 50% of the maximum marks. [To be read along with 17 OB 8.6] 7) The internal guide shall award the marks for seminar and internship report after evaluation. He/she will also be the internal examiner for Viva – Voce conducted during SEE.

8) The external guide from the industry shall be an examiner for the viva voce on Internship. Viva-Voce on internship shall be conducted at the college and the date of Viva-Voce shall be fixed in consultation with the external Guide. The Examiners shall jointly award the Viva - Voce marks.
9) In case the external Guide expresses his inability to conduct viva voce, the Chief Superintendent of the institution shall appoint a senior faculty of the Department to conduct viva-voce along with the internal guide. The same shall be informed in writing to the concerned Chairperson, Board of Examiners (BOE).
10) The students are permitted to carry out the internship anywhere in India or abroad. The University will not provide any kind of financial assistance to any student for carrying out the Internship.

17OB4.2 **Failing to undergo Internship:** Internship is one of the head of passing. Completion of internship is mandatory. If any student fails to undergo /complete the internship, he/she shall be considered as failed in that Course and shall not be permitted to appear for SEE in that Course. However, student shall appear for SEE after satisfying the conditions prescribed for Internship. The reappearance shall be considered as an attempt.

17OB5.0 **Seminar and Project**

17OB5.1 **Seminar:** Seminar is one of the head of passing.

i) Each candidate shall deliver seminar as per the Scheme of Teaching and Examination on the topics chosen from the relevant fields for about 30 minutes.
ii) The Head of the Department shall make arrangements for conducting seminars through concerned faculty members of the Department. The committee constituted for the purpose by the Head of the Department shall award the CIE marks for the seminar. The committee shall consist of three faculty from the Department and the senior most acting as the Chairman/Chairperson. [To be read along with 17 OB 8.6].

17OB5.2 **Project Work:** Project is one of the head of passing.

Project work shall preferably be batch wise, the strength of each batch shall not exceed maximum of four students.

17OB5.3 Viva-voce examination in project work shall be conducted batch-wise.

17OB 6.0 **Computation of SGPA and CGPA**

17OB 6.1 (i) The University adopts absolute grading system wherein the marks are converted to grades, and every semester results will be declared with semester grade point average (SGPA) and Cumulative Grade Point Average (CGPA). The CGPA will be calculated for every semester, except for the first semester.

(ii) The grading system with the letter grades and the assigned range of marks under absolute grading system are as given below:

Level	**Outstanding**	**Excellent**	**Very Good**	**Good**	**Above Average**	**Average**	**Fail**
Letter Grade	S	A	B	C	D	E	F
Grade Points	10	9	8	7	6	4	00
Percentage of Marks Scored in a Course	$\geq$ 90	<90 $\geq$80	< 80 $\geq$70	< 70 $\geq$60	< 60 $\geq$ 45	< 45 $\geq$40	< 40
	(90 -100)	(80 - 89)	(70 - 79)	(60 - 69)	(45 - 59)	(40 - 44)	(0 - 39)

	(iii) A student obtaining Grade 'F' in a Course shall be considered failed and is required to reappear in subsequent SEE. Whatever the letter grade secured by the student during his / her reappearance shall be retained. However the number of attempts taken to clear a Course shall be indicated in the grade cards/ transcripts.
17OB 6.2	**Computation of SGPA and CGPA (as per UGC Guidelines)** The following procedures shall be used to compute the Semester Grade Point Average (SGPA) and Cumulative Grade Point Average (CGPA) respectively: i) The SGPA is the ratio of sum of the product of the number of credits with the grade points secured by a student in all the Courses taken by him/her and the sum of the number of credits of all the Courses undergone by a student, i.e., $SGPA = \frac{\sum (C_i \times G_i)}{\sum C_i}$ Where C_i is the number of credits of the i^{th} Course and G_i is the grade point scored by the student in the i^{th} Course. ii) The CGPA is also calculated in the same manner taking into account all the Courses undergone by a student over all the semesters of a programme, i.e., $CGPA = \frac{\sum (C_i \times S_i)}{\sum C_i}$ Where S_i is the SGPA of the i^{th} semester and C_i is the total number of credits in that semester. The SGPA and CGPA shall be rounded off to 2 decimal places and reported in the transcripts.

17OB 6.2 (continued)

Illustration for Computation of SGPA and CGPA

Computation of SGPA

Illustration No.1

Course	Credit	Grade letter	Grade point	Credit Point = (Credit × Grade)
Course 1	4	B	08	4 × 08 = 32
Course 2	4	D	06	4 × 06 = 24
Course 3	4	C	07	4 × 07 = 28
Course 4	3	S	10	3 ×10 = 30
Course 5	3	E	04	3 × 04 = 12
Course 6	3	D	06	3 ×06 = 18
Course 7	2	A	09	2 × 09 = 18
Course 8	2	D	06	2 × 06 = 12
Total	25	--	--	174

Thus, **SGPA= 174/25 = 6.96**

Illustration No.2

Course	Credit	Grade letter	Grade point	Credit Point = (Credit × Grade)
Course 1	4	B	08	4 × 08 = 32
Course 2	4	D	06	4 × 06 = 24
Course 3	4	C	07	4 × 07 = 28
Course 4	3	S	10	3 × 10 = 30
Course 5	3	F	00	3 × 00 = 00
Course 6	3	D	06	3 × 06 = 18
Course 7	2	A	09	2 × 09 = 18
Course 8	2	D	06	2 ×06 = 12
Total	25	--	--	162

Thus, **SGPA= 162/25=6.48**

If a Student secures letter grade C during reappearance then the SGPA is Calculated as shown below.

Illustration No. 2(a)

Course	Credit	Grade letter	Grade point	Credit Point = (Credit × Grade)
Course 5	3	C	07	7× 03 = 21

Total Credit Points = Credit Points of first Attempt) + Credit Points of subsequent attempt =162 + 21 = 183

Total credits of the semester = 25

Thus, **SGPA= 183/25=7.32**

Illustration No.3

Course	Credit	Grade letter	Grade point	Credit Point = (Credit x Grade)
Course 1	4	B	08	4 x 08 = 32
Course 2	4	D	06	4 x 06 = 24
Course 3	4	C	07	4 x 07 = 28
Course 4	3	S	10	3 x10 = 30
Course 5	3	A	09	3 x 04 = 18
Course 6	3	D	06	3 x 06 = 18
Course 7	2	A	09	2 x 09 = 18
Course 8	2	D	06	2 x 06 = 12
Total	25	--	--	189

Thus, **SGPA= 189/25=7.56**

$$CGPA\ (from\ illustratiions\ 2\ and\ 3) = \frac{25 \times 7.32 + 25 \times 7.56}{50} = 7.44$$

<table>
<tr><td>17OB 6.2
(continued)</td><td>
<table>
<tr><th>Semester</th><th>I</th><th>II</th><th>III</th><th>IV</th><th>V</th><th>VI</th><th>VII</th><th>VIII</th></tr>
<tr><td>Credits of the semester</td><td>24</td><td>24</td><td>27</td><td>27</td><td>24</td><td>24</td><td>24</td><td>26</td></tr>
<tr><td>SGPA</td><td>7.00</td><td>8.50</td><td>9.20</td><td>6.86</td><td>8.18</td><td>7.73</td><td>8.68</td><td>9.40</td></tr>
<tr><td colspan="9"></td></tr>
</table>
Thus CGPA
$$= \frac{(24 \times 7.00 + 24 \times 8.50 + 27 \times 9.20 + 27 \times 6.86 + 24 \times 8.18 - 24 \times 7.73 + 24 \times 8.68 + 26 \times 9.40)}{200} = 8.20$$
</td></tr>
<tr><td>17OB 6.3</td><td>Transcript Format: Based on the secured letter grades, grade points, SGPA and CGPA, the transcript for each semester and a consolidated transcript indicating the performance in all semesters shall be issued.</td></tr>
<tr><td>17OB 7.0</td><td>Conversions of Grades into Percentage and Declaration of Class</td></tr>
<tr><td>17OB 7.1</td><td>Conversion Formula For The Conversion Of CGPA Into Percentage Is Given Below
Percentage Of Marks Secured, P = [CGPA Earned - 0.75] × 10
Illustration For A CGPA Of 8.20:
P = [CGPA Earned 8.2 - 0.75]× 10 = 74.5 %</td></tr>
<tr><td>17OB 7.2</td><td>Class Declaration:
After the conversion of final CGPA into percentage of marks (P), a graduating student is declared to have passed in
(i) First Class with Distinction (FCD) if $P \geq 70\%$
(ii) First Class (FC) if $P \geq 60\%$ but $<70\%$ and
(iii) Second Class (SC) if $P < 60\%$.</td></tr>
<tr><td>17OB8.0</td><td>Continuous Internal Evaluation</td></tr>
<tr><td>17OB8.1</td><td>For each theory and practical paper, the CIE marks shall be 40.
For Technical seminar, the CIE marks shall be 100.
For Internship/ Professional Practice, the CIE marks shall be 50.
For Project Phase –I and Project seminar and Project Phase –II, the CIE shall be 100 respectively.</td></tr>
<tr><td rowspan="2">17OB8.2</td><td>CIE Marks in each theory Course shall be the sum of marks prescribed for test and assignment.
Marks prescribed for test shall be 30 and that for assignment is 10.
The CIE marks for test in a theory Course shall be based on three tests generally conducted at the end of fifth, tenth and fourteenth week of each semester. Each test shall be conducted for a maximum of 30 marks and the final marks shall be the average of three tests.
The remaining 10 marks shall be awarded based on the evaluation of Assignments/Unit tests/written Quizzes that support to cover some of the Course/program outcomes. Final CIE marks awarded shall be the sum of these two out of maximum of 40 marks.</td></tr>
<tr><td>The candidates shall write the Internal Assessment Tests and Assignments/Unit-tests/written Quizzes in Blue Books which shall be preserved by the Principal/ Head of the Department for at least three years after the announcement of University results and shall be made available for verification at the direction of the Registrar (Evaluation).</td></tr>
</table>

17OB8.3	In the case of a Practical, the CIE marks shall be based on the laboratory journals/ records (30 Marks on continuous evaluation based on conduct of experiment, viva and report writing) and one practical test (10 Marks) to be conducted at the end of the semester.
17OB8.4	(i) The CIE marks for I year Computer Aided Engineering / Drawing: a) 24 marks for class work (sketching and Computer Aided engineering Drawing). b) 16 marks for test conducted in the same pattern as that of SEE (The marks secured can be taken as best of the two tests). (ii) The CIE marks for other Drawings/ Design Drawings offered by various branches shall be based on the evaluation of the sheets and one test in the ratio 60:40.
17OB8.5	The CIE marks in the case of projects and seminars in the final year shall be based on the evaluation at the end of VIII semester by a committee consisting of the Head of the concerned Department and two senior faculty members of the Department, one of whom shall be the project / seminar guide.
17OB8.6	i. For theory Courses, there shall not be any minimum requirements of CIE marks. ii. Minimum requirement of CIE marks for Practical/ Internship/Project work shall be 50% of the maximum marks. iii. For seminar, the minimum requirement of CIE marks shall be 40% of the maximum marks.
17OB8.7	i) Students failing to secure a minimum of 50% of the CIE marks in Practical/ Internship/Project work shall not be eligible for the Practical / Internship/Project examination conducted by the University and they shall be considered as failed in that/those Course/s. However, they can appear for University examinations conducted in other Courses of the same semester and backlog Courses if any. Students after satisfying the prescribed minimum CIE marks in the Course/s when offered during subsequent semester shall appear for SEE. ii) If any student fails to secure a minimum of 40% of the maximum CIE marks in seminar/ fails to deliver the seminar, he/she shall be considered as failed in that Course and shall not be eligible for the award of degree. However, the student shall become eligible for the award of degree after satisfying the requirements prescribed for seminar during the subsequent semester/s. iii) The Course/s under 17OB8.6 (ii) and (iii), when repeated are considered as attempts.
17OB8.8	CIE marks of those students, who come under 17OB8.7, shall be sent separately to the Registrar (Evaluation).
17OB8.9	If a student remains absent for all the CIE tests conducted, the CIE Marks shall be marked as AB for the Courses against the University Seat Number (USN) of the student in the marks sheet submitted to the University by the Principal of the College.
17OB8.10	Improvement of CIE marks shall not be allowed in a. Theory Courses and b. Laboratory/Workshop/Seminar/Internship/Project where the student has already secured the minimum required marks.
17OB8.11	The final list, incorporating corrections (if any) of CIE marks awarded to the students in the Theory/Practical/Internship/Project work/ Seminar, shall be displayed on the notice board of the college at least seven days before the closure of the semester and a certified copy of the same shall be sent by the Principal to the University Examination Section within the stipulated date. Every page of the CIE marks sheet shall bear the signatures of the concerned Teacher/Teachers, Head of the Department and Principal.
17OB8.12	Any corrections or overwriting of CIE marks shall bear the signature(s) of concerned Teacher(s) and in such cases the Head of the Department shall indicate the number of corrections on every sheet and attest it with his/her signature.

17OB8.13	CIE marks shall reach the University before the commencement of examination as per the notification issued from the office of the Registrar (Evaluation) from time to time. After the submission of CIE marks to the University, any request under any circumstances for change of CIE marks shall not be considered.
17OB 9.0	**Eligibility for Passing and Award of Degree(To be read along with 17OB4.2, 5.1, 5.2, 8.6 and8.7)**
17OB 9.1	(a) For a pass in a theory Course/Drawing, the student shall secure minimum of 35% of the maximum marks prescribed in the University examination and in total 40% of the maximum marks (i.e., prescribed for SEE and CIE) including the CIE marks secured by the student. (b) The Minimum Passing letter grade in a Course is 'E'. (c) For a pass in a Practical/Internship/Project/Viva-voce examination, a student shall secure a minimum of 40% of the maximum marks prescribed for the University Examination. The Minimum Passing Grade in a Course is 'E'.
17OB 9.2	1) A student who obtain any grade from 'S' to 'E' shall be considered as passed. 2) If a student secure F grade in any of the head of passing (17 OB 4.2, 17 OB 5.1, 17 OB 5.2 and 17 OB 11.2) he/she has to reappear in that head for the SEE. 3) A student will be declared successful at the end of academic year if he/she has not more than four 'F' grades in the immediate preceding two semesters. 4) A student will be declared successful at the end of program, when he/she has none of the Courses remaining with F grade and shall have CGPA of greater than or equal to 5.00. 5) In case, the CGPA falls below 5.00 at the end of the program, the student shall be permitted to appear again for SEE in full or part of the previous semester Courses by rejecting the performance for required number of Course/s (other than seminar, Project and Practical's) and times, subject to the provision of 17OB1.5, to make up CGPA equal to or greater than 5.0. The student should reject the SEE results of the previous attempt and obtain written permission from the Registrar (Evaluation) to reappear in the subsequent SEE.
17OB 9.3	The students who do not satisfy the provision 17OB9.2 (1) and the students who remain absent for the University examinations shall be deemed to have failed in that Course/s. They have to reappear for the University examination in the subsequent examinations. The CIE marks awarded to the student/s at first attempt in the concerned theory Course/s will be carried forward. Revised CIE marks are considered only in cases under the provisions of 17OB8.7.
17OB 9.4	Students who pass a Course of a semester as per 17OB 9.1 and has earned CGPA equal to or greater than 5.00 shall not be allowed to appear for any individual Course/s again, unless they opt for rejection of results of entire semester as per 17OB 9.5.
17OB9.5	A student may, at his/her desire, reject the total performance of a semester (including CIE marks) or reject only the result of his/her performance in University examination of a semester. The rejection is permitted only once during the entire programme of study.
17OB9.6	The student who desires to reject the results of a semester shall reject performance in all the Courses of the semester, irrespective of whether the student has passed or failed in any Course. However, the rejection of performance of VIII semester project shall not be permitted.

17OB9.7	A student, who desires to reject the total performance of a semester including CIE marks, has to take readmission for that semester. Application for approval of readmission shall be sent to the Registrar through the Principal of College within 30 days from the date of the announcement of the results. Late submission of application shall not be accepted for any reasons. Readmission to First semester in such cases shall not be considered as fresh admission and therefore the student will continue to have the same University Seat Number, which was allotted earlier. The Course duration (as per 17OB1.5) will be counted with reference to old USN.
17OB9.8	The student, who rejects only the results of University examination of a semester, shall be permitted to re-appear for University examinations of all the Courses of that semester in the subsequent examinations. However, the CIE marks obtained by the student in the rejected semester shall be retained. Applications for rejection and approval to reappear for University examination shall be sent to the Registrar (Evaluation) through the Principal of the College within 30 days from the date of announcement of the results. Late submission of applications shall not be accepted for any reasons. If the rejection of results of University examination is of odd semester, the student shall be allowed to take admission to the immediate next even semester. However, if the rejection of results of University examination is of even semester, then the student shall not be allowed to take admission to the next odd semester (as per 17OB11.2).
17OB9.9	Students who opt for rejection of results of University examination are eligible for the award of class and distinction, but are not eligible for the award of ranks.
17OB9.10	A student shall be declared to have completed the program of B.E. / B.Tech. degree, provided the student has undergone the stipulated Course work as per the Scheme of Teaching and Examination and has earned the prescribed number of credits as per the provision 17OB1.6, having CGPA ≥ 5 with none of the registered courses remaining with 'F' grade.
17OB10.0	**Attendance Requirement**
17OB10.1	Courses of each semester shall be treated as a separate unit for calculation of the attendance. The candidate has to put in a minimum attendance of 85% in each Course with a provision to condone 10% of the attendance by the Vice-Chancellor on the specific recommendations of the Principal of the college where the candidate is studying, based on medical grounds, participation in University/State/ National/ International level sports and cultural activities, seminars, workshops, paper presentation etc., of significant value. The supporting documents for condoning the shortage of attendance are to be submitted along with the recommendations.
17OB 10.2	The datum for the calculation of attendance shall be the number of Teaching hours prescribed for a Course [50 hours for 04 credit Courses (theory), and 40 hours for 03 credit Courses (theory) counted from the date of commencement of the semester. In case of Laboratories, the number of classes (deemed as teaching hours) is equal to the number of experiments prescribed under main heading]. In case of late admission, approved by competent authority (DTE/VTU), to I semester/III semester (lateral entry scheme)/ III semester (lateral entry scheme) of Engineering programme conducted during evening the attendance shall be reckoned from the date of admission to the programme.

17OB10.3	The Course Instructor/ Mentor/College shall inform the students as well as their parents about the attendance status periodically. Students who are facing the shortage of attendance be mentored to make up the shortage. Principals shall also notify every month, the list of candidates who are under short of attendance.
17OB10.4	A candidate, who does not satisfy the attendance requirement (in one or more Courses) as mentioned in 17OB10.1 shall not be eligible to appear for the SEE of that semester and shall not be permitted to take admission to next higher semester. The candidate shall be required to repeat that semester during the subsequent year.
17OB 10.5	The list of the candidates falling short of attendance shall be sent to Registrar (Evaluation) at least once in a month and final list shall be sent one week prior to the commencement of the examination. The detained students should obtain permission from Registrar, VTU for readmission to the semester concerned as a repeater.
17OB 11.0	**Promotion and Eligibility**
17OB 11.1	There shall be no restriction for promotion from an odd semester to the next even semester, provided the student has fulfilled the attendance requirement.
17OB 11.2	A student shall be eligible for promotion from an even semester to next odd semester if the student has not failed in more than four heads of passing of the immediately preceding two semesters and has passed in all the Courses of all the lower semester examinations. Each credit Course shall be treated as a head of passing. **Illustrations:** a) A student seeking eligibility to III semester should not have failed in more than 4 heads of passing of I and II semesters considered together. b) A student seeking eligibility to V semester should have passed in all the subjects of I and II semesters and should not have failed in more than 4 heads of passing of III and IV semesters considered together. c) A student seeking eligibility to VII semester should have passed in all the subjects up to IV semester and should not have failed in more than 4 heads of passing of V and VI semesters considered together. **Lateral entry scheme:** a. A student seeking eligibility to V semester should not have failed in more than 4 heads of passing of III and IV semesters considered together. b. A student seeking eligibility to VII semester should have passed in all the subjects of III and IV semesters and should not have failed in more than 4 heads of passing of fifth and sixth semesters considered together.

17OB 11.3	a. All students admitted to I semester and to III semester under lateral entry scheme to B.E./B.Tech. programme have to undergo the Mandatory non – credit Courses viz., Environmental Studies and English Language. However these Courses shall not be considered for the Eligibility criterion prescribed for promotion, award of Class, calculation of SGPA and CGPA. b. The Courses viz., Advanced Mathematics I and II, to be completed by the candidates (diploma holders) admitted to III semester under lateral entry scheme shall not be considered for the eligibility criterion prescribed for promotion, award of Class, calculation of SGPA and CGPA. However, a pass in the above Courses is mandatory for the completion of the programme and award of degree. c. The Courses Viz., (i) Computer Aided Engineering Drawing (ii) Programming in C and Data structure and (iii) Environmental Studies (if not studied at B.Sc. level), to be completed by the candidates who have passed B.Sc. degree and admitted to III semester of the programme, shall not be considered for the award of Class, calculation of SGPA and CGPA. However, a pass in the above Courses is mandatory for the completion of the programme and award of degree.
17OB 12.0	**Temporary Discontinuation/Break in the Program**
17OB 12.1	a) If a candidate, for any reason, temporarily discontinues the Programme or take a break from the programme during any semester intentionally, he/she may be permitted to continue the programme by registering to the same semester of the prevailing scheme. The candidate shall complete all the remaining Course work subject to the provision 17 OB 1.5. Also the Candidates may have to complete additional Course/s, if any, as per the decision of concerned Board of Studies and approval of Dean, Faculty of Engineering, on establishing equivalence between two schemes. A Grade card shall be issued to that effect. Additional Course/s shall not be considered for the eligibility criterion prescribed for promotion. However, based on the individual cases, it is considered to decide the SGPA and CGPA to admit the student for the award of degree. Such candidates shall not be eligible for the award of rank. b) Candidates who takes admission to any semester of the existing scheme from another scheme, as a repeater/fresher because of various reasons have to complete additional Course/s, if any, as per the decision of concerned Board of Studies and approval of Dean, Faculty of Engineering, on establishing equivalence between two schemes. A Grade card shall be issued to that effect. Additional Course/s shall not be considered for the eligibility criterion prescribed for promotion. However, based on the individual cases, it is considered to decide the SGPA and CGPA to admit the student for the award of degree. Such candidate shall not be eligible for the award of rank.

17OB 13.0	**Award of Prizes, Medals and Ranks**
17OB 13.1	For the award of Prizes and Medals, the conditions stipulated by the Donor shall be considered subject to the provisions of the statutes framed by the University for such awards.
17OB 13.2	1) For award of rank in a Specialization of Bachelor of Engineering/ Technology, the CGPA secured by the students from III to VIII semester is considered. 2) A student shall be eligible for a rank at the time of award of degree of Bachelor of Engineering/ Technology, provided the student, a) (i) Has passed I to VIII semester in all the Courses in first attempt only in case of candidates admitted I year. (ii) Has passed III to VIII semester in all the Courses in first attempt only in case of candidates admitted under lateral entry scheme. (iii) Has completed all the prescribed Audit/mandatory Courses. b) Is not a repeater in any semester because of rejection of result of a semester/ shortage of attendance etc. c) Has completed all the semesters without any break/discontinuity. d) Has completed all the semesters (I to VIII/III to VIII) in VTU constituent college or in any VTU affiliated non-autonomous college. e) Has not been transferred from autonomous institution affiliated to VTU or from any other University. 3) The total number of ranks awarded shall be 10% of total number of students appeared in VIII semester subject to a maximum of 10 ranks in a Specialization. 4) For award of ranks in a Specialization, a minimum of 10 students should have appeared in the VIII semester examination. **Illustration:** a. If 1228 students appeared for the VIII semester in Electronics and Communication Engineering programme, the number of ranks to be awarded for Electronics and Communication Engineering shall be 10. b. If 90 students appeared for the VIII semester in Biomedical Engineering, the number of ranks to be awarded for Biomedical Engineering will be 09. 5) In case of fractional number of ranks, it is rounded to higher integer only when the first decimal place value is greater than or equal to 5.
17 OB 13.3	Ranks are awarded based on the merit of the students as determined CGPA. If two or more students get the same CGPA, the tie shall be resolved by considering the number of times a student has obtained higher SGPA. If it is not resolved even at this stage, the number of times a student has obtained higher grades like S, A, B etc., shall be taken into account to decide the order of the rank.

17OB 14.0	**Transfers of Students**
17OB 14.1	Transfer of students from one college to another college within Karnataka state shall be permitted only at the beginning of third, fifth, and seventh semesters, subject to availability of seats within the permitted intake in respective Colleges and subject to the prior approval of the University. (a) Transfer of students from one non - autonomous to another non – autonomous college affiliated to VTU is permitted with the approval of the Registrar, VTU subject to the provision 17OB11.2. The students seeking transfer shall have to, (i) Obtain No Objection certificate for admission from the University and from both the colleges before the commencement of term as notified by VTU. (ii) Complete the programme subject to the provision 17OB1.5. (b) Transfer of students from an autonomous to non – autonomous college affiliated to VTU is permitted with the approval of the Registrar, VTU provided the candidates have passed in all the Courses of the previous semesters. The students seeking transfer shall have to, (i) Obtain No Objection certificate for admission from the University and from both the colleges before commencement of term as notified by VTU. (ii) Complete additional Course/s, if any, as per the decision of concerned Board of Studies and approval of Dean, Faculty of Engineering, on establishing equivalence between two schemes. A Grade card shall be issued to that effect. Additional Course/s shall not be considered for the Eligibility criterion prescribed for promotion, Class, calculation of SGPA and CGPA. However, a pass in the Additional Courses, if any, is mandatory before the completion of Degree. iii) Complete the programme subject to the provision 17OB1.5. (c) In the case of students from Universities other than VTU, the students must have passed in all the Courses of I and II semesters for admission to III semester and all the Courses of I to IV semesters for admission to V semester and all the Courses of I to VI semesters for admission to VII semester. The students seeking admission from other Universities to VTU shall have to, i) Apply for establishment of equivalence with prescribed fees as notified by the VTU and obtain No Objection certificate for admission from the University before commencement of term as notified by VTU. ii) Produce No Objection certificate for admission from both the colleges before commencement of term as notified by VTU. iii) Complete additional Course/s, if any, as per the decision of concerned Board of Studies and approval of Dean, Faculty of Engineering, on establishing equivalence between two schemes. A Grade card shall be issued to that effect. Additional Course/s shall not be considered for the eligibility criterion prescribed for promotion, Class, calculation of SGPA and CGPA. However, a pass in the additional Courses, if any, is mandatory before the completion of Degree. (ii) Complete the programme subject to the provision 17OB1.5.
17 OB 14.2	Transfer of students within the College from one branch to another branch at the start of III semester shall be permitted with the approval of the Registrar, VTU subject to the provisions made by the Government of Karnataka and AICTE in this behalf.
17OB 14.3	The University may prescribe fee for administrative purpose, which shall be notified from time to time, for transfer from one college to another (Change of College) or one branch to another branch (change of branch within the college).

17 OB 15.0	**Applicability and Power to Modify**
17 OB15.1	The regulations governing the Degree of Bachelor of Engineering/Technology of Visvesvaraya Technological University shall be a binding on all concerned.
17 OB15.2	i) Notwithstanding anything contained in the foregoing, the University shall have the power to issue directions/ orders to address any difficulty. ii) Nothing in the foregoing may be construed as limiting the power of the University to amend, modify or repeal any or all of the above.

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Annexure -1

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
Scheme of Teaching and Examination 2017-2018
Choice Based Credit System (CBCS)

I SEMESTER B.E./B.Tech. (PHYSICS GROUP)

Sl. No	Course Code	Course Title	Teaching Department	Board	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17MAT11	Engineering Mathematics -I	Mathematics	Basic Science	04	--	03	60	40	100	4
2	17PHY12	Engineering Physics	Physics	Basic Science	04	--	03	60	40	100	4
3	17CIV13	Elements of Civil Engineering and Mechanics	Civil Engineering	Civil Engineering	04	--	03	60	40	100	4
4	17EME14	Elements of Mechanical Engineering	Mechanical Engineering	Mechanical Engineering	04	--	03	60	40	100	4
5	17ELE17	Basic Electrical Engineering	E and E Engineering	E and E Engineering	04	--	03	60	40	100	4
6	17WSL16	Workshop Practice	ME, Auto, IP, IEM, Mfg Engineering	Mechanical Engineering	01Hour Instruction 02Hour Practical		03	60	40	100	2
7	17PHYL17	Engineering Physics Laboratory	Physics	Basic Science	01Hour Instruction 02Hour Practical		03	60	40	100	2
8	17ENG18	Language – English (Audit Course)	Humanities	--	01		--	--	--	--	--
				TOTAL	**Theory:21 hours Practical: 06 hours**		**21**	**420**	**280**	**700**	**24**

II SEMESTER B.E./B.Tech. (CHEMISTRY GROUP)

Sl. No	Course Code	Course Title	Teaching Department	Board	Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	Credits
1	17MAT21	Engineering Mathematics -II	Mathematics	Basic Science	04	--	03	60	40	100	4
2	17CHE22	Engineering Chemistry	Chemistry	Basic Science	04	--	03	60	40	100	4
3	17PCD23	Programming in C and Data Structures	Any Engineering Department	Computer Science and Engineering	04	--	03	60	40	100	4
4	17CED24	Computer Aided Engineering Drawing	ME, Auto, IP, IEM, Mfg Engineering	Mechanical Engineering	02Hour Instruction 04-Hour Practice		03	60	40	100	4
5	17ELN25	Basic Electronics	ECE/EEE/TC/E and I.	E and C Engineering	04	--	03	60	40	100	4
6	17CPL26	Computer Programming Laboratory	Any Engineering Department	Computer Science and Engineering	01Hour Tutorial 02Hour Practical		03	60	40	100	2
7	17CHEL27	Engineering Chemistry Laboratory	Chemistry	Basic Science	01Hour Tutorial 02Hour Practical		03	60	40	100	2
8	17CIV28	Environmental Studies (Audit Course)	Civil/ Environmental Engineering	Civil Engineering	01Tutorial		--	30	20	50	--
				TOTAL	**Theory:21 hours Practical: 08 hours**		**21**	**450**	**300**	**750**	**24**

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Scheme of Teaching and Examination 2017-2018
Choice Based Credit System (CBCS)

I SEMESTER B.E./B.Tech (CHEMISTRY GROUP)

Sl. No	Course Code	Course Title	Teaching Department	Board	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17MAT11	Engineering Mathematics -I	Mathematics	Basic Science	04	--	03	60	40	100	4
2	17PHY12	Engineering Chemistry	Chemistry	Basic Science	04	--	03	60	40	100	4
3	17PCD13	Programming in C and Data Structures	Any Engineering Department	Computer Science and Engineering	04	--	03	60	40	100	4
4	17CED14	Computer Aided Engineering Drawing	ME, Auto, IP, IEM, Mfg Engineering	Mechanical Engineering	02Hour Instruction 04Hour Practice		03	60	40	100	4
5	17ELN17	Basic Electronics	ECE/EEE/TC/E and I.	E and C Engineering	04	--	03	60	40	100	4
6	17CPL16	Computer Programming Laboratory	Any Engineering Department	Computer Science and Engineering	01Hour Tutorial 02Hour Practical		03	60	40	100	2
7	17CHEL17	Engineering Chemistry Laboratory	Chemistry	Basic Science	01Hour Tutorial 02Hour Practical		03	60	40	100	2
8	17CIV18	Environmental Studies (Audit Course)	Civil/ Environmental Engineering	Civil Engineering	01HourTutorial		--	30	20	50	--
				TOTAL	**Theory:21 hours Practical: 08 hours**		**21**	**450**	**300**	**750**	**24**

II SEMESTER B.E./B.Tech (PHYSICS GROUP)

Sl. No	Course Code	Course Title	Teaching Department	Board	Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	Credits
1	17MAT21	Engineering Mathematics -II	Mathematics	Basic Science	04	--	03	60	40	100	4
2	17PHY22	Engineering Physics	Physics	Basic Science	04	--	03	60	40	100	4
3	17CIV23	Elements of Civil Engineering and Mechanics	Civil Engineering	Civil Engineering	04	--	03	60	40	100	4
4	17EME24	Elements of Mechanical Engineering	Mechanical Engineering	Mechanical Engineering	04	--	03	60	40	100	4
5	17ELE25	Basic Electrical Engineering	E and E Engineering	E and E Engineering	04	--	03	60	40	100	4
6	17WSL26	Workshop Practice	ME, Auto, IP, IEM, Mfg Engineering	Mechanical Engineering	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
7	17PHYL27	Engineering Physics Laboratory	Physics	Basic Science	01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17ENG28	Language – English (Audit Course)	Humanities	--	01		--	--	--	--	--
				TOTAL	**Theory:21 hours Practical: 06 hours**		**21**	**420**	**280**	**700**	**24**

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III SEMESTER

Sl. No	Course Code	Course	Course Title	Teaching Department	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17MAT31	Core Course	Engineering Mathematics-III		04	--	03	60	40	100	4
2	17XX32	Core Course			04	--	03	60	40	100	4
3	17XX33	Core Course			04	--	03	60	40	100	4
4	17XX34	Core Course			04	--	03	60	40	100	4
5	17XX35	Core Course			04	--	03	60	40	100	4
6	17XX36	Foundation Course			03	--	03	60	40	100	3
7	17XXL37	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17XXL38	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
9	17KL/CPH39/49	Core Course	Kannada/Constitution of India, Professional Ethics and Human Rights	Humanities	01		01	30	20	50	01
TOTAL					**Theory:24hours** **Practical: 06 hours**		**25**	**510**	**340**	**850**	**28**

1. Core Course: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.
2. Foundation Course: The courses based upon the content that leads to Knowledge enhancement.
3. Kannada/Constitution of India, Professional Ethics and Human Rights: 50 % of the programs of the Institution have teach Kannada/ Constitution of India, Professional Ethics and Human Rights in cycle based concept during III and IV semesters.
4. Audit Course:
(i) All lateral entry students (except B.Sc candidates) have to register for Additional Mathematics – I which is 04 contact hours per week.
(ii) Language English (Audit Course) be compulsorily studied by all lateral entry students (except B. Sc candidates)

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IV SEMESTER

Sl. No	Course Code	Course	Course Title	Teaching Department	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17MAT41	Core Course	Engineering Mathematics-IV		04	--	03	60	40	100	4
2	17XX42	Core Course			04	--	03	60	40	100	4
3	17XX43	Core Course			04	--	03	60	40	100	4
4	17XX44	Core Course			04	--	03	60	40	100	4
5	17XX45	Core Course			04	--	03	60	40	100	4
6	17XX46	Foundation Course			03	--	03	60	40	100	3
7	17XXL47	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17XXL48	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
9	17KL/CPH39/49	Core Course	Kannada/Constitution of India, Professional Ethics and Human Rights	Humanities	01		01	30	20	50	01
				TOTAL	**Theory:24hours Practical: 06 hours**		**25**	**510**	**340**	**850**	**28**

1. Core Course: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.
2. Foundation Course: The courses based upon the content that leads to Knowledge enhancement.
3. Kannada/Constitution of India, Professional Ethics and Human Rights: 50 % of the programs of the Institution have teach Kannada/ Constitution of India, Professional Ethics and Human Rights in cycle based concept during III and IV semesters.
4.Audit Course:
(i) All lateral entry students (except B. Sc candidates) have to register for Additional Mathematics – II which is 04 contact hours per week.
(ii) Language English (Audit Course) be compulsorily studied by all lateral entry students (except B. Sc candidates)

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V SEMESTER

Sl. No	Course Code	Course	Course Title	Teaching Department	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17XX51	Core Course	Management and Entrepreneurship **Excluding CSE, ISE and EV Programs.** (The course must be related to Management and Entrepreneurship. However, the title and syllabus content can be as per the programme requirement).		04	--	03	60	40	100	4
2	17XX52	Core Course			04	--	03	60	40	100	4
3	17XX53	Core Course			04	--	03	60	40	100	4
4	17XX54	Core Course			04	--	03	60	40	100	4
5	17XX55X	Professional Elective			03	--	03	60	40	100	3
6	17XX56Y	Open Elective			03	--	03	60	40	100	3
7	17XXL57	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17XXL58	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
				TOTAL	**Theory:22hours Practical: 06 hours**		**24**	**480**	**320**	**800**	**26**

Electives

Professional Elective		**Open Elective*** Offered by the Department of ______________________**	
Courses under Code 17XX55X	**Course Title**	**Courses under Code 17XX56Y**	**Course Title**
17XX551		17XX561	
17XX552		17XX562	
17XX553		17XX563	
17XX554		17XX564	

***Students can select any one of the open electives offered by any Department (Please refer to consolidated list of VTU for open electives). Selection of an open elective is not allowed provided;

- The candidate has pre – requisite knowledge.
- The candidate has not studied during I and II year of the programme.
- The syllabus content of open elective is similar to that of Departmental core courses or professional electives.
- A similar course, under any category, is prescribed in the higher semesters.

Registration to electives shall be documented under the guidance of Programme Coordinator and Adviser.

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.
2. Professional Elective: Electives relevant to chosen specialization/ branch.
3. Open Elective: Electives from other technical and/ or emerging subject areas.

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Under Choice Based Credit System (CBCS)
(w.e.f. academic year 2017 – 18)
Annexure -1 (page -6)

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

Scheme of Teaching and Examination 2017-2018
Choice Based Credit System (CBCS)

B.E./B.Tech___________________________________

VI SEMESTER

Sl. No	Course Code	Course	Course Title	Teaching Department	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17XX61	Core Course	Management and Entrepreneurship **Excluding CSE, ISE and EV Programs.** (The course must be related to Management and Entrepreneurship. However, the title and syllabus content can be as per the programme requirement).		04	--	03	60	40	100	4
2	17XX62	Core Course			04	--	03	60	40	100	4
3	17XX63	Core Course			04	--	03	60	40	100	4
4	17XX64	Core Course			04	--	03	60	40	100	4
5	17XX65X	Professional Elective			03	--	03	60	40	100	3
6	17XX66Y	Open Elective			03	--	03	60	40	100	3
7	17XXL67	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17XXL68	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
				TOTAL	**Theory:22hours Practical: 06 hours**		**24**	**480**	**320**	**800**	**26**

Electives

Professional Elective		**Open Elective*** Offered by the Department of ______________________**	
Courses under Code 17XX65X	**Course Title**	**Courses under Code 17XX66Y**	**Course Title**
17XX651		17XX661	
17XX652		17XX662	
17XX653		17XX663	
17XX654		17XX664	

***Students can select any one of the open electives offered by any Department (Please refer to consolidated list of VTU for open electives). Selection of an open elective is not allowed provided;

- The candidate has pre – requisite knowledge.
- The candidate has not studied during I and II year of the programme.
- The syllabus content of open elective is similar to that of Departmental core courses or professional electives.
- A similar course, under any category, is prescribed in the higher semesters.

Registration to electives shall be documented under the guidance of Programme Coordinator and Adviser.

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.
2. Professional Elective: Electives relevant to chosen specialization/ branch.
3. Open Elective: Electives from other technical and/ or emerging subject areas.

Visvesvaraya Technological University, Belagavi
Regulations Governing the Degree of Bachelor of Engineering/ Technology (B.E/B.Tech.)
Under Choice Based Credit System (CBCS)
(w.e.f. academic year 2017 – 18)
Annexure -1 (page -7)

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

Scheme of Teaching and Examination 2017-2018
Choice Based Credit System (CBCS)

B.E./B.Tech__________________________________

VII SEMESTER

Sl. No	Course Code	Course	Course Title	Teaching Department	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17XX71	Core Course			04	--	03	60	40	100	4
2	17XX72	Core Course			04	--	03	60	40	100	4
3	17XX73	Core Course			04	--	03	60	40	100	4
4	17XX74 X	Professional Elective			03	--	03	60	40	100	3
5	17XX75Y	Professional Elective			03	--	03	60	40	100	3
6	17XXL76	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
7	17XXL77	Laboratory			01-Hour Instruction 02-Hour Practical		03	60	40	100	2
8	17XXP78	Core Course	Project Phase – I and Project seminar			03	--	--	100	100	2
				TOTAL	**Theory:18 hours Practical and Project: 09 hours**		**21**	**420**	**380**	**800**	**24**

Electives

Professional Elective		**Professional Elective**	
Courses under Code 17XX74X	**Course Title**	**Courses under Code 17XX75Y**	**Course Title**
17XX741		17XX751	
17XX742		17XX752	
17XX743		17XX753	
17XX744		17XX754	

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.
2. Professional Elective: Electives relevant to chosen specialization/ branch.
3. **Project Phase – I andProject seminar:** Comprises of Literature Survey, Problem identification, Objectives and Methodology. CIE marks shall be based on the report covering Literature Survey, Problem identification, Objectives and Methodology and seminar presentation skill.

Visvesvaraya Technological University, Belagavi
Regulations Governing the Degree of Bachelor of Engineering/ Technology (B.E/B.Tech.)
Under Choice Based Credit System (CBCS)
(w.e.f. academic year 2017 – 18)
Annexure -1 (page -8)

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

Scheme of Teaching and Examination 2017-2018
Choice Based Credit System (CBCS)

B.E./B.Tech__________________________________

VIII SEMESTER

Sl. No	Course Code	Course	Course Title	Teaching Department	Teaching Hours /Week		Examination				Credits
					Theory	Practical/ Drawing	Duration in hours	SEE Marks	CIE Marks	Total Marks	
1	17XX81	Core Course			04	--	03	60	40	100	4
2	17XX82	Core Course			04	--	03	60	40	100	4
3	17XX83X	Professional Elective			03	--	03	60	40	100	3
4	17XX84	Core Course	Internship/ Professional Practice		Working hours of the place of Internship		03	50	50	100	2
5	17XXP85	Core Course	Project work Phase -II		--	06	03	100	100	200	6
6	17XXS86	Core Course	Technical Seminar		--	04	--	--	100	100	1
				TOTAL	**Theory:11 hours Project and Seminar: 10 hours**		**15**	**390**	**310**	**700**	**20**

Professional Electives

Courses under Code 17XX83X	Course Title
17XX831	
17XX832	
17XX833	
17XX834	

1. Core subject: This is the course, which is to be compulsorily studied by a student as a core requirement to complete the requirement of a programme in a said discipline of study.
2. Professional Elective: Electives relevant to chosen specialization/ branch.
3. Internship/ Professional Practice: To be completed between the V and VI semester vacation and/or VI and VII semester vacation period.

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

SCHEME OF TEACHING AND EXAMINATION FOR **M.TECH. in Thermal Engineering/Thermal Power Engineering**

CHOICE BASED CREDIT SYSYTEM (CBSC)

I SEMESTER

Subject Code	Name of the Subject	Teaching hours/week		Duration of Exam in Hours	Marks for		Total Marks	CREDITS
		Lecture	Practical / Field Work / Assignmen		I.A.	Exam		
16MTP11	Applied Mathematics	4	-	3	**20**	**80**	100	4
16MTP12	Finite Element Method	4	-	3	**20**	**80**	100	4
16MTP13	Advanced Fluid Mechanics	4	-	3	**20**	**80**	100	4
16MTP14	Thermodynamics &Combustion Engineering	4	-	3	**20**	**80**	100	4
16MTP15X	Elective – I	4	-	3	**20**	**80**	100	4
16MTP16	Thermal Engineering measurement - Lab 1	--	3	3	**20**	**80**	100	2
16MTP/MTH 17	SEMINAR	--	-	--	**100**	**--**	100	1
	Total	**20**	**3**	18	**220**	**480**	**700**	**23**

Elective – I

16MTP 151	Non Conventional Energy System	14 MTP 153	Energy Conservation and Management
16MTP 152	Nuclear Energy Conversion	14MTP 154	Refrigeration and Air Conditioning

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

SCHEME OF TEACHING AND EXAMINATION FOR **M.TECH. in Thermal Engineering/Thermal Power Engineering**

CHOICE BASED CREDIT SYSYTEM (CBSC)

II SEMESTER

Subject Code	Name of the Subject	Teaching hours/week		Duration of Exam in Hours	Marks for		Total Marks	CREDITS
		Lecture	Practical / Field Work / Assignment		I.A.	Exam		
16MTP21	Advanced Heat Transfer	4	-	3	**20**	**80**	100	4
16MTP22	Steam &Gas Turbines	4	-	3	**20**	**80**	100	4
16MTP23	Advanced Power Plant Cycles	4	-	3	**20**	**80**	100	4
16MTP24	Theory of 1C Engines	4	-	3	**20**	**80**	100	4
16MTP25X	Elective – II	4	-	3	**20**	**80**	100	4
16MTP26	Simulation Laboratory Projects on Thermal Engineering - Lab 2	--	3	3	**20**	**80**	100	2
16MTP/MTH27	SEMINAR	--	-	--	**100**	--	100	1
	**PROJECT WORK PHASE-I COMMENCEMENT(6 WEEKS DURATION)	--	--	--	--	--	--	--
	Total	**20**	**3**	18	**220**	**480**	**700**	**23**

Elective – II

16MTP251	Thermal Power Station – 1	16MTP253	Modeling and Simulation of Thermal Systems
16MTP252	Alternate Fuels for 1C Engines	16MTP254	Computational Methods in Heat Transfer &Fluid Flow

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

SCHEME OF TEACHING AND EXAMINATION FOR M.TECH. in Thermal Engineering/Thermal Power Engineering

CHOICE BASED CREDIT SYSYTEM (CBSC)

III SEMESTER

Subject Code	Title	Teaching hours/week		Examination			Total Marks	CREDITS
		Theory	Practical / Field Work / Assignment	Duration	I.A. Marks	Theory / Practical Marks		
16MTP31	Seminar/ Presentation on Internship (After 8 weeks from the date of commencement)	-	-	-	25	-	25	
16 MTP32	Report on Internship	-	-	-	25	-	25	
16 MTP 33	Evaluation and Viva-Voce of Internship	-	-	-	-	50	50	20
16 MTP34	Project Work Phase-1	-	-	-	50	-	50	1
	Total	**-**	**-**	-	**100**	**50**	**150**	**21**

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
SCHEME OF TEACHING AND EXAMINATION FOR **M.TECH. in Thermal Engineering/Thermal Power Engineering**
CHOICE BASED CREDIT SYSYTEM (CBSC)

VI SEMESTER

Subject Code	Title	Teaching hours/week		Examination Duration			Total Marks	CREDITS
		Theory	Practical / Field Work /	Duration in Hrs	I.A. Marks	Theory / Practical Marks		
16MTP41	Design of heat Transfer Equipments for thermal power plant	4	2	3	20	**80**	100	4
	ELECTIVE-III	4	2	3	20	**80**	100	3
16 MTP 43	Evaluation of Project Work Phase-II	-	-	-	50	-	50	3
16 MTP 44	Evaluation of Project Work and Viva-Voce	-	-	3	-	**100+100**	200	10
	Total	**-**	**4**	9	**90**	**360**	**450**	**20**

ELECTIVE-III

16MTP421	Convective Heat and Mass Transfer	16MTP423	Design & Analysis of Thermal Systems
16MTP422	Engine Flow & Combustion	16MTP424	Experimental Methods in Thermal Power Engineering

Note:

1. Project Phase-2: 16-week duration during 4th semester**.** Evaluation shall be done by the committee constituted comprising of HoD as Chairman, Guide and Senior faculty of the department.

2. Project Evaluation: Evaluation shall be taken up at the end of 4th semester. Project work evaluation and Viva-Voce examination shall conducted
3. **Project evaluation:**

a. Internal Examiner shall carry out the evaluation for 100 marks.
b. External Examiner shall carry out the evaluation for 100 marks.
c .The average of marks allotted by the internal and external examiner shall be the final marks of the project evaluation.

c. Viva-Voce examination of Project work shall be conducted jointly by Internal and External examiner for 100 marks.

CHOICE BASED CREDIT SYSTEM (CBCS)
REGULATIONS GOVERNING
THE DEGREE OF BACHELOR OF ENGINEERING / TECHNOLOGY
(B.E./B.Tech.)

DEFINITIONS OF KEY WORDS:

1. **University:** Visvesvaraya Technological University, Belagavi.
2. **Academic Year:** Two consecutive (one odd + one even) semesters constitute one academic year.
3. **Semester:** Each semester will consist of 15-18 weeks of academic work equivalent to 90 actual teaching days. The odd semester may be scheduled from August to January and even semester from February to July.
4. **Choice Based Credit System (CBCS):** The CBCS provides choice for students to select from the prescribed courses *(core, elective and Foundation Courses).*
5. **Credit Based Semester System (CBSS):** Under the CBSS, the requirement for awarding a degree or certificate is prescribed in terms of number of credits to be earned by the students.
6. **Programme:** An educational programme leading to award of a Degree or certificate.
7. **Course:** Usually referred to, as 'papers' is a component of a programme. All courses need not carry the same weightage. The courses should define learning objectives and learning outcomes. A course may be designed to comprise lectures/ tutorials/laboratory work/ field work/ outreach activities/ project work/ vocational training/viva/ seminars/ term papers/assignments/ presentations/ self-study etc. or a combination of some of these.
8. **Branch:** Specialization or discipline of B.E./B.Tech. Degree Programme, like Civil Engineering, Textile Engineering, etc.
9. **Letter Grade:** It is an index of the performance of students in a said course. Grades are denoted by letters S, A, B, C, D, E and F.
10. **Grade Point:** It is a numerical weightage allotted to each letter grade on a 10-point scale.
11. **Credit:** A unit by which the course work is measured. It determines the number of hours of instructions required per week. One credit is equivalent to one hour of teaching (lecture or tutorial) or two hours of practical work/field work per week.
12. **Credit Point:** It is the product of grade point and number of credits for a course.
13. **Semester Grade Point Average (SGPA):** It is a measure of academic performance of student/s in a semester. It is the ratio of total credit points secured by a student in various courses registered in a semester and the total course credits

taken during that semester. It shall be expressed up to two decimal places.

14. **Cumulative Grade Point Average (CGPA):** It is a measure of overall cumulative performance of a student over all semesters. The CGPA is the ratio of total credit points earned by a student in various courses in all semesters and the sum of the total credits of all courses in all the semesters. It is expressed up to two decimal places.
15. **First Attempt:** If a student has completed all formalities and become eligible to attend the examinations and has attended at least one head of passing, such attempt (first sitting) shall be considered as first attempt.
16. **Transcript or Grade Card or Certificate:** Based on the grades earned, a grade certificate shall be issued to all the registered students after every semester. The grade certificate will display the course details (code, title, number of credits, grade secured) along with SGPA of that semester and CGPA earned till that semester.

Choice Based Credit System (CBCS): The CBCS provides choice for students to select from the prescribed courses.

Sequencing Plan for the B.E. / B.Tech. Degree Curriculum

Semesters	***Course Coverage***
I –II	HSS, BS and ES; Common for all Branches; Mandatory Courses
III-IV	BS Common for all Branches and ES; PS – Core/Elective
V-VII	PS- Core & Electives; Other Electives; Branch-wise Orientation
VIII	PS-Electives/Elective ; Other Electives, Internship, Project work

Abbreviations:

HSS - Humanities and Social Sciences
BS - Basic Sciences
ES - Engineering Sciences
PS - Professional Subjects

Credit Structure for Course Work

Lectures (hrs/wk /Sem)	Tutorial (hrs/wk/ Sem)	Lab. Work (hrs/wk/Sem)	*Credits* (Lec:instruction:Lab)	*Credits* (Total)
4	0	0	4:0:0	4
3	0	0	3:0:0	3
0	1	2	0:1:2	2