	S J P N Trust's **Hirasugar Institute of Technology, Nidasoshi** *Inculcating Values, Promoting Prosperity* Approved by AICTE, Recognized by Govt. of Karnataka and Affiliated to VTU Belagavi.	Office COE --- **2017-18(Odd)**	

ACADEMIC CALENDAR OF EVENTS
ODD SEMESTER: 7th AUGUST TO 25th NOVEMBER-2017

Week	Month	Days							No of Working days	Activities / Events
		MON	TUE	WED	THU	FRI	SAT	SUN		
1	AUGUST	7	8	9	10	11	12	13	6	**7-ODD SEM COMMENCEMENT**
2		14	15	16	17	18	19	20	5	**19-Aug.:Wel-Come function to I-Sem students (Tentative)**
3		21	22	23	24	25	26	27	5	**21 to 26 Aug.: Department welcome functions**
4		28	29	30	31				4	--
	SEPTEMBER					1	2	3		--
5		4	5	6	7	8	9	10	6	**8 to11 Sept.: Indoor games**
6		11	12	13	14	15	16	17	6	**14 to16 Sept.: I-Internal Assessment**
7		18	19	20	21	22	23	24	5	**21 to 22 Sept.: Feedback-1**
8		25	26	27	28	29	30		4	--
	OCTOBER							1		--
9		2	3	4	5	6	7	8	4	--
10		9	10	11	12	13	14	15	7	**15 to 17 Oct.: II-Internal Assessment**
11		16	17	18	19	20	21	22	2	**19 & 21-Oct.: Compensatory Holidays**
12		23	24	25	26	27	28	29	6	**25 to 26 Oct.: feedback-2**
13		30	31						5	**18 to 20 Nov.: III-Internal Assessment**
	NOVEMBER			1	2	3	4	5		**22 to 24 Nov.: Lab Internal Assessment**
14		6	7	8	9	10	11	12	5	**25-Nov.: LAST WORKING DAY**
15		13	14	15	16	17	18	19	7	**28-Nov.: Display of Average IA marks**
16		20	21	22	23	24	25	26	6	1-FEB-2018: **Commencement of EVEN-SEM-2018**
					TOTAL WORKING DAYS→				**83**	--

- DISPLAY AND SENDING IA MARKS TO PARENTS WITHIN FOUR DAYS AFTER IA.
- CENTRAL COUNCELLING WITHIN A WEEK FROM LAST DATE OF IA.

IMPORTANT DATES

SEM	ODD SEM Commencement	Last Working Day	Practical Exams/ Viva Voce	Theory Exams
I	7 AUG.2017	25 NOV. 2017	29 NOV. TO 8 DEC.2017	11 to 30 DEC 2017
III/V/VII	7 AUG.2017	25 NOV. 2017	29 NOV. TO 8 DEC.2017	11 DEC 2017 TO 10 JAN-2018

***As per VTU's COE* HOLIDAYS**

SNo	Date	Day	Holiday	SNo	Date	Day	Holiday
1	15-08-17	TUE	Independence day	8	02-10-17	MON	Mahatma Gandhi Jayanti
2	25-08-17	FRI	Ganesh Chaturthi	9	05-10-17	THU	Maharshi Walmiki Jayanti
3	28-08-17	MON	Mahadasoha	10	18-10-17	WED	Naraka Chaturdashi
4	02-09-17	SAT	Bakrid	11	20-10-17	FRI	Balipadya
5	19-09-17	Tue	Mahalaya Amavasya	12	01-11-17	WED	Kannda Rajyotsva
6	29-09-17	FRI	Ayudha Pooja	13	06-11-17	MON	Kanakadas Jayanti
7	30-09-17	SAT	Vijayadashmi	--	--	--	--

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CALENDAR OF EVENTS FOR THE ACADEMIC YEAR 2017-18
EVEN SEMESTER B.E. II, IV, VI & VIII

w.e.f.:01.02.2018

DATE	EVENTS
01-02-2018	**Registration & Commencement of Classes**
09-02-2018	Awareness Program on "Wild Life Conservation"
17-02-2018	Annual Sports Meet
05-03-2018 06-03-2018 07-03-2018	**First Internal Assessment**
12-03-2018 13-03-2018	Feed Back-1
13-03-2018	Display of First Internal Assessment Marks
15-03-2018	Submission of Feedback-1 report to office
16-03-2018 17-03-2018 18-03-2018	HIT Quest, HIT SAMBHRAMA-2018, Chandramana Ugadi/New Year Celebration
24-03-2018	Technical Competitions for Diploma Students
11-04-2018 12-04-2018 13-04-2018	**Second Internal Assessment**
21-04-2018	Display of Second Internal Assessment Marks
23-04-2018 24-04-2018	Feed Back-2
26-04-2018	Submission of Feedback-2 Report to Office
18-05-2018 19-05-2018 20-05-2018	**Third Internal Assessment**
22-05-2018 23-05-2018 24-05-2018	**Lab Internal Assessment**
23-05-2018	**Last Working Day**
25-05-2018	Final Year Project Exhibition
26-05-2018	Graduation Day
28-05-2018	Commencement of Practical Exams
28-05-2018	Commencement of Theory Exams(VIII- Sem)
29-05-2018	Display of Third Internal Assessment Marks
01-06-2018	Display of Final Internal Assessment Marks
11-06-2018	Commencement of Theory Exams(II, IV & VI Sem)
Total Number of Working Days- 87	

Prof. B.R.Umarane
Coordinator

Dr. S.C.Kamate
Principal

CALENDAR

February

S	M	T	W	T	F	S
				1	2	3
4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
25	26	27	28			

13- Mahashivaratri, 14-Maha Dasoha

March

S	M	T	W	T	F	S
				1	2	3
4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
25	26	27	28	29	30	31

18- Chandramana Ugadi
29-Mahaveer Jayanti 30-Good Friday

April

S	M	T	W	T	F	S
1	2	3	4	5	6	7
8	9	10	11	12	13	14
15	16	17	18	19	20	21
22	23	24	25	26	27	28
29	30					

14-Dr. B. R. Ambedkar Jayanti
18- Basava Jayanti

May

S	M	T	W	T	F	S
		1	2	3	4	5
6	7	8	9	10	11	12
13	14	15	16	17	18	19
20	21	22	23	24	25	26
27	28	29	30	31		

01- Labours Day

June

S	M	T	W	T	F	S
					1	2
3	4	5	6	7	8	9
10	11	12	13	14	15	16
17	18	19	20	21	22	23
24	25	26	27	28	29	30

15- Qutub-E-Ramazan

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CSE
Academic
Consolidated TT
2017-18 (Odd)

CONSOLIDATED TIME TABLE

W.E.F: 07 / 08 /2017

DAY	TIME / SEM	9.00AM-10.00AM	10.00AM-11.00AM		11.15AM-12.15PM	12.15PM-01.15PM		02.00PM-03.00PM	03.00PM-04.00PM	04.00PM-05.00PM
MONDAY	III	DMS	ADE	Break-11:00AM-11:15AM	USP	CO	Lunch break-1.015pm To 2.00pm	<----------ADEL(B1)/DSL(B2)---------->		
	V	DBMS	ATC		CN	TPA		IST/AJJ	CN#/ATC#	DBMS#
	VII	PW	OOMD		ECS	C#		<----------NL(B1)/WPL(B2)---------->		
TUESDAY	III	TPA	CO	Break-11:00AM-11:15AM	DSA	M-III		<----------ADEL(B3)/DSL(B1)---------->		
	V	ATC	ME		IST/AJJ	DBMS				
	VII	JAVA	C#		PW	ACA		<---------- NL(B2)/WPL(B3)---------->		
WEDNESDAY	III	<----------ADEL(B2)/DSL(B3)---------->				ADE		M-III	DMS	USP
	V	CN	.NET	Break-11:00AM-11:15AM	ATC	ME		<----------CNL(B2)/DBMSL(B3) ---------->		
	VII	C#	JAVA		OOMD	PW		<---------- NL(B3)/WPL(B1)---------->		
THURSDAY	III	M-III	USP	Break-11:00AM-11:15AM	DSA	DMS		ADE	DMS#/CO#	DSA#/USP#
	V	ME	DBMS		CN	.NET		<---------- CNL(B3)/DBMSL(B1) ---------->		
	VII	ACA	ECS		OOMD	JAVA				
FRIDAY	III	CO	DSA	Break-11:00AM-11:15AM	DMS	USP			**ADDITIONAL MATHMATICS	
	V	IST/AJJ	.NET		DBMS	ATC		<---------- CNL(B1)/DBMSL(B2) ---------->		
	VII	ECS	ACA		JAVA	OOMD			**ENVIRONMENT STUDIES	
SATURDAY	III	DSA	CO	Break-11:00AM-11:15AM	M-III	ADE				
	V	.NET	ME		IST/AJJ	CN				
	VII	ACA	C#		PW	ECS				

EXTRA CLASS

** FOR LATERAL ENTRY STUDENTS

HOD

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HIT, Nidasoshi.

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CSE
Academic
Consolidated TT
2017-18 (Even)

CONSOLIDATED TIME TABLE

W.E.F: 01/02/2018

DAY	TIME / SEM	9.00AM-10.00AM	10.00AM-11.00AM		11.15AM-12.15PM	12.15PM-01.15PM		02.00PM-03.00PM	03.00PM-04.00PM	04.00PM-05.00PM
MONDAY	IV	OOC	SE	Break-11:00AM-11:15AM	DAA	OOC/M-IV*	Lunch break-1.15pm To 2.00pm	DC	SE/MM*	DAA/DC*
	VI	CGV	OS		PAP	SS&CD		<----------SSOSL(B1)/CGVL(B2)---------->		
	VIII	INS	SMS		ST/ADHOC	SA		<------PROJECT WORK (B1,B2,B3,B4)-------->		
TUESDAY	IV	OOC	DC	Break-11:00AM-11:15AM	OOC	SE		<----------DAAL(B1)/MPL(B2)---------->		
								MATDIP401**		
	VI	CNSCL	OS		DM&DW	CGV		<----------SSOSL(B2)/CGVL(B3)---------->		
	VIII	SMS	ST/ADHOC		SMS	ST/ADHOC		<------PROJECT WORK (B5,B6,B7,B8)-------->		
WEDNESDAY	IV	M-IV	SE	Break-11:00AM-11:15AM	MM	OOC		<----------DAAL(B2)/MPL(B3)---------->		
	VI	OS	PAP		CGV	CNSCL		<----------SSOSL(B3)/CGVL(B1)---------->		
	VIII	ST/ADHOC	SMS		INS	SMS		<-------PROJECT WORK (B9,B10,B11)-------->		
THURSDAY	IV	DC	M-IV	Break-11:00AM-11:15AM	DAA	MM		<----------DAAL(B3)/MPL(B1)---------->		
	VI	PAP	CNSCL		SS&CD	DM&DW		PAP/ SS&CD *	OS/CNSCL*	CGV / DM&DW*
	VIII	SA	ST/ADHOC		SA	INS		<---------- SEMINAR---------->		
FRIDAY	IV	MM	DAA	Break-11:00AM-11:15AM	DC	M-IV		<---------- DEPT. ACTIVITIES ---------->		
	VI	SS&CD	DM&DW		CNSCL	CGV		<---------- DEPT. ACTIVITIES ---------->		
	VIII	INS	SA		INS	SA		<---------- DEPT. ACTIVITIES ---------->		
SATURDAY	IV	SE	MM	Break-11:00AM-11:15AM	M-IV	DAA				
	VI	DM&DW	SS&CD		OS	PAP				
	VIII									

**Only For Lateral Entry Students

*Tutorial Class

Time Table Coordinator

HOD

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TEACHING STAFF LOAD DETAILS: 2017-18 (ODD SEM.)

Department: Computer Science & Engineering

SL NO	Name of the faculty	Designation	Date of joining	Exp. in years	Load in Units		Total Academic Load in Units	Additional Work	
					Theory Load (Class. tut)	Lab/Project/Seminar Load		Dept	Institute
1	Dr.Parshuram Baraki	Prof.	21/09/2017	17	AJJ(9)=9	00+02+01=03	12	---	
2	Mr. S G Gollagi	AP	14/05/1998	19	DSA (9) + ATC (9) = 18	00+02+01=03	21	Seminar/FDP / Workshops / HIT-QUEST/Conference Coordinator	Exam Coordinator
3	Mr. S V Manjaragi	AP	01/09/2004	13	JAVA (9) + OOMD(9) =18	00+02+01=03	21	HOD	Admission Coordinator
4	Mr. Y M Naik	AP	06/10/2004	13	ADE (9)+ ACA(9) = 18	00+02+01=03	21	Seminar VIII(Sem)	
5	Mr. R R Patil	AP	11/07/2016	14	PCD (09)+ PCD(09)=18	00+02+01=03	21	CSI/KSCST Coordinator	
6	Mrs. A A Daptardar	AP	08/05/2007	11	DMS (9)+ C# .Net(9) =18	00+02+01=03	21	Industry-Institute-Interaction/ Project/ Alliance/ MoU, Ind. Visit etc)	
7	Mr. C R Belavi	AP	23/08/2008	9	DBMS (9) = 9	DBMSL 09+02+01=12	21	IA Coordinator	Website Coordinator
8	Mr. M G Huddar	AP	01/08/2009	8	USP (9)+ PW (9)=18	00+02+01=03	21	1. Industry-Institute-Interaction/ Project/ Alliance/ MoU, Ind. Visit etc) 2. IEEE coordinator	

SLNO	Name of the faculty	Designation	Date of joining	Exp. in years	Load in Units		Total Academic Load in Units	Additional Work	
					Theory Load (Class)	Lab/Project/Seminar Load		Dept	Institute
9	Mrs. S B Hosagoudar	AP	29/07/2014	8	IST (9) + JAVA & J2EE(9)=18	00+02+01=03	21	STAC/Competitive Examination/VTU Examination/(GATE/CAT/GRE etc., Ranks in VTU Exams.)	
10	Mr. S M Mayannavar	AP	14/02/2011	6	CO (9)=9	(DSL)09+02+01=12	21	1.NBA Coordinator 2. Seminar/FDP / Workshops / HIT-QUEST/Conference Coordinator	
11	Mr. M R Pawar	AP	01/08/2011	6	ECS (9)=9	(ADEL) 9+02+01=12	21	1. Training & Placement , Alumni Coordinator	
12	Mrs. S R Madyapagol	AP	15/07/2015	2	ME (9)=9	(WPL) 09+02+01 =12	21	1. STAC/Competitive Examination/VTU Examination/(GATE/CAT/GRE etc., Ranks in VTU Exams.) 2. ISTE Coordinator	
13	Mr. M A Chitale	AP	15/07/2013	4	CN (09)=09	(V-CNL) + (VII-CNL) 09+06+02+01 = 18	27	1. EMS / Feedback 2. Edusat/e-earning	
14	Mr. M A Gholap	AP	21/01/2012	1	.Net FAN(9)=9	(CNL) 03+02+01=06	15	Robo-Vidya/IT Maintenance	
15	MR. K K Kamati	AP	28/01/2017	1	--	(CPL) 09+09+02+01=	21	1. Dept. Library 2. CSI/KSCST	

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CSE
Work Load
2017-18 (Even)
Rev. 00

S. No.	Faculty Name	Sem./ Div.	Strength	Subject/ Subject Code	Theory Hrs./ Week	Practical/Seminar/Project Hrs./ Week	Work Load (Hrs./Week)	Staff Sign.
1.	Dr. Parashuram Baraki	VIII	45	SMS	05	03	08	
2.	Prof. S. G. Gollagi	VI	49	SS&CD	05	--	12	
		VI	49	DAA (01-CO) + SSOS Lab (01 – CO)	--	07		
3.	Prof. S. V. Manjaragi	IV	52	OOC	05	03	08	
4	Prof. Y. M. Naik	IV	52	MM	05	--	17	
		VIII	45	Adhoc	05	--		
		IV	52	MP Lab (02 – CO)	--	07		
5.	Prof. R. R. Patil	II	41	PCD	05	--	17	
		VI	49	CGV	05	--		
		VI	49	CGV Lab (02- CO)	--	07		
6.	Prof. A. A. Daptardar	VI	49	OS	05	--	19	
		VI	49	SSOS Lab(03- Main) + CPL (01- CO)	--	14		
7.	Prof. C. R. Belavi	II	38	PCD	05	--	17	
		VIII	45	ST	05	--		
		II	38	CP Lab (01- CO) + SSOS Lab (01 – CO)	--	07		
8.	Prof. M. G. Huddar	VI	49	PAP	05	--	21	
		VI	49	CGV Lab(03- Main) + MPL (01- CO) + CPL (01- CO)	--	16		
9.	Prof. S. B. Hosagoudra	VIII	45	SA	05	--	14	
		VI	49	SSOS Lab(01- CO) + CPL (02- CO)	--	09		
10.	Prof. S. M. Mayannavar	IV	52	DAA	05	--	12	
		IV	52	DAA Lab (02- CO)	--	07		
11.	Prof. M. R. Pawar	VI	49	DM&DW	05	--	19	
		IV	52	MP Lab (03- Main) + CGV Lab (01- CO)	--	14		
12.	Prof. M. A. Chitale	IV	52	DC	05	--	19	
		IV	52	DAA Lab (03- Main) + CP Lab (01- CO)	--	14		
13.	Prof. M. A. Gholap	VI	49	CNSCL	05	--	22	
		IV	52	SE	05	--		
		II	38	CP Lab (03- Main)	--	12		
14.	Prof. K. K. Kamati	VIII	45	INS	05	--	17	
		II	38	CP Lab (03- Main)	--	12		

H.O.D

Principal

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Examination	
Academic	
IA-I	
2017-18 (ODD)	

TIME-TABLE: INTERNAL ASSESSMENT TEST - I

Date→ Time→ Semester↓	14-9-2017		15-9-2017		16-9-2017	
	11AM-12 NOON	3PM-4PM	11AM-12 NOON	3PM-4PM	11AM-12 NOON	3PM-4PM
I	15**11	15**12	15**13	15**14 / 15CED14*	15**15	15**18
III	15**31	15**32	15**33	15**34	15**35 / 15ME35A / 15ME35B	15**36 / 15ME36B / 15ME36A*
V	15**51	15**52	15**53	15**54	15ME554/15EC553/15CS552 15CS553/15EE553	15EE563/15EC562/15CS564 15EE562/15EE563
VII	10**71	10**72	10**73	10**74	10ME754 / 10ME758/ 10EC751/ 10CS753/10EE752 / 10EE756	10ME769 / 10EC762 / 10EC763 10CS761/10EE764

Seating Arrangement(As Per the Roll Call)

Wing	Block	Semester-Branch : RollNos		
C	1	IIIA-ME:1-30 (30)	V-EE:1-30(30)	
	2	IIIA-ME:31-61(31)	V-EE:31-60(30)	
	3	VA-ME:1-30(30)	III-EE:1-30(30)	
	4	VA-ME:31-60(30)	III-EE:31-54(24)	
	5	VIIA-ME:1-30(30)	VII-EE:1-30(30)	
	6	VIIA-ME:31-63(33)	VII-EE:31-65(35)	
	7	VA-ME: 61-72(12)	V-EE:61-68(8)	

Wing	Block	Semester-Branch : RollNos		
B	8	1A-FY: 1-30(30)	IIIB-ME:1-30(30)	
	9	1A-FY:31-40(10)	1B-FY: 1-20(20)	IIIB-ME:31-61(31)
	10	1B-FY: 21-41(21)	VB-ME:1-30(30)	
	11	1C-FY:1-30(30)	VB-ME:31-60(30)	
	12	1C-FY: 31-45(15)	VB-ME: 61-68(8)	
	13	1D-FY:1-30(30)	VIIB-ME:1-30(30)	
	14	1D-FY: 31-35(5)	VIIB-ME:31-63(33)	

Wing	Block	Semester-Branch : RollNos	
A	15	III-CS:1-30(30)	V-EC:1-30(30)
	16	III-CS:31-52(22)	V-EC:31-65(35)
	17	V-CS:1-30(30)	III-EC:1-30(30)
D	18	V-CS:31-50(20),	III-EC:31-45(15)
	19	VII-CS:1-30(30),	VII-EC:1-30(30)
	20	VII-CS:31-44(14),	VII-EC:31-60(30)
	21	VII-EC:61-79(19)	

* 15CED14→1:30PM - 2:45PM[C1 & D1], 2:30PM - 3:45PM[C2 & D2], 3:30PM - 4:45PM[C3 & D3] || *15ME36A→1:30PM - 3:00PM [A1 & A2] , 3:00PM - 4:30PM [A3 & A4]

Examination Coordinator

CS. (G. Gollagi)

Dr. S.Gokamate

Principal

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Examination
Academic
IA-II
2017-18 (ODD)

TIME-TABLE: IA TEST - 2

Date→ Time→ Semester↓	15-10-2017		16-10-2017		17-10-2017	
	11AM-12 NOON	3PM-4PM	11AM-12 NOON	3PM-4PM	11AM-12 NOON	3PM-4PM
I	15**11	15**12	15**13	15**14 / 15CED14*	15**15	15**18 (C-cycle)
III	15**31	15**32	15**33	15**34	15**35 / 15ME35A / 15ME35B	15**36 / 15ME36B / 15ME36A*
V	15**51	15**52	15**53	15**54	15ME554/15EC553/15CS552 15CS553/15EE553	15EE563/15EC562/15CS564 15EE562/15EE563
VII	10**71	10**72	10**73	10**74	10ME754 / 10ME758/ 10EC751/ 10CS753/10EE752 / 10EE756	10ME769 / 10EC762 / 10EC763 10CS761/10EE764

Seating Arrangement(As Per the Roll Call)

Wing	Block	Semester-Branch : RollNos		
C	1	IIIA-ME:1-30 (30)	V-EE:1-30(30)	
	2	IIIA-ME:31-60(30)	V-EE:31-60(30)	
	3	VA-ME:1-30(30)	III-EE:1-30(30)	
	4	VA-ME:31-60(30)	III-EE:31-55(25)	
	5	VIIA-ME:1-30(30)	VI.-EE:1-30(30)	
	6	VIIA-ME:31-63(33)	VII-EE:31-65(35)	
	7	VA-ME: 61-77(17)	V-EE:51-55(5)	IIIA-ME:51-68(8)

Wing	Block	Semester-Branch : RollNos		
B	8	1A-FY: 1-30(30)	IIIB-ME:1-30(30)	
	9	1A-FY:31-40(10)	1B-FY: 1-20(20)	IIIB-ME:31-60(30)
	10	1B-FY: 21-41(21)	VB-ME:1-30(30)	
	11	1C-FY:1-30(30)	VB-ME:31-60(30)	
	12	1C-FY: 31-45(15)	VB-ME: 61-68(8)	
	13	1D-FY:1-30(30)	VIIB-ME:1-30(30)	
	14	1D-FY: 31-37(7)	VIIB-ME:31-63(33)	IIIB-ME:61-68(8)

Wing	Block	Semester-Branch : RollNos	
A	15	III-CS:1-30(30)	V-EC:1-30(30)
	16	III-CS:31-52(22)	V-EC:31-65(35)
	17	V-CS:1-30(30)	III-EC:1-30(30)
D	18	V-CS:31-50(20),	III-EC:31-42(12)
	19	VII-CS:1-30(30),	VII-EC:1-30(30)
	20	VII-CS:31-47(17),	VII-EC:31-60(30)
	21	VII-EC:61-81(21)	

* 15CED14→1:30PM - 2:45PM[C1 & D1], 2:30PM - 3:45PM[C2 & D2], 3:30PM - 4:45PM[C3 & D3] || *15ME36A→1:30PM - 3:00PM [A1 & A2] , 3:00PM - 4:30PM [A3 & A4]

Examination Coordinator

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Hirasugar Institute of Technology, Nidasoshi.

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Examination
Academic
IA - III
2017-18 (ODD)

TIME-TABLE: IA *TEST* - III

Date→ Time→ Semester↓	18-11-2017		19-11-2017		20-11-2017	
	11AM - 12 NOON	3PM - 4PM	11AM -12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM
I	17**11	17**12	17**13	17**14 / 17CED14*	17**15	17**18(C) / 17ENG18(P)
III	15**31	15**32	15**33	15**34	15**35 / 15ME35A / 15ME35B	15**36 / 15ME36B / 15ME36A*
V	15**51	15**52	15**53	15**54	15ME554/15EC553/15CS552 15CS553/15EE553	15EE563/15EC562/15CS564 15EE562/15EE563
VII	10**71	10**72	10**73	10**74	10ME754 / 10ME758/ 10EC751/ 10CS753/10EE752 / 10EE756	10ME769 / 10EC762 / 10EC763 10CS761/10EE764

Seating Arrangement(As Per the Roll Call)

Wing	Block	Semester–Branch : RollNos		
C	1	IIIA-ME:1-30 (30)	V-EE:1-30(30)	
	2	IIIA-ME:31-60(30)	V-EE:31-60(30)	
	3	VA-ME:1-30(30)	III-EE:1-30(30)	
	4	VA-ME:31-60(30)	III-EE:31-55(25)	VII-EE: 62-66 (5)
	5	VIIA-ME:1-30(30)	VII-EE:1-30(30)	
	6	VIIA-ME:31-63(33)	VII-EE:31-61(31)	
	7	VA-ME: 61-77(17)	V-EE:61-68(8)	IIIA-ME:61-68(8)

Wing	Block	Semester–Branch : RollNos		
B	8	1A-FY: 1-30(30)	IIIB-ME:1-30(30)	
	9	1A-FY:31-40(10)	1B-FY: 1-20(20)	IIIB-ME:31-60(30)
	10	1B-FY: 21-41(21)	VB-ME:1-30(30)	
	11	1C-FY:1-30(30)	VB-ME:31-60(30)	
	12	1C-FY: 31-45(15)	VB-ME: 61-68(8)	
	13	1D-FY:1-30(30)	VIIB-ME:1-30(30)	
	14	1D-FY: 31-37(7)	VIIB-ME:31-63(33)	IIIB-ME:61-68(8)

Wing	Block	Semester–Branch : RollNos	
A	15	III-CS:1-30(30)	V-EC:1-30(30)
	16	III-CS:31-52(22)	V-EC:31-65(35)
	17	V-CS:1-30(30)	III-EC:1-30(30)
D	18	V-CS:31-50(20),	III-EC:31-42(12)
	19	VII-CS:1-30(30),	VII-EC:1-30(30)
	20	VII-CS:31-45(15),	VII-EC:31-60(30)
	21	VII-EC:61-82(22)	

* 17CED14→1:30PM - 2:45PM (C1 & D1), 2:30PM - 3:45PM(C2 & D2), 3:30PM - 4:45PM(C3 & D3) || * 15ME36A→1:30PM - 3:00PM (A1 & A2) , 3:00PM - 4:30PM (A3 & A4)

14/11/17

Examination Coordinator

(S. G. Gollagi)

14/11

Dr. S.C.Kamate

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Institute
Academic
Internal Assessment I - 13
2017-18 (EVEN)

TIME-TABLE: IA *TEST* - I

Date→ Time→ Semester↓	05-03-2018		06-03-2018		07-03-2018	
	11AM - 12 NOON	3PM - 4PM	11AM -12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM
2	17**21	17**22	17**23	17**24 / 17CED24*	17**25	17**28(C)
4	15**41	15**42	15**43	15**44	15**45 / 15ME45A / 15ME45B	15**46 / 15ME46B / 15ME46A*
6	15**61	15**62	15**63	15**64	15ME653/15EC654/15CS651 /15EE651	15ME664/15EC663/15CS664 15EE662
8	10**81	10**82			10ME833/ 10EC832/ 10EC834/10CS835/10EE836	10ME844 / 10EC841 /10EC842/ 10CS841/10CS842/10EE842/10EE844

Seating Arrangement(As Per the Roll Numbers)

Wing	Block	Semester-Branch : Roll Nos		
C	1	IV A-ME:1-30 (30)	VI-EE:1-30(30)	
	2	IV A-ME:31-60(30)	VI -EE:31-47(17)	
	3	VI A-ME:1-30(30)	IV-EE:1-30(30)	
	4	VI A-ME:31-60(30)	IV-EE:31-54(24)	
	5	VIII A-ME:1-30(30)	VIII-EE:1-30(30)	
	6	VIII A-ME:31-63(33)	VIII-EE:31-57(27)	
	7	VI A-ME: 61-62(02)	IVA-ME:61-65(5)	

Wing	Block	Semester-Branch : Roll Nos		
B	8	1A-FY: 1-30(30)	IV B-ME:1-30(30)	
	9	1A-FY:31-38(08)	1B-FY: 1-20(20)	IVB-ME:31-61(31)
	10	1B-FY: 21-41(21)	VIB-ME:1-30(30)	
	11	1C-FY:1-30(30)	VIB-ME:31-58(28)	
	12	1D-FY:1-30(30)	VIIIB-ME:1-30(30)	
	13	1C-FY: 31-45(15)	1D-FY: 31-36(6)	VIIIB-ME:31-63(33)

Wing	Block	Semester-Branch : Roll Nos	
A	14	IV-CS:1-30(30)	VI-EC:1-30(30)
	15	IV-CS:31-52(22)	VI-EC:31-56(26)
	16	VI-CS:1-30(30)	IV-EC:1-30(30)
D	17	VI-CS:31-49(19)	IV-EC:31-41(11)
	18	VIII-CS:1-30(30)	VIII-EC:1-30(30)
	19	VIII-CS:31-45(15)	VIII-EC:31-60(30)
	20	VIII-EC:61-81(21)	

* **17CED24**→1:30PM - 2:45PM (A1 & B1), 2:30PM - 3:45PM(A2 & B2), 3:30PM - 4:45PM(A3 & B3) || * **15ME46A**→1:30PM - 3:00PM (B1 & B2) , 3:00PM - 4:30PM (B3 & B4)

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Institute
Academic
Internal Assessment I-13
2017-18 (EVEN)

TIME-TABLE: IA *TEST* - II

Date→ Time→ Semester↓	11-04-2018		12-04-2018		13-04-2018	
	11AM - 12 NOON	3PM - 4PM	11AM -12 NOON	3PM - 4PM	11AM - 12 NOON	3PM - 4PM
2	17**21	17**22	17**23	17**24 / 17CED24*	17**25	17**28(C)
4	15**41	15**42	15**43	15**44	15**45 / 15ME45A / 15ME45B	15**46 / 15ME46B / 15ME46A*
6	15**61/10**61	15**62/10**62	15**63/10**63	15**64/10**64	15ME653/15EC654/15CS651/ 15EE651/10**65	15ME664/15EC663/15CS664/ 15EE662/10ME665/10EC662/10CS666/ 10EE666
8	10**81	10**82	10ME833/ 10EC832/ 10EC834/10CS835/10EE836	10ME844 / 10EC841/ 10EC842/10CS841/10CS842/10 EE842/10EE844		

Seating Arrangement(As Per the Roll Numbers)

Wing	Block	Semester-Branch : Roll Nos		
C	1	IV A-ME:1-32 (32)	VI-EE:1-30(30)	
	2	IV A-ME:33-65(33)	VI -EE:31-47(17)	
	3	VI A-ME:1-31(31)	IV-EE:1-30(30)	
	4	VI A-ME:32-62(31)	IV-EE:31-54(24)	
	5	VIII A-ME:1-30(30)	VIII-EE:1-30(30)	
	6	VIII A-ME:31-63(33)	VIII-EE:31-57(27)	

Wing	Block	Semester-Branch : Roll Nos		
B	7	1A-FY: 1-30(30)	IV B-ME:1-30(30)	
	8	1A-FY:31-38(08)	1B-FY: 1-20(20)	IVB-ME:31-61(31)
	9	1B-FY: 21-41(21)	VIB-ME:1-30(30)	
	10	1C-FY:1-30(30)	VIB-ME:31-58(28)	
	11	1D-FY:1-30(30)	VIIIB-ME:1-30(30)	
	12	1C-FY: 31-45(15)	1D-FY: 31-36(6)	VIIIB-ME:31-63(33)

Wing	Block	Semester-Branch : Roll Nos	
A	13	IV-CS:1-30(30)	VI-EC:1-30(30)
	14	IV-CS:31-52(22)	VI-EC:31-56(26)
	15	VI-CS:1-30(30)	IV-EC:1-30(30)
D	16	VI-CS:31-49(19)	IV-EC:31-41(11)
	17	VIII-CS:1-30(30)	VIII-EC:1-30(30)
	18	VIII-CS:31-45(15)	VIII-EC:31-60(30)
	19	VIII-EC:61-81(21)	

* **17CED24**→1:30PM - 2:45PM (A1 & B1), 2:30PM - 3:45PM(A2 & B2), 3:30PM - 4:45PM(A3 & B3) || * **15ME46A**→1:30PM - 3:00PM (B1 & B2) , 3:00PM - 4:30PM (B3 & B4)

7/4/18

Examination Coordinator

7/4/18

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LESSON PLAN

Subject with code : ME for IT Industry (15CS51) Lect. Per week : 04

Class : V

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
Module 1 - Introduction	Meaning, nature & characteristics of mgmt	8/8/17	8/8/17		
	Functional area, goals, level of management	9/8/17	9/8/17		
	Brief overview of evolution of mgmt. theories	10/8/17	10/8/17		
	Nature & importance of planning	12/8/17	12/8/17		
	Types of plans & steps in planning	16/8/17	14/8/17 16/8/17		
	Nature & purpose of organization.	17/8/17	17/8/17		
	Types of organization	19/8/17	19/8/17		
	Meaning of staffing	22/8/17	21/8/17		
	Process of recruitment	23/8/17	22/8/17		
	Process of selection.	24/8/17	23/8/17		
Planned Hrs: 10 hours				Actual Hrs : 11 hrs	
Module 2 - Directing & controlling	Meaning of Directing	26/8/17	30/8/17		
	Nature of Directing	29/8/17	31/8/17		
	Leadership styles	30/8/17	4/9/17		
	Motivation theories	31/8/17	5/9/17		
	Meaning & importance of communication	1/9/17	6/9/17		
		5/9/17	7/9/17		
	Co-ordination - meaning & importance	6/9/17	9/9/17		
	Meaning of controlling	7/9/17	11/9/17		
	Steps in controlling	9/9/17	12/9/17		
	Methods of establishing control.	12/9/17	13/9/17		
Planned Hrs: 10 hours				Actual Hrs : 10 hrs	

HOD

PRINCIPAL

LESSON PLAN

Subject with code : ME for IT Industry 15CS51 Lect. Per week : 04

Class : V

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
Module 3 - Entrepreneur	Meaning & Characteristics of Entrepreneur	13/9/17	20/9/17		
	Classification & Types of Entrepreneurs	20/9/17	21/9/17		
	Stages in Entrepreneurial process	21/9/17	23/9/17		
	Role of Entrepreneur in Economic development	23/9/17	25/9/17 26/9/17		
	Entrepreneurship in India & its barriers.	26/9/17	27/9/17 (1)		
	Identification of business opportunities.	27/9/17	27/9/17 (2)		
	Market feasibility study	28/9/17	3/10/17		
	Technical feasibility study	3/10/17			
	financial feasibility study	4/10/17	4/10/17		
	Social feasibility study.	7/10/17	7/10/17		
Planned Hrs: 10 hours				Actual Hrs : 10 hrs.	
Module 4 - Preparation of Project & ERP	Meaning of project & project identification	10/10/17	10/10/17		
	Project selection, Project report	11/10/17	11/10/17		
	Need & significance of project report	12/10/17	12/10/17		
	contents & formulation.	14/10/17			
	Guidelines by planning commission	24/10/17	14/10/17		
	Meaning & importance of ERP & functional areas of mgmt.	25/10/17	24/10 & 25/10		
	Marketing / sales - supply chain management	26/10/17	25/10		
	Finance & accounting	28/10/17	26/10		
	Human resources, Types of reports	30/10/17	28/10		
	Methods of report Generation	31/10/17	31/10 (1) & 31/10 (2)		
Planned Hrs: 10 hours				Actual Hrs : 10	

HOD PRINCIPAL

LESSON PLAN

Course : System S/W & Compiler Design Code : 15CS63 Lect. Per week : 4+½

Sem. : VI [CBCS]

Mod-ule/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
I	Introduction to System S/W	5/2/18	5/2/18		Pgam 15/2/18
	Machine Architecture of SIC	8/2/18	8,9/2		
	Machine Architecture of SIC/XE	9,10/18	10,15/2		
	SIC Programming Examples	12/2/18	15/2/18		
	Basic Assembler functions	15/2/18	16,17/2		
	Machine Dependent assembler features	15/2/18	19/2/18		Pgam 19/2/18
	,,	16/2/18	22/2/18		
	Machine Independent assembler features	17/2/18	23/2/18		
	Assembler Design options	19/2/18	24/2/18		
	Basic macro processor functions	23/2/18	26/2/18		Pgam 26/2/18
Planned Hrs: 10+1				Actual Hrs : 13	
II	Introduction to loader, Basic loader f2s	24/2/18	1/3/18		
	Machine Dependent loader features	26/2/18	1/3/18		
	,,	01/3/18	2/3/18		Pgam 2/3/18
	Machine Independent loader Features	1/3/18	8/3/18		
	,,	2/3/18			
	Loader Design options	3/3/18	9/3/18		
	Loader Design options	8/3/18	10/3/18		Pgam 10/3/18
	Implementation Examples	9/3/18	12/3/18		
	Implementation Examples	10/3/18	14/3/18		Pgam 14/3/18
	,,	12/3/18			
Planned Hrs: 10				Actual Hrs : 08	

Pgam
HOD

PRINCIPAL

LESSON PLAN

Course : SSCD Code : 15CS63 Lect. Per week : 4

Sem. : VI

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
III	Introduction to Compiler	15/3/18	15/3/18		
	"	15/3/18	16/3/18		
	Introduction to lexical Analyzer	19/3/18	17, 19/3/18		
	"	22/3/18	24/3/18		Plane 24/3/18
	Alphabets + Tokens in Computer languages	23/3/18	25/3/18		
	Recognition of Token + Representation	24/3/18	2/4/18		
	Recognition of Token + Representation	26/3/18	5/4/18		Plane 5/4/18
	Finite Automata	29/3/18	6/4/18		
	Implementation of LA	29/3/18	7/4/18		
	Error Recovery	30/3/18	16/4/18		Plane 16/4/18
Planned Hrs: 10				Actual Hrs : 11	
IV	Introduction to Parser, Role of PARSER	31/3/18	19/4/18		
	Context-Free Grammars	2/4/18	20/4/18		
	Top-down Parser: RDP	5/4/18	26/4/18		Plane 26/4/18
	Top-down Parser: LL(1)	6/4/18	27/4/18		
	"	7/4/18	28/4/18		
	Bottom-up Parser: SRP	9/4/18	30/4/18		Plane 30/4/18
	Bottom-up Parser: LR	14/4/18	3/5/18		
	"	16/4/18	4/5/18		
	"	19/4/18	5/5/18		Plane 5/5/18
	Operator Precedence Parser	19/4/18			
Planned Hrs: 10				Actual Hrs : 09	

Plane
HOD

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Syllabus Covered from 07/08/2017 to 22/09/2017 (Up to Week No. 07)

S. No.	Semester	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual No. of classes engaged	% of Syllabus covered	No. of Modules Completed	Faculty Sign.
1	III	Engineering Mathematics – III	15MAT31	Sri.S. L. Patil	21	24	38	1.7	
2		Analog and Digital Electronics	15CS32	Sri. Y. M. Naik	22	17	26	1.3	
3		Data Structures and Applications	15CS33	Sri. S G. Gollagi	29	26	38	1.7	
4		Computer Organization	15CS34	Sri. S. M. Mayannavar	22	25	34	1.7	
5		Unix and Shell Programming	15CS35	Sri. M. G. Huddar	22	24	49	2.4	
6		Discrete Mathematical Structures	15CS36	Mrs. A. A. Daptardar	30	30	44	2.2	
1	V	Management & Entrepreneurship for IT Industry	15CS51	Mrs .S. R. Madyapagol	24	25	45	2.3	
2		Computer Networks	15CS52	Sri. M. A. Chitale	28	23	30	1.5	
3		Database Management System	15CS53	Sri. C. R. Belavi	23	20	20	1.9	
4		Automata Theory & Computability	15CS54	Sri.S. G. Gollagi	23	21	30	1.5	
5		Introduction to Software Testing	15CS552	Mrs.S.B. Hosgoudar	20	18	35%	1.6	
6		Advanced Java & J2EE	15CS553	Sri. S.V. Manjaragi	20	16	33%	1.4	
7		Dot Net Framework for Application Development	15CS564	Sri. M. A. Gholap	22	24	37.5%	1.8	
1	VII	Object Oriented Modeling & Design	10CS71	Sri. S.V. Manjaragi	21	20	33%	2.5	
2		Embedded Computing Systems	10CS72	Sri M.R. Pawar					
3		Programming the Web	10CS73	Sri M. G. Huddar	21	22	34%	2.7	
4		Advanced Computer Architectures	10CS74	Sri Y. M. Naik	13	17	25	2.2 units	
5		Java & J2EE	10CS753	Mrs.S.B. Hosgoudar	25	24	32.69%	2.8 units	
6		C# Programming & .Net	10CS761	Mrs A. A. Daptardar	23	23	38%	3.2	

Labs:

S.N	Sem.	Lab.	Sub. Code	Faculty	Actual no. of Expts	No. Expt.s completed	Sign.
1	III	Analog and Digital Electronics Laboratory	15CSL37	Sri .M.R. Pawar			
2		Data Structures with C Laboratory	15CSL38	Sri .S. M. Mayannavar	12	06	
3	V	Computer Network Laboratory	15CSL57	Sri. M. A. Chitale	12	04	
4		DBMS Laboratory with Mini Project	15CSL58	Sri .C. R. Belavi	05	02	
5	VII	Networks Laboratory	10CSL77	Sri M. A. Chitale/ M. A. Gholap	12/12	05/04	
6		Web Programming Laboratory	10CSL78	Mrs S. R. Madyapagol	12	05	

H.O.D.
Computer Science & Engg.

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Syllabus Covered from 07/08/2017 to 28/10/2017 (Up to Week No. 12)

S. No.	Semester	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual No. of classes engaged	% of Syllabus covered	No. of Modules Completed	faculty Sign.
1	III	Engineering Mathematics – III	15MAT31	Sri.S. L. Patil	36	36	70 %	3.8	
2		Analog and Digital Electronics	15CS32	Sri. Y. M. Naik	38	26	40 %	2.0	
3		Data Structures and Applications	15CS33	Sri. S G. Gollagi	42	39	65 %	3.2	
4		Computer Organization	15CS34	Sri. S. M. Mayannavar	35	37	60 %	3	64
5		Unix and Shell Programming	15CS35	Sri. M. G. Huddar	35	37	70 %	3.5	
6		Discrete Mathematical Structures	15CS36	Mrs. A. A. Daptardar	43	49	68 %	3.4	
1	V	Management & Entrepreneurship for IT Industry	15CS51	Mrs .S. R. Madyapagol	35	35	70	3.8	
2		Computer Networks	15CS52	Sri. M. A. Chitale	42	39	64 %	3.2	
3		Database Management System	15CS53	Sri. C. R. Belavi	38	38	65 %	3.3	
4		Automata Theory & Computability	15CS54	Sri.S. G. Gollagi	40	37	60 %	3	
5		Introduction to Software Testing	15CS552	Mrs.S.B. Hosgoudar	34	34	65 %	3.1	66
6		Advanced Java & J2EE	15CS553	Sri. Parshuram Barki	13	12	90 %	1.5	
7		Dot Net Framework for Application Development	15CS564	Sri. M. A. Gholap	36	37	64 %	3.2	
1	VII	Object Oriented Modeling & Design	10CS71	Sri. S.V. Manjaragi	35	33	61.1	5	
2		Embedded Computing Systems	10CS72	Sri M.R. Pawar	38	38	53 %	4.2	
3		Programming the Web	10CS73	Sri M. G. Huddar	35	38	61 %	5	
4		Advanced Computer Architectures	10CS74	Sri Y. M. Naik	40	27	51 %	4	60
5		Java & J2EE	10CS753	Mrs.S.B. Hosgoudar	39	38	59 %	4.9	
6		C# Programming & .Net	10CS761	Mrs A. A. Daptardar	36	37	58 %	4.8	

Labs:

S.N	Sem.	Lab.	Sub. Code	Faculty	Actual no. of Expts	No. Expt.s completed	Sign.
1	III	Analog and Digital Electronics Laboratory	15CSL37	Sri .M.R. Pawar	10	10	
2		Data Structures with C Laboratory	15CSL38	Sri .S. M. Mayannavar	12	10	
3	V	Computer Network Laboratory	15CSL57	Sri. M. A. Chitale	12	08	
4		DBMS Laboratory with Mini Project	15CSL58	Sri .C. R. Belavi	05	04	
5	VII	Networks Laboratory	10CSL77	Sri M. A. Chitale/ M. A. Gholap	12	08/08	
6		Web Programming Laboratory	10CSL78	Mrs S. R. Madyapagol	12	10	

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Computer Science & Engg.
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Syllabus Covered from 07/08/2017 to 4/11/2017 (Up to Week No. 13)

S. No.	Semester	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual No. of classes engaged	% of Syllabus covered	No. of Modules Completed	faculty Sign.
1	III	Engineering Mathematics – III	15MAT31	Sri.S. L. Patil	42	40	85	4.3	
2		Analog and Digital Electronics	15CS32	Sri. Y. M. Naik	41	30.	55%.	3.5 Modules	
3		Data Structures and Applications	15CS33	Sri. S G. Gollagi	44	41	75%	3.8	
4		Computer Organization	15CS34	Sri. S. M. Mayannavar	41	43	78.1	3.9	
5		Unix and Shell Programming	15CS35	Sri. M. G. Huddar	38	41	82	4.1	
6		Discrete Mathematical Structures	15CS36	Mrs. A. A. Daptardar	46	54	84	4.2	
1	V	Management & Entrepreneurship for IT Industry	15CS51	Mrs .S. R. Madyapagol	43	43	86	4.1	
2		Computer Networks	15CS52	Sri. M. A. Chitale	45	42	72	3.6	
3		Database Management System	15CS53	Sri. C. R. Belavi	45	43	80%.	4	
4		Automata Theory & Computability	15CS54	Sri.S. G. Gollagi	44	41	75%.	3.8	
5		Introduction to Software Testing	15CS552	Mrs.S.B. Hosgoudar	34	36	~~70~~ (80%)	3.8	
6		Advanced Java & J2EE	15CS553	Sri. Parshuram Barki	20	19	80%	2.2	
7		Dot Net Framework for Application Development	15CS564	Sri. M. A. Gholap	39	39	73%.	3.75	
1	VII	Object Oriented Modeling & Design	10CS71	Sri. S.V. Manjaragi	40	39	70%.	5.5	
2		Embedded Computing Systems	10CS72	Sri M.R. Pawar	38	42	70%.	5.5 unit	
3		Programming the Web	10CS73	Sri M. G. Huddar	38	42	70%.	5.5	
4		Advanced Computer Architectures	10CS74	Sri Y. M. Naik	41	30	55%.	4.5 unit	
5		Java & J2EE	10CS753	Mrs.S.B. Hosgoudar	42	41	69%.	5.4 Units	
6		C# Programming & .Net	10CS761	Mrs A. A. Daptardar	41	40	70%.	5.5 units	

Labs:

S.N	Sem.	Lab.	Sub. Code	Faculty	Actual no. of Expts	No. Expt.s completed	Sign.
1	III	Analog and Digital Electronics Laboratory	15CSL37	Sri .M.R. Pawar	10	10	
2		Data Structures with C Laboratory	15CSL38	Sri .S. M. Mayannavar	12	11	
3	V	Computer Network Laboratory	15CSL57	Sri. M. A. Chitale	12	10	
4		DBMS Laboratory with Mini Project	15CSL58	Sri .C. R. Belavi	05	04	
5	VII	Networks Laboratory	10CSL77	Sri M. A. Chitale/ M. A. Gholap	12	10	
6		Web Programming Laboratory	10CSL78	Mrs S. R. Madyapagol	12	11	

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Syllabus Covered from 05-02-2018 to 10-03-2018 (Up to 05 Week)

S. No.	Semester	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual No. of classes engaged	% of Syllabus covered	Faculty Sign.	
1	IV	Engineering Mathematics-IV	15MAT41	Sri. S. A. Patil	18	18	36%		
2		Software Engineering	15CS42	Sri. M. A. Gholap	16	11	18%		
3		Design & Analysis of Algorithms	15CS43	Sri. S. M. Mayannavar	18	21	21%		24% (S) 83% (A)
4		Micro Processors & Microcontrollers	15CS44	Sri Y. M. Naik	19	16	26%		
5		Object Oriented Concepts	15CS45	Sri. S.V. Manjaragi	14	14	21%		
6		Data Communication	15CS46	Sri. M. A. Chitale	20	14	18%		
1	VI	Cry. Network Security & Cyber Law	15CS61	Sri. M. A. Gholap	18	15	18%		
2		Computer Graphics & Visualization	15CS62	Sri. R.R. Patil	18	16	21%		
3		System Software & Compiler Design	15CS63	Sri. S. G. Gollagi	20	16	24		26% (S) 83% (A)
4		Operating System	15CS64	Smt. A. A. Daptardar	18	18	32%		
5		Data Mining & Data Warehousing	15CS651	Sri. M. R. Pawar	18	18	22%		
6		Python Application Programming	15CS664	Sri. M. G. Huddar	19	17	35%		
1	VIII	Software Architecture	10IS81	Smt. S. B. Hosagoudar	24	20	36%		
2		System Modeling & Simulation	10CS82	Dr. Parashuram Baraki	16	15	23%		
3		Information & Network Security	10CS835	Sri. K. K. Kamati	21	17	30%		27% (S) 81% (A)
4		Adhoc Networks	10CS841	Sri Y. M. Naik	18	14	23%		
5		Software Testing	10CS842	Sri. C. R. Belavi	18	14	23%		

Labs:

S.N	Sem.	Lab.	Sub. Code	Faculty	Actual no. of Expts	No. Expts completed	Faculty Sign.
1	IV	Design & Analysis of Algorithms Lab	15CSL47	Sri. M. A. Chitale	12	03	
2		Micro Processors Lab	15CSL48	Sri .M.R. Pawar	13	02	
3	VI	System Software & Operating System	15CSL67	Smt. A. A. Daptardar	10	03	
4		Computer Graphics Lab & Mini Project	15CSL68	Sri. M. G. Huddar	09	02	

12/3/18

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CSE. Dept.
Academic
Syllabus Coverage
2017-18(Even)

Syllabus Covered from 05-02-2018 to 013-04-2018 (Up to 10 Week)

S. No.	Semester	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual No. of classes engaged	% of Syllabus covered	Faculty Sign.	S	A
1	IV	Engineering Mathematics-IV	15MAT41	Sri. S. A. Patil	31	32	64%			
2		Software Engineering	15CS42	Sri. M. A. Gholap	35	27	45%			
3		Design & Analysis of Algorithms	15CS43	Sri. S. M. Mayannavar	35	36	52		52%	80%
4		Micro Processors & Microcontrollers	15CS44	Sri Y. M. Naik	35	34	52%			
5		Object Oriented Concepts	15CS45	Sri. S.V. Manjaragi	33	31	44%			
6		Data Communication	15CS46	Sri. M. A. Chitale	41	33	50%			
1	VI	Cry. Network Security & Cyber Law	15CS61	Sri. M. A. Gholap	33	29	46%			
2		Computer Graphics & Visualization	15CS62	Sri. R.R. Patil	37	31	42%			
3		System Software & Compiler Design	15CS63	Sri. S. G. Gollagi	36	30	51			
4		Operating System	15CS64	Smt. A. A. Daptardar	36	33	60%		52%	80%
5		Data Mining & Data Warehousing	15CS651	Sri. M. R. Pawar	35	33	50			
6		Python Application Programming	15CS664	Sri. M. G. Huddar	33	30	62.5			
1	VIII	Software Architecture	10IS81	Smt. S. B. Hosagoudar	38	35	63%			
2		System Modeling & Simulation	10CS82	Dr. Parashuram Baraki	36	30	55%			
3		Information & Network Security	10CS835	Sri. K. K. Kamati	38	30	50%			
4		Adhoc Networks	10CS841	Sri Y. M. Naik	36	29	55%		55%	80%
5		Software Testing	10CS842	Sri. C. R. Belavi	32	29	50%			

Labs:

S.N	Sem.	Lab.	Sub. Code	Faculty	Actual no. of Expts	No. Expts completed	Faculty Sign.
1	IV	Design & Analysis of Algorithms Lab	15CSL47	Sri. M. A. Chitale	12	05	
2		Micro Processors Lab	15CSL48	Sri .M.R. Pawar	13	06	
3	VI	System Software & Operating System	15CSL67	Smt. A. A. Daptardar	10	06	
4		Computer Graphics Lab & Mini Project	15CSL68	Sri. M. G. Huddar	09	06	

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E&E Engg. Dept.
Academic
Time-Table
2017-18(Odd-Sem)

CONSOLIATED TIME TABLE 2017 – 2018 (R1)

w.e.f. 24/08/2017

Day / Time	Sem	9.00am-10.00am	10.00am - 11.00am		11.15am - 12.15pm	12.15 pm-1.15 pm		2.00 pm 3.00 pm	3.00 pm 4.00 pm	4.00 pm - 5.00 pm
Monday	III	EEM	AEC	Short break (11:00am to 11:15am)	T&G	M3	Lunch break (1:15 pm to 2:00 pm)	ECA*	T&P	AEC*
	V	PE	MC		ME	S&S *		MC Lab B3 (MPY)/ PE Lab B2(KBN)		
	VII	HV	VLSI		TC/PLC	EPU		PSS Lab B1(SGH) / HV Lab B3(RHN)		
Tuesday	III	AEC	T&G		M3	ECA		DSD*	T&G*	
	V	MC	ME		S&S	E&C		--		
	VII	IDA	TC/PLC		EPU	CTPS		PSS Lab B3(PMM)/ HV Lab B2(RHN)		
Wednesday	III	M3	T&G		ECA	DSD		EC Lab B1(SSB)/ M/C Lab B3(AUN)		
	V	ME	S&S		PE	PLC/RES		MC Lab B1 (MPY)/ PE Lab B3(KBN)		
	VII	EPU	TC/PLC		VLSI	CTPS		PSS Lab B2(SGH) HV Lab B1(RHN)		
Thursday	III	T&G	ECA		DSD	EEM		EC Lab B3(SSB)/ M/C Lab B2(BRU)		
	V	S&S	E&C		PLC/RES	T&P		--		
	VII	HV	CTPS		IDA	EPU		--		
Friday	III	ECA	DSD		EEM	AEC		EC Lab B2(MAM) /M/C Lab B1(BRU)		
	V	PLC/RES	S&S		PE	MC		MC Lab B2 (MPY) /PE Lab B1(KBN)		
	VII	CTPS	IDA		HV	VLSI		--		
Saturday	III	DSD	EEM		AEC	M3				
	V	E&C	PE		MC	ME				
	VII	HV	VLSI		IDA	TC/PLC				

Faculty details

III-Semester			V- Semester			VIII-Semester		
Staff Name	Sub.	Code	Staff Name	Sub.	Code	Staff Name	Sub.	Code
S I Shiva'math	M-3	15MAT31	P.M.Murari	ME	15EE51	H.R.Zinage	CTPS	10EE71
P.M.Murari	ECA	15EE32	M.P.Yanagimath	MC	15EE52	O.B.Heddurshetti	EPU	10EE72
A.U.Neshti	T&G	15EE33	O.B.Heddurshetti	PE	15EE53	S.D.Hirekodi	HV	10EE73
M.A. Mulla	AEC	15EE34	M.A. Mulla	SNS	15EE54	K.B.Nrgalur	IDA	10EE74
B.R.Umrane	DSD	15EE35	S.S.Birade A.U.Neshti	PLC RES	15EE562/ 15EE563	S.D.Hirekodi S.S.Birade	TC/PLC	10EE752/ 10EE756
H.R.Zinage	E&EM	15EE36	S.G.Huddar	E&C	15EE553	R H Nayak	VLSI	10EE764
B.R.Umrane/ A.U.Neshti	M/c Lab-1	15EEL37	M.P.Yanagimath	µc Lab	15EEL57	R H Nayak	HV Lab	10EEL77
S.S.Birade \M.A. Mulla	AE+LD Lab	15EEL38	K.B.Nrgalur	PE Lab	15EEL58	S.G.Huddar/ P.M.Murari	PSS Lab	10EEL78

(RHN)

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E&E Engg. Dept.
Academic
Time-Table (R-3)
2017-18 (Even-Sem)

CONSOLIDATED TIME TABLE 2017 – 2018 w.e.f. 05/02/2018

Day / Time	Sem	9.00am-10.00am	10.00am - 11.00am		11.15am - 12.15pm	12.15 pm-1.15 pm		2.00 pm 3.00 pm	3.00 pm 4.00 pm	4.00 pm - 5.00 pm
Monday	IV	EMT	TD	Short break (11:00am to 11:15am)	EMT	M-IV	Lunch break (1:15 pm to 2:00 pm)			
	VI	CAED	DSP		EMD	PSA-I				
	VIII	PSOC	EDEC		EADSM/EDS	RES		SEMINAR		
Tuesday	IV	M-IV	EMT		EM	PGE		EM-II Lab B1 OALI Lab B2		
	VI	PSA-I	DSP		CS	DSP		DSP Lab B1 CS Lab B2		
	VIII	EADSM/EDS	EDEC		RES	PSOC		SEMINAR		
Wednesday	IV	EM	OALI		TD	EMT		EM-II Lab B2 OALI Lab B3		
	VI	CS	EMD		DSP	CAED		DSP Lab B2 CS Lab B3		
	VIII	RES	EADSM/EDS		EDEC	PSOC				
Thursday	IV	M-IV	TD		OALI	PGE		EM-II Lab B3 OALI Lab B1		
	VI	EMD	CS		ST	PSA-I		DSP Lab B3 CS Lab B1		
	VIII	EDEC	EADSM/EDS		RES	PSOC				
Friday	IV	TD	PGE		EM	OALI				
	VI	CS	EMD		CAED	ST				
	VIII	PROJECT WORK			PROJECT WORK					
Saturday	IV	PGE	EM		OALI	M-IV				
	VI	ST	PSA-I							
	VIII	PROJECT WORK			PROJECT WORK					

Faculty details

IV-Semester			VI- Semester			VIII-Semester		
Staff Name	Sub.	Code	Staff Name	Sub.	Code	Staff Name	Sub.	Code
S.L.Patil	M-IV	15MAT41	K.B.Negalur	CS	15EE61			
S.G.Huddar	PGE	15EE42	P.M.Murari	PSA-I	15EE62	O.B.Heddurshetti	EDEC	10EE81
P.M.Murari	TD	15EE43	M.P.Yanagimath	DSP	15EE63	H.R.Zinage	PSOC	10EE82
R H Nayak	EM	15EE44	A.U.Neshti	EMD	15EE64	Dr.S.B.Hadadi	RES	10EE836
B.R.Umarane	EMT	15EE45	S.B.Patil	CAED	15EE651	S.D.Hirekodi/ S.G.Huddar	EADSM/ EDS	10EE842/ 844
S.S.Birade	OALI	15EE46	O.B.Heddurshetti	ST	15EE662			
A.U.Neshti	EM-II Lab	15EEL47	K.B.Negalur	CS Lab	15EEL67			
S.S.Birade	OALI Lab	15EEL48	R H Nayak	DSP Lab	15EEL68			

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09/02/18
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STAFF WORK LOAD OF ODD SEMESTER (AUG.TO NOV. -2017)

S. N	Name of the faculty	Sem	Subjects	
			Theory	Lab
1	Prof. B R Umarane	III	Digital System Devices	-
2	Prof. S B Patil	I	Basic Electrical Engg.	-
3	Prof. S D Hirekodi	VII	High Voltage Engg.	-
		VII	Testing & Commissioning of Electrical Equipments	--
4	Prof. Hema R Zinage	VII	Computer Techniques in Power System	
		III	Electrical & Electronic Measurements	
5	Prof. M P Yanagimath	V	Micro controller	Micro controller Lab
6	Prof. O B Heddurshetti	VII	Electrical Power Utilization	--
		V	Power Electronics	--
7	Prof. A U Neshti	III	Transformer & Generator	Electrical Machines-1 Lab
		V	Renewable energy sources	-
8	Prof. P M Murari	III	Electric Circuit Analysis	-
		V	Management & Entrepreneurship	
		VII	--	Power System Simulation Lab(2 Batch)
9	Prof. K B Negalur	V		Power Electronics Lab
		VII	Industrial Drives & Applications	-
10	Prof. M A Mulla	III	Analog Electronic Circuits	Electronics Lab (2-Batches)
		V	Signals & Systems	-
11	Prof. S S Birade	V	Programmable Logic Controllers	-
		VII	Programmable Logic Controllers	-
12	Prof. Sujata G Huddar	V	Estimating & Costing	
		V	Electrical Engineering Materials	Power System Simulation Lab(1 Batch)
13	Prof. Roopa H. Naik	VII	VLSI Circuit & Design	Relay & High voltage Lab

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E&E Engg. Dept.
Academic
Work Load
2017-18 (Even-Sem)

Work Load

S. No.	Faculty Name	Sem./ Div.	Strength	Subject/ Subject Code	Theory Hours/ Week	Seminar/Project Hours/ Week	Practical hours/ Week	Total Hrs /week	Staff Sign.
1.	Dr Hadadi Sudheendra	VIII	57	Renewable Energy Source / 10EE836	04	02 +2	--	08	
2.	Prof B. R. Umarane	IV	54	Electro Magnetic Theory /15EE45	04	02	--	06	
3.	Prof S. D. Hirekodi	VIII	40	Energy Audit & demand side Management/10EE842	04	02	--	06	
4	Prof S B Patil	II (C)	45	Basic Electrical Engineering/17ELE15/25	05	02 +2	--	12	
		VI	47	Computer Aided Electrical Drawing / 15EE651	03				
5.	Prof H R Zinage	II (D)	36	Basic Electrical Engineering/17ELE15/25	05	02	--	11	
		VIII	57	Power System Operation Control / 10EE82	04				
6.	Prof Mahesh Yanagimath	VI	47	Digital Signal Processing / 15EE63	04	02	--	06	
7.	Prof.Onkar Heddurshetti	VI	47	Sensors and Transducers/15EE662	03	02	--	09	
		VIII	57	Electrical Design Estimating Costing / 10EE81	04				
8.	Prof. Amit U Nesthi	VI	47	Electrical Machine Design / 15EE64	04	02	09	15	
		IV	54	Electrical Machines Lab-II / 15EEL47					
9.	Prof. Pramod Murari	IV	54	Transmission & Distribution / 15EE43	04	02	--	10	
		VI	47	Power System Analysis – 1 / 15EE62	04				
10.	Prof. Keshav Negalur	VI	47	Control Systems/ 15EE61	04	02	09	15	
		VI	47	Control Systems Lab / 15EEL67					
11.	Prof. Sagar ~~Birde~~ Birade	IV	54	Operational Amplifier & Linear IC / 15EE46	04	02	09	15	
		IV	54	Operational Amplifier & Linear IC Lab / 15EEL48					
12.	Prof. Sujata Huddar	VIII	17	Electrical Distribution System /10EE844	04	02	--	10	
		IV	54	Power Generation Engg. /15EE42	04				
13.	Prof. Roopa Nayak	IV	54	Electrical Motors / 15EE44	04	02	09	15	
		VI	47	Digital Signal Processing Lab / 15EEL68					

HOD 09/02

Principal 10/2

* 1 hour theory =2 units,
1 hr practical =1 unit

LESSON PLAN

Subject with code : VLSI circuit & Design / 10EE764 Lect. Per week : 04

Class : VII sem

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
5	Scaling models & scaling factors for device parameters.	04/10/17	23/10/17		
	Limitations of scaling.	6/10/17	25/10/17		
	Limits of interconnect & contact resistance.	6/10/17	27/10/17		
	Limits due to subthreshold currents.	7/10/17	28/10/17		30/10
	Limits on logic levels & supply voltage due to noise.	11/10/17	30/10/17		
	Limits due to current density & observations.	12/10/17	3/11/17		
	Problems	13/10/17			
	Problems.	13/10/17			
Planned Hrs: 06				Actual Hrs : 06	
6.	Architectural issues, switch logic	14/10/17	4/11/17		7/11
	Gate logic	25/10/17			
	Other forms of CMOS Logic	26/10/17	08/11/17		
	Combinational Logic	27/10/17	09/11/17		
	Sequential circuits	27/10/17			
	Dynamic Register element	28/10/17	10/11/17		
	Dynamic shift Register	02/11/17	10/11/17		
	Other system considerations.	03/11/17	11/11/17		13/11
Planned Hrs: 07				Actual Hrs : 06	

HOD PRINCIPAL

LESSON PLAN

Subject with code : CTPS Lect. Per week : 4

Class : 7th sem

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
V & VI	Economic Operation of Power System	10/10	10/10		
	Introduction, Performance curves				
	Economic generation scheduling neglecting losses e.g.: limits	11/10	11/10		
	" including gene	12/10	12/10		
	ratio limits & neglecting losses				
	Iterative techniques	13/10	12/10		
	Economic Dispatch including tra. losses	24/10	13/10		
	approximate penalty factor				
	Iterative technique for soln of economic	25/10	13/10		
	dispatch with losses				
Planned Hrs:			Actual Hrs :		
	Derivation of transmission loss formula	26/10	24/10		
	Numerical problems	27/10	25/10		
	Numerical problems	31/10	26/10, 27/10		
	Numerical problems	2/11	31/10		
	Optimal scheduling for Hydrothermal	3/11	2/11		
	plants - problem formulation, soln				
	procedure & algorithm	7/11	3/11		
Planned Hrs: 12			Actual Hrs : 12		

HOD PRINCIPAL

LESSON PLAN

Course : Control system Code : 15EE61 Lect. Per week : 04

Sem. : VI sem (2017-18) Even.

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
1	Introduction	1-2-18	5-2-18		
	Classification of Control system	2-2-18	6-2-18		
	Modelling of Mechanical system	6-2-18	6-2-18		
	elements, electrical systems.	7-2-18 8-2-18	7-2-18		
	Analogous system	9-2-18	8-2-18 9-2-18		
	Transfer function, SISO	13-2-18	15-2-18		
	procedure for deriving	14-2-18	16-2-18		
	transfer functions,	15-2-18	17-2-18 20-2-18		
	servomotors, synchros,	16-2-18	21-2-18 22-2-18		
	Gear trains.	20-2-18	22-2-18		
Planned Hrs: 11				Actual Hrs : 13	
2	Block diagram of a closed loop system.	21-2-18	23-2-18		
	procedure for drawing	22-2-18	27-2-18		
	block diagram + block dgm	23-2-18 27-2-18	28-2-18 1-3-18		
	reduction to find Transfer function.	28-2-18 3-3-18	8-3-18 9-3-18		
	SFG: Construction of SFG	2-3-18 8-3-18	13-3-18		
	Basic properties of SFG	9-3-18	14-3-18		
	SFG algebra.	13-3-18	15-3-18		
	construction of SFG for	14-3-18 15-3-18	16-3-18 20-3-18		
	Control system.	16-3-18	22-3-18 23-3-18		
Planned Hrs: 13				Actual Hrs : 13	

HOD PRINCIPAL

LESSON PLAN

Course : Energy Auditing & D.S.M (2017-18 - Even) Code : 10EE842 Lect. Per week : 04

Sem. : VIII

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
V	· Causes of low Power factor	06/02/18	6/02/18		
	· Dis-advantages of Low Power factor & Advantages of high Power factor	07/02/18	07/02/18		
	· Power factor improvement equipment	08/02/18	08/02/18		
	· Calculation of power factor correction	12/02/18	15/2/18		
	· Numerical examples	14/02/18	26/2/18		
	· Most economical power factor	15/02/18	27/2/18		
	· Location of capacitors	19/02/18	27/2/18		
Planned Hrs: 07				Actual Hrs : 06	
VI	· Energy efficient Motors	20/02/18	08/3/18		
	· Lighting Basics	21/02/18	12/3/18		
	· Good practices in Lighting	22/02/18	12/3/18		
	· Electrical Tariff	26/02/18	28/2/18		
	· Concept of ABT	27/02/18	01/3/18		
Planned Hrs: 05				Actual Hrs : 04	

HOD

PRINCIPAL

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Hirasugar Institute of Technology, Nidasoshi.

Inculcating Values & Promoting Prosperity

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E&E Engg. Dept.
Academic
Syllabus Coverage
2017-18(Odd)

Syllabus Covered from 07/8/2016 to 04/11/2017 (Up to Week No. 13)

S. No.	Semester	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual no. of classes engaged	% of Syllabus covered	No. of modules completed	faculty Sign.
1	III	Engineering Mathematics-III	15EE31	Prof. S I Shivamogimath	46	45	87%	4.4	
2		Electric Circuit Analysis	15EE32	Prof. P.M.Murari	40	39	80%	4	
3		Transformers & Generators	15EE33	Prof. A.U.Neshti	49	46	86%	4.3	
4		Digital System Design	15EE34	Prof. B.R.Umrane	46	52	80%	4.1	
5		Analog Electric Circuits	15EE35	Prof. M.A. Mulla	47	50	80%	4	
6		Electrical & Electronic Measurements	15EE36	Prof. H.R.Zinage	44	40	80%	4	
1	V	Management & Entrepreneurship	15EE51	Prof. P.M.Murari	38	37	80%	4	
2		Microcontroller	15EE52	Prof. M.P.Yanagimath	43	42	80%	4	
3		Power Electronics	15EE53	Prof. O.B.Heddurshetti	38	45	84%	4	
4		Signals & Systems	15EE54	Prof. M.A. Mulla	49	49	85%	4.5	
5		Estimating & Costing	15EE553	Prof. S.G.Huddar	32	33	80%	04	
6		Programmable Logic Controller	15EE564	Prof.S.S.Birade	30	34	80%	04	
7		Renewable Energy Sources	15EE663	Prof. A.U.Neshti	22	28	78	3.9	
1	VII	Computer Techniques in Power System	10EE71	Prof. H.R.Zinage	41	37	72%	5.1 unit	
2		Electrical Power Utilization	10EE72	Prof. O.B.Heddurshetti	40	40	80%	5.5 unit	
3		High Voltage Engineering	10EE73	Prof. S.D.Hirekodi	40	37	73%	5.8	
4		Industrial Drives & Applications	10EE74	Prof. K.B.Negalur	40	39	74%	5.2 unit	
5		Testing & Commissioning of Electrical Equipments	10EE752	Prof. S D Hirekodi	38	36	75%	6	
6		Programmable Logic Controller	10EE756	Prof.S.S.Birade	38	35	70%	06	
7		VLSI Circuit & Design	10EE764	Prof. R H Nayak					

Labs:

S.N	Sem.	Lab.	Sub. Code	Faculty	Actual no. of Expts	No. Expt.s completed	Sign.
1	III	Electrical Machne-I Lab	15EEL37	Prof. A.U.Neshti/Prof. B.R.Umarane	10	09	
2		Electronics Lab	15EEL38	Prof. S.S.Birade /M.A.Mulla	10	09	
3	V	Microcontroller Lab	15EEL57	Prof. M.P.Yanagimath	13	11	
4		Power Electronics Lab-15	15EEL58	Prof. K.B.Negalur	12	10	
5	VII	Relay & High voltage La	10EEL77	Prof. R H Nayak			
6		Power System Simulation Lab	10EEL78	Prof. P.M.Murari/Prof. S.G.Huddar	09	09	

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E&E Engg. Dept.
Academic
Syllabus coverage-D12
2017-18(Even)

Syllabus Covered from 5th February 2018 to 17th March 2018 (Up to week No. : 06)

S. No.	Class	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual no. of classes engaged	% of syllabus covered	Staff Sign.	
1	IV	Engg. Maths -IV	15MAT41	Prof. S L Patil	19	17	45%		35.8%
2		Power Generation & Economics	15EE42	Prof. S G Huddar	21	19	38		
3		Transmission & Distribution	15EE43	Prof. P. M. Murari	21	23	32		
4		Electrical Motors	15EE44	Prof. R H Nayak	16	18	36%		
5		Electro Magnetic Field Theory	15EE45	Prof. B R Umarane	16	20	20		
6		Operational Amplifier & Linear IC's	15EE46	Prof. S S Birade	20	20	34%		
1	VI	Control System	15EE61	Prof.K.B.Negalur	23	23	40%		36%
2		Power System Analysis - I	15EE62	Prof.P.M.Murari	21	19	40		
3		Digital Signal Processing	15EE63	Prof.M.P.Yanagimath	17	19	34%		
4		Electrical Machine Design	15EE64	Prof.A.U.Neshti	22	16	32%		
5		Computer Aided Electrical Drawing	15EE651	Prof.S.B.Patil	14	12	40		
6		Sensors & Transducers	15EE662	Prof.O.B.Heddurshetti	16	17	30%		
1	VIII	Electrical Design,Estimating and Costing	10EE81	Prof. O.B.Heddurshetti	19	16	32%		28.5%
2		Power System Operation & control	10EE82	Prof. H.R Zinage	14	14	28%		
3		Renewable Energy Sources	10EE832	Dr.S.B.Hadadi	-	-	-	-	
4		Electrical Distribution Systems	10EE844	Prof. S G Huddar	14	14	27%		
5		Energy Auditing & Demand Side management	10EE842	Prof. S D Hirekodi	14	14	30		

Labs:

S.N	Faculty	Sem.	Lab/Code	Actual no. of Expts.	No. Expt.s completed	Sign.
1	Prof. A.U.Neshti	IV	Electrical Machines -II Lab-15EEL47	12	05	
2	Prof. S.S.Birade	IV	Operational Amplifier & Linear IC's Lab- 15EEL48	10	03	
3	Prof. K.B.Negalur	VI	Control Systems Lab - 15EEL67	11	03	
4	Prof. R H Nayak	VI	Digital Signal Processing Lab - 15EEL68	12	03	

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HIRASUGAR INSTITUTE OF TECHNOLOGY, NIDASOSHI.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGG

TENTATIVE TIME TABLE ODD SEMESTER 2017-18 W.E.F: 07/08/17

Time/ Day	SEM	9:00 to 10:00AM	10:00 to 11:00AM	11:15 to 12:15PM	12:15 to 1:15PM	2:00 to 3:00 PM	3:00 to 4:00 PM	4:00 to 5:00 PM
MON	III	DE (DBM)	NA (PVP)	AE (SBA)	EI (NKH)	AE/DE LAB(B1/B2) (SBA/DBM)		
	V	DSP/V.HDL LAB(B1/B2) (BPK/SSP)			Placement Class	V.HDL (SSP)	C++ (VBD)	DSP (SSI)
	VII	CCN (NKH)	ESD (NMP)	PE (DMK)	DAA (VBD)	PE/VLSI LAB(B1/B2) (DMK/SSK)		
TUE	III	Placement Class	M3 (SAP)	EI (NKH)	NA (PVP)	AE/DE LAB(B2/B1) (SBA/DBM)		
	V	DSP/V.HDL LAB(B2/B1) (SSI/SSP)			ME (SSM)	ITC (VGK)	OS (NMP)	DSP (SSI)
	VII	RTS/IP (SSM/BPK)	DAA (VBD)	PE (DMK)	OFC (MMG)	PE/VLSI LAB(B2/B1) (DMK/SSK)		
WED	III	M3 (SAP)	NA (PVP)	EMT (SSK)	AE (SBA)	AE/DE LAB(B3/B4) (SBA/PVP)		
	V	ITC (VGK)	DSP (SSI)	V.HDL (SSP)	C++ (VBD)	ME (SSM)		
	VII	DAA (VBD)	RTS/IP (SSM/BPK)	CCN (NKH)	ESD (NMP)	PE/VLSI LAB(B3/B4) (DMK/SSK)		
THU	III	M3 (SST)	AE (SBA)	EMT (SSK)	DE (DBM)	AE/DE LAB (B4/B3) (SBA/DBM)		
	V	OS (NMP)	ME (SSM)	V.HDL (SSP)	C++ (VBD)	DSP/V.HDL LAB (B3/B4) (SSI/SSP)		
	VII	PE (DMK)	OFC (MMG)	ESD (NMP)	RTS/IP (SSM/BPK)	PE/VLSI LAB(B4/B3) (PVP/SSK)		
FRI	III	EMT (SSK)	DE (DBM)	M3 (SST)	NA (PVP)	EI (NKH)		
	V	ME (SSM)	OS (NMP)	C++ (VBD)	ITC (VGK)	DSP/V.HDL LAB (B4/B3) (BPK/SSP)		
	VII	OFC (MMG)	PE (DMK)	CCN (NKH)	ESD (NMP)			
SAT	III	EMT (SSK)	DE (DBM)	AE (SBA)	EI (NKH)			
	V	ITC (VGK)	DSP (SSI)	V.HDL (SSP)	OS (NMP)			
	VII	OFC (MMG)	CCN (NKH)	RTS/IP (SSM/BPK)	DAA (VBD)			

Seating Arrangement: III Sem : (1st Floor) D-211
V Sem : (1st Floor) D-210
VII Sem : (2nd Floor) D-301

Note:

III Sem : Friday 3 TO 5 MathDip For Lateral Entry
V Sem : Friday 3 TO 5 CIP/CIV For Lateral Entry

Coordinator H.O.D Principal

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ECE
Academic
Time Table Rv:00
2017-18(EVEN)

Time/ Day	SEM	9:00 to 10:00AM	10:00 to 11:00AM	11:15 to 12:15PM	12:15 to 1:15PM	2:00 to 3:00 PM	3:00 to 4:00 PM	4:00 to 5:00 PM
MON	IV	S&S	PCS	PCS	M4	µp/ LIC LAB (B1/B2) PVP/SBA		
	VI	CCN	ARM	DSS	DC	EMD/ CN LAB (B1/B2) SSP/NMP		
	VIII	NS/HPCN	RTOS/MMC	WC	DSS	Project/Seminar		
TUE	IV	PCS	µp	M4	CS	µP/ LIC LAB (B2/B1) PVP/DBM		
	VI	EMD/ CN LAB(B2/B1) SSP/NMP			ARM	DC	DSS	Verilog
	VIII	RTOS/MMC	WC	DSS	NS/HPCN	Project/Seminar		
WED	IV	M4	S&S	PCS	CS	µP/ LIC LAB (B3/B4) BPK/DMK		
	VI	EMD/ CN LAB (B3/B4) SSP/NMP			DSS	VLSI	ARM	CCN
	VIII	DSS	RTOS/MMC	WC	NS/HPCN	Project/Seminar		
THUR	IV	µp	M4	S&S	LIC	µP/ LIC LAB (B4/B3) BPK/VBD		
	VI	Verilog	DSS	VLSI	ARM	DEPARTMENTAL ACTIVETIES		
	VIII	WC	DSS	RTOS/MMC	NS/HPCN	Project/Seminar		
FRI	IV	LIC	CS	µp	S&S	DEPARTMENTAL ACTIVETIES		
	VI	DC	Verilog	CCN	VLSI	EMD/ CN LAB (B4/B3) SSP/NMP		
	VIII	Project/Seminar				DEPARTMENTAL ACTIVETIES		
SAT	IV	CS	LIC	LIC	µp	**IV Sem : (1st Floor) – D-211**		
	VI	VLSI	CCN	Verilog	DC	**VI Sem : (1st Floor) – D-210**		
	VIII	Project/Seminar				**VIII Sem : (2nd Floor) – D-301**		

Coordinator
Dr. Shilpa Shingri

HOD
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Electronics & Commn. Engg
HIT NIDASOSHI.

Principal

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HIRASUGAR INSTITUTE OF TECHNOLOGY, NIDASOSHI.
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGG
Teaching Faculty Work Load for Odd Semester for Academic Year (2017-18)

S.N	Name of the Faculty	Sem	Subjects		Work Load	Total Units
			Theory	Lab		
1	Dr. V. G. Kasabegoudar	V	ITC		8+8	16
		I	BE			
2	Prof. M. M. Gadag	VII	OFC		8+8+6	22
		I	BE			
		HOD work				
3	Prof. S.B. Akkole	III	AE	AEC Lab(4)	8+12	20
4	Prof. V. B. Dhere	V	C++		8+8	16
		VII	DAA			
5	Prof. S. S. Kamate	III	EM		8+12	20
		VII		VLSI Lab(4)		
6	Prof. S. S. Patil	V	Verilog HDL	Verilog HDL Lab(4)	8+12	20
7	Prof. N.K. Honnagoudar	III	EI		8+8	16
		VII	CCN			
8	Prof. N. M. Patel	V	OS		8+8	16
		VII	ESD			
9	Prof. D. M. Kumbhar	VII	PE	PE Lab (3)	8+9	17
10	Prof. D. B. Madihalli	III	DE	DE Lab (3)	8+9	17
11	Prof. P. V. Patil	III	NA	DE Lab (1)	8+3+3	14
		VII		PE Lab (1)		
12	Prof. S.S. Ittannavar	V	DSP	DSP Lab(2)	8+6	14
13	Prof. S. S. Malaj	VII	RTS		8+8	16
		V	M&E			
14	Prof. B. P. Khot	V		DSP Lab(2)	8+6	14
		VII	IP			

M.M. Gadag 7/8/17
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ECE
Work Load
2017-18 (Even)
Rev. 01

S. No.	Faculty Name	Sem./ Div.	Strength	Subject/ Subject Code	Theory Hrs./ Week	Practical/Seminar/Project Hrs./ Week	Work Load (Hrs./Week)	Staff Sign.
1.	Dr. Veeresh G. Kasabegoudar	IV	41	Principles of Commn System../15EC45	04	--	04	
2.	Dr. S. B. Shrigiri	I-B	41	Basic Electronics /17ELN15	05	--	09	
		IV	41	Signals and Systems /15EC44	04	--		
3.	Prof. M. M. Gadag	I-A	38	Basic Electronics /17ELN15	05	--	05	
4	Prof. S. B. Akkole	VI	55	Digital Switching Systems (Ele) /15EC654	04	--	13	
		VIII	76	Project Coordinator/10ECP85	--	06		
		IV	41	AC + LIC Lab /15ECL48	--	03		
5.	Prof. S. S. Kamate	VI	55	VLSI /15EC63	04		10	
		VIII	76	Seminar Coordinator /10ECS86		06		
6.	Prof. V. B. Dhere	VIII	76	Digital Switching Systems /10EC82	04		07	
		VI	55	AC + LIC Lab /15ECL48		03		
7.	Prof. S. S. Patil	VI	55	ARM µC & ES /15EC62	04		16	
		VI	55	Embedded Controller Lab /15ECL67		12		
8.	Prof. N. K. Honnagoudar	VIII	76	Wireless communication /10EC81	04	--	08	
		VI	55	CCN /15EC64	04	--		
9.	Prof. N. M. Patel	VIII	76	NS (Ele)10EC832	04		16	
		VI	55	CCN Lab (04 Batches)15ECL68	--	12		
10.	Prof. D. M. Kumbhar	VI	55	DSS using Verilog (Ele) /15EC663	04		07	
		IV	41	AC + LIC Lab (01 Batch)15ECL48	--	03		
11.	Prof. S. S. Malaj	VIII	76	RTOS (Ele)10EC842	04	--	08	
		IV	41	Control Systems /15EC43	04	--		
12.	Prof. D. B. Madihalli	IV	41	Linear Integrated circuits /15EC46	04		07	
		IV	41	AC + LIC Lab /15ECL48		03		
13.	Prof. P. V. Patil	IV	41	Microprocessor (µP) /15EC42	04	--	10	
		IV	41	Microprocessor µP Lab (02 Batches) 15ECL47	--	06		
14.	Prof. S. S. Ittannavar	VI	55	Digital Communication/15EC61	04	--	08	
		VIII	76	MMC (Ele)10EC841	04	--		
15.	Prof. B. P. Khot	VIII	76	HPCN (Ele)10EC834	04	--	10	
		IV	41	Microprocessor µP Lab (02 Batches)/15ECL47	--	06		

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E&C Engg. Dept.
Academic
Syllabus Coverage
2017-18(Odd)

Syllabus Covered from 07/08/2017 to 09/09/2017 (Up to Week No. 05) 1st IA

S. No.	Semester	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual no. of classes engaged	% of Syllabus covered	No. of modules completed	Faculty Sign
1	III	Engineering Maths -III	15MAT31	S. S. Thabaj/S A Patil	21	20	38%	1.8	
2		Analog Electronics	15EC32	S B Akkole	18	18	30	1.5	
3		Digital Electronics	15EC33	D B Madihalli	16	16	24%	1.2	
4		Network analysis	15EC34	P V Patil	16	16	20%	1.0	
5		Electronics Instrumentation	15EC35	N K Honnagoudar	17	14	18	0.9	
6		Engineering Electromagnetics	15EC36	S S Kamate	~~18~~ 18	~~16~~ 19	20%	1.0	
1	V	Management and Entrepreurship	15EC51	S S Malaj	18	16	27%	1.2	
2		Digital Signal Processing	15EC52	S S Ittannavar	18	17	28%	1.5	
3		Verilog HDL	15EC53	S S Patil	18	18	30%	1.5	
4		Information & Theory Coding	15EC54	V G Kasabegoudar	18	13	20%	0.9	
5		Operating system	15EC55	N M Patel	18	15	30	1.5	
6		OOP Using C++	15EC56	V B Dhere	14	13	27%	1.3	
1	VII	Computer Communication Networks	10EC71	N. K. Honnagoudar	18	17	25%	2	
2		Optical Fiber Communication	10EC72	M M Gadag	18	20	28%	2 units	
3		Power Electronics	10EC73	D M Kumbar	13	12	23%	1.4	
4		Embedded System Design	10EC74	N M Patel	18	15	25	2 units	
5		DSP Algorithms & Architecture	10EC751	V B Dhere	15	15	26.92	1.8 units	
6		Real Time Systems	10EC762	S S Malaj	18	15	27%	1.9 units	
		Image Processing	10EC763	B P Khot	18	16	28%	1.6 units	

Labs:

S.N	Sem.	Lab.	Sub. Code	Faculty	Actual no. of Expts	No. Expt.s completed	Faculty Sign
1	III	Analog Electronics Lab	15ECL37	S B Akkole	12	3	
2		Digital Electronics Lab	15ECL38	D B Madihalli/P V Patil	12	4	
3	V	Digital Signal Processing Lab	15ECL57	S S Ittannavar/B P Khot	13		
4		Verilog HDL	15ECL58	S S Patil	12	4	
5	VII	VLSI Lab	10ECL77	S S Kamate	12	5	
6		Power Electronics Lab	10ECL78	D M Kumbar/P V Patil	13	2	

HOD 11/9/

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E&C Engg. Dept.	
Academic	
Syllabus Coverage	
2017-18(Odd)	

Syllabus Covered from 09/10/2017 to 14/10/2017 (Up to Week No. 10) 2nd IA

S. No.	Semester	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual no. of classes engaged	% of Syllabus covered	No. of modules completed	Faculty Sign
1	III	Engineering Maths -III	15MAT31	S. S. Thabaj/S A Patil	32	32	70%	3.1	
2		Analog Electronics	15EC32	S B Akkole	30	30	60%	2.8	
3		Digital Electronics	15EC33	D B Madihalli	28	27	41%	2.1	
4		Network analysis	15EC34	P V Patil	28	28	46%	2.4	
5		Electronics Instrumentation	15EC35	N K Honnagoudar	27	26	45%	2.2	
6		Engineering Electromagnetic	15EC36	S S Kamate	~~34~~ 32	38	40%	2	
1	V	Management and Entrepreurship	15EC51	S S Malaj	35	33	55%	2.7 modules	
2		Digital Signal Processing	15EC52	S S Ittannavar	33	33	48%	2.4	
3		Verilog HDL	15EC53	S S Patil	33	32	60%	3 modules	
4		Information & Theory Coding	15EC54	V G Kasabegoudar	~~26~~ 33	26	45%	2.2	
5		Operating system	15EC55	N M Patel	33	26	55%	2.8 M	
6		OOP Using C++	15EC56	V B Dhere	33	33	65%	3+ mod	
1	VII	Computer Communication Networks	10EC71	N. K. Honnagoudar	28	25	52	4.2	
2		Optical Fiber Communication	10EC72	M M Gadag	35	32	58%	4.0 units	
3		Power Electronics	10EC73	D M Kumbar	33	27	50%	4 units	
4		Embedded System Design	10EC74	N M Patel	33	25	50%	4 units	
5		DSP Algorithms & Architecture	10EC751	V B Dhere	33	33	63.4%	5.2 units	
6		Real Time Systems	10EC762	S S Malaj	28	26	59%	4 units	
		Image Processing	10EC763	B P Khot	30	27	39%	3.1 unit	

Labs:

S.N	Sem.	Lab.	Sub. Code	Faculty	Actual no. of Expts	No. Expt.s completed	Faculty Sign
1	III	Analog Electronics Lab	15ECL37	S B Akkole	12	6	
2		Digital Electronics Lab	15ECL38	D B Madihalli/P V Patil	12	7	
3	V	Digital Signal Processing Lab	15ECL57	S S Ittannavar/B P Khot	13	7	
4		Verilog HDL	15ECL58	S S Patil	12	6	
5	VII	VLSI Lab	10ECL77	S S Kamate	12	7	
6		Power Electronics Lab	10ECL78	D M Kumbar/P V Patil	13	6	

H.M. Yedag

HOD

Electronics & Commn. Engg

S J P N Trust's

Hirasugar Institute of Technology, Nidasoshi.

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E&C Engg. Dept.
Academic
Syllabus coverage
2017-18(Even)

Syllabus Covered from 05/02/2018 to 03/03/2018 (Up to 4th Week) Before 1st IA

S. No.	Class	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual no. of classes engaged	% of Syllabus covered	Staff Sign.
1	IV	Engg Maths-IV	15MAT41	Prof. S S Thabaj	13	12	30%	
2		Microprocessor	15EC42	Prof. P V Patil	14	12	17%	
3		Control Systems	15EC43	Prof. S S Malaj	13	13	18%	
4		Signal & Systems	15EC44	Dr. S B Shrigiri	15	13	20%	
5		Principles of Communication System	15EC45	Dr. V G Kasabegoudar	14	11	16%	
6		Linear integrated circuits	15EC46	Prof. D B Madihalli	14	14	10%	
1	VI	Digital Communication	15EC61	Prof. S S Ittannavar	14	14	26%	
2		ARM Microcontroller & Embedded systems	15EC62	Prof. S S Patil	15	13	15%	
3		VLSI Design	15EC63	Prof. S S Kamate	14	14	19%	
4		Computer Communication Network	15EC64	Prof. N K Honnagoudar	14	13	18%	
5		Digital switching Systems	15EC654	Prof. S B Akkole	14	14	28	
6		Digital System Design using Verilog	15EC663	Prof. D M Kumbhar	15	11	19%	
1	VIII	Wireless Communication	10EC81	Prof. N K Honnagoudar	14	10	25.5	
2		Digital Switching Systems	10EC82	Prof. V B Dhere	15	12	28.84	
3		Network Security	10EC832	Prof. N M Patel	14	10	17.30%	
4		High-performance Computer Network	10EC834	Prof. B P Khot	14	9	19.23%	
5		Multimedia Communication	10EC841	Prof. S S Ittannavar	14	10	19.23%	
6		Real-time Operating Systems	10EC842	Prof. S S Malaj	14	12	23%	

Syll, 47%. 18.5%, 87%.

21.0%, 78%.

22%, 81%.

Labs:

S.N	Faculty	Sem.	Lab/Code	Actual no. of Expts.	No. Expts completed	Sign.
1	Prof. P V Patil & Prof. B P Khot	IV	Microprocessor Lab	12	03	
2	Prof. S B Akkole, Prof. V B Dhere, Prof. D M Kumbar & Prof. D B Madihalli	IV	Linear ICs & Communication Lab	12	02	
3	Prof. S S Patil	VI	Embedded Controller Lab	12	00	
4	Prof. N M Patel	VI	Computer Network Lab	12	02	

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E&C Engg. Dept.
Academic
Syllabus coverage
2017-18(Even)

Syllabus Covered from 05/02/2018 to 10/04/2018 (Up to 10th Week) Before 2nd IA

S. No.	Class	Subject	Sub. Code	Faculty	No. of Classes as per Time Table	Actual no. of classes engaged	% of Syllabus covered	Staff Sign.	Average
1	IV	Engg Maths-IV	15MAT41	Prof. S S Thabaj	31	29	62%		53.83%
2		Microprocessor	15EC42	Prof. P V Patil	32	32	60% (3)		
3		Control Systems	15EC43	Prof. S S Malaj	34	32	55%		
4		Signal & Systems	15EC44	Prof. S S Kamate	32	24	50%		
5		Principles of Communication System	15EC45	Dr. V G Kasabegoudar	34	30	~~58~~ 50%		
6		Linear integrated circuits	15EC46	Prof. D B Madihalli	30	26	46%		
1	VI	Digital Communication	15EC61	Prof. S S Ittannavar	31	33	52%		54.16%
2		ARM Microcontroller & Embedded systems	15EC62	Prof. S S Patil	33	30	55%	SSP	
3		VLSI Design	15EC63	Prof. S S Kamate	32	23	40%		
4		Computer Communication Network	15EC64	Prof. N K Honnagoudar	32	26	58%		
5		Digital switching Systems	15EC654	Prof. S B Akkole	32	32	65%		
6		Digital System Design using Verilog	15EC663	Prof. D M Kumbhar	31	29	55%		
1	VIII	Wireless Communication	10EC81	Prof. N K Honnagoudar	31	27	55%		52.17%
2		Digital Switching Systems	10EC82	Prof. V B Dhere	31	25	58%		
3		Network Security	10EC832	Prof. N M Patel	31	24	48.07%		
4		High-performance Computer Network	10EC834	Prof. B P Khot	29	24	48%		
5		Multimedia Communication	10EC841	Prof. S S Ittannavar	31	24	44%		
6		Real-time Operating Systems	10EC842	Prof. S S Malaj	28	25	60%		

Labs:

S.N	Faculty	Sem.	Lab/Code	Actual no. of Experiments.	No. Expts completed	Sign.
1	Prof. P V Patil & Prof. B P Khot	IV	Microprocessor Lab	12	07	
2	Prof. S B Akkole, Prof. V B Dhere, Prof. D M Kumbar & Prof. D B Madihalli	IV	Linear ICs & Communication Lab	12	07	
3	Prof. S S Patil	VI	Embedded Controller Lab	12	9	
4	Prof. N M Patel	VI	Computer Network Lab	12	08	

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LESSON PLAN

Subject with code : Digital Electronics - 15EC33 Lect. Per week : 04

Class : III rd sem

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
1	Definition of combinational logic, canonical forms	07/08	07/08 10/08	DBM	
	Generation of switching eqn's	10/08	11/08 12/08	DBM	
	from truth table. K-map -	11/08	16/08 17/08	DBM	
	for 3, 4, 5 variables,	12/08	19/08 21/08	DBM	
	Donot care terms, simplify	14/08	24/08 29/8	DBM	
	maltem eqns, Q-M mini	17/08	31/8	DBM	
	mization technique, QM -	18/08	31/8	DBM	
	using donot care terms	19/08	01/09	DBM	
	Reduced Prime Implicant Tables	21/08	04/09	DBM	
		24/08	07/09	DBM	
Planned Hrs: 10				Actual Hrs : 14	
2	General Approach to combi	26/08	08/09	DBM	
	nation logic design, Decoders,	28/08	09/09 11/09	DBM	
	BCD decoders, encoders,	31/08	18/09(1) 18/09(2)	DBM	
	digital multiplexer using -	01/09	21/09 22/09	DBM	
	multiplexers as Boolean function	04/09	23/09 28/09	DBM	
	generators.	07/09	06/10	DBM	
	Adders, subtractors and	08/09	07/10	DBM	
	Cascading full Adders.	09/09	09/10(1) 09/10(2)	DBM	
	Look Ahead carry generators,	11/09	12/10	DBM	
	Binary Comparators.	18/09	12/10	DBM	
Planned Hrs: 10				Actual Hrs : 14	

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LESSON PLAN

Subject with code : Image Processing 10EC763 Lect. Per week : 4

Class : VII Sem

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
Unit 1	Introduction				
	What is digital Image Processing	8/8/17	9/8/17	Bchate	
	Aplications of Image Processing	9/8/17	10/8/17	Bchate	
	Fundamental Steps in Image	10/8/17	12/8/17	Bchate	
	Processing				
	Components of Image Processing	12/8/17	16/8/17	Bchate	
	System				
	Elements of visual perception	16/8/17	17/8/17	Bchate	
Planned Hrs: 6				Actual Hrs : 6	
Unit 2	Image sensing and acquisition	17/8/17	19/8/17	Bchate	
	Image formation Model	19/8/17	22/8/17	Bchate	
	Image Sampling and quantization	22/8/17	23/8/17	Bchate	
	Some basic Relationships	23/8/17	24/8/17	Bchate	
	between pixels				
	Linear Operations	24/8/17	26/8/17	Bchate	
	Non-linear Operations	26/8/17	29/8/17	Bchate	
Planned Hrs: 6				Actual Hrs : 6	

HOD PRINCIPAL

LESSON PLAN

Course : D.S.D using Verilog. Code : 15EC663 Lect. Per week : 4

Sem. : VI

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
1.	Digital Systems and Embedded systems.	6-2	6-2		
	Real world circuits.	8-2	9-2		
	Models	9-2	10-2		
	Design Methodology	10-2	(8/2, 12/2) (Tojo) 15-2		
	Combinational components and circuits.	15-2	17-2		
	Verification of combinational circuits.	16-2	20-2		
	Sequential Datapaths and control	20-2	22, 23 -2		
	Sequential Datapaths & control.	22-2	24, 27 -2		
	clocked synchronous Timing Methodology.	23-2	1-3, 8-3		
	Clocked synchronous Timing Methodology	24-2	9-3		
Planned Hrs: 10				Actual Hrs :	
2.	General concepts.	27-2	13-3		
	Memory - asynchronous static RAM.	1-3	15-3		
	Synchronous static RAM.	2-3 Holi	16-3		
	Multiport memories.	3-3 Nagle meeting	17-3		
	Dynamic RAM.	8-3	19-3		
	ROM.	9-3	23-3		
	Error Detection	10-3 Women's day	24-3		
	Examples.	13-3	24-3		
	Error correction	15-3	25-3		
	Examples.	15-3	25-3		
Planned Hrs: 9				Actual Hrs :	

HOD

PRINCIPAL

LESSON PLAN

Course : High Performance Computer Network Code : 10EC834 Lect. Per week : 4

Sem. : VIII

Module/ Unit	Details of content to be taught	Date		Signature	
		Planned	Actual	Faculty	HOD
Unit 1	History of Communication Network	5/2/18	8/2/18	Bchots	
	Networking Principles brief explanation	6/2/18	19/2/18	Bchots	
	Future Networks, Internet	7/2/18	20/2/18	Bchots	
	Pure ATM Network	8/2/18	21/2/18	Bchots	
	Cable TV Network.	12/2/18	22/2/18	Bchots	
	Wireless Network System.	15/2/18	26/2/18	Bchots	
Planned Hrs: 6				Actual Hrs : 6	
Unit 2	Network Applications.	19/2/18	27/2/18	Bchots	
	Traffic characterization and QOS	19/2/18	27/2/18	Bchots	
	Network services	20/2/18	28/2/18	Bchots	
	High Performance Networks	21/2/18	1/3/18	Bchots	
	Network Elements	26/2/18	8/3/18	Bchots	
	Layered architecture	27/2/18	8/3/18	Bchots	
	Open data Network. Model, Network	28/2/18	12/3/18	Bchots	
	Architecture, Network Bottlenecks	1/3/18	13/3/18	Bchots	
Planned Hrs: 7				Actual Hrs : 6	

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MECH
Academic
Consolidated Time Table
2017-18 (EVEN)

DAY	TIME / SEM	9.00AM-10.00AM	10.00AM-11.00AM	Break-11:00AM-11:15AM	11.15AM-12.15PM	12.15PM-01.15PM	Lunch break-1.015pm To 2.00pm	02.00PM-03.00PM	03.00PM-04.00PM	04.00PM-05.00PM
MONDAY	IV(A)	KOM	M&M		ATD	FM		M&M Lab(A1) / M/c Shop(A2)		
								MATDIP**		
	IV(B)	ATD	KOM		FM*	MAT-IV		CAMD Lab(B1) / M&MT Lab(B2) / F&F Lab(B3)		
								MATDIP**		
	VI(A)	HT	FEA		CIM*	MF		HT LAB(A1) / M&A LAB(A2)		
	VI(B)	FEA	DOME-II*		DOME-II	CIM		HT	TQM	MF
	VIII(A)	CE	OM		AE	PPE		PROJECT WORK		
	VIII(B)	PPE	OM		AE	CE		PROJECT WORK		
TUESDAY	IV(A)	ATD	KOM		MAT-IV	ATD*		M&M Lab(A2) / M/c Shop(A3)		
	IV(B)	MCW	KOM		FM	ATD		CAMD Lab(B4) / M&MT Lab(B1) / F&F Lab(B2)		
	VI(A)	TQM	FEA		HT*	FEA*		HT LAB(A2) / M&A LAB (A3)		
	VI(B)	TQM	DOME-II		CIM	FEA		HT*	MF	
	VIII(A)	OM	CE		PPE	AE		PROJECT WORK		
	VIII(B)	PPE	OM		CE	AE		PROJECT WORK		
WEDNESDAY	IV(A)	MAT-IV	ATD		M&M	ATD*		M&M Lab(A3) / M/c Shop(A4)		
	IV(B)	KOM	MAT-IV		MCW	CAMD		CAMD Lab(B3) / M&MT Lab(B4) / F&F Lab(B1)		
	VI(A)	FEA	CIM		TQM	FEA*		HT LAB(A3) / M&A LAB (A1)		
	VI(B)	CIM	DOME-II		DOME-II	FEA				
	VIII(A)	AE	CE		OM	PPE		SEMINAR		
	VIII(B)	CE	PPE		OM	AE		SEMINAR		
THURSDAY	IV(A)	MTO	FM		MAT-IV	M&M		M&M Lab(A4) / M/c Shop(A1)		
	IV(B)	FM	ATD*		MAT-IV	KOM*		CAMD		
	VI(A)	HT	DOME-II		CIM	TQM		DOME-II*	CIM*	
	VI(B)	MF	CIM		HT*	FEA		HT LAB(B1) / M&A LAB (B2)		
	VIII(A)	PPE	AE		CE	OM		TP CELL ACTIVITIES		
	VIII(B)	AE	CE		PPE	OM		TP CELL ACTIVITIES		
FRIDAY	IV(A)	KOM*	FM*		KOM	MTO		MAT-IV	M&M	MTO
	IV(B)	FM	ATD*		MCW	MAT-IV		CAMD Lab(B2) / M&MT Lab(B3) / F&F Lab (B4)		
	VI(A)	HT*	CIM		DOME-II	MF				
	VI(B)	TQM	FEA *		HT	CIM*		HT LAB(B2) / M&A LAB (B3)		
	VIII(A)	PPE	AE		CE	OM				
	VIII(B)									
SATURDAY	IV(A)	FM*	KOM*		MTO	FM				
	IV(B)	MCW	KOM*		ATD	FM*				
	VI(A)	MF	DOME-II		HT	DOME-II*				
	VI(B)	HT	HT LAB(B3) / M&A LAB (B1)							
	VIII(A)									
	VIII(B)									

*Tutorial class /**ADDITIONAL MATHEMATICS

Prepared by: HOD PRINCIPAL

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MECH
Academic Consolidated Time Table
2017-18 (ODD)

DAY	TIME / SEM	9.00AM-10.00AM	10.00AM-11.00AM		11.15AM-12.15PM	12.15PM-01.15PM		02.00PM-03.00PM	03.00PM-04.00PM	04.00PM-05.00PM
MONDAY	III(A)	CAMD Lab(A3)/MT Lab(A1)/ F&F Lab(A2)				MAT-III	Lunch break-1.015pm To 2.00pm	BTD	MCW	MOM
	III(B)	BTD	MS	Break-11:00AM-11:15AM	MAT-III	MTO		MOM	MS	M&M
	V(A)	DOME-I	M&E		EE	MP-III		ECE LAB(A1)/FM&M LAB(A2)		
	V(B)	M&E	TM		DOM	DOME-I		EE		
	VII(A)	EE	OR		PLCM	NCES/TQM		Design Lab(A1)/CIM Lab(A2)		
	VII(B)	H&P	PLCM		OR	NCES/TQM		EE		
TUESDAY	III(A)	CAMD Lab(A1)/MT Lab(A2)/F&F Lab(A3)				MS		CAMD	CAMD	MCW
	III(B)	MS	MAT-III	Break-11:00AM-11:15AM	BTD**	MTO		MOM	MOM*	BTD*
	V(A)	EE	DOM		DOME-I	M&E		ECE LAB(A2)/FM&M LAB(A3)		
	V(B)	TM	M&E		EE	MP-III		M&E*	DOME-I*	EE*
	VII(A)	OR	EE		H&P	PLCM		Design Lab(A2)/CIM Lab(A3)		
	VII(B)	MV	PLCM		OR	EE				
WEDNESDAY	III(A)	CAMD Lab(A2)/MT Lab(A3)/ F&F Lab(A1)				MOM**		MCW	MAT-III	BTD**
	III(B)	M&M	MOM**	Break-11:00AM-11:15AM	BTD	MTO		M&M	MTO*	MAT-III*
	V(A)	EE	TM		DOME-I	**TPA**		ECE LAB(A3)/FM&M LAB(A1)		
	V(B)	DOME-I	DOM		TM	**TPA**		DOM*	MP-III*	TM*
	VII(A)	H&P	OR		EE	NCES/TQM		Design Lab(A3)/CIM Lab(A1)		
	VII(B)	PLCM	MV		H&P	NCES/TQM				
THURSDAY	III(A)	MCW	MOM		MS	BTD**				
	III(B)	M&M	BTD		MAT-III	MOM**		M & M Lab(B1) / M/c Shop (B3)		
	V(A)	DOME-I	DOM		TM	MP-III		M&E*	DOME-I*	EE*
	V(B)	M&E	EE		DOM	MP-III		ECE LAB(B1)/FM&M LAB(B2)		
	VII(A)	MV	H&P		EE	NCES/TQM				
	VII(B)	EE	OR		PLCM	NCES/TQM		Design Lab(B1)/CIM Lab(B2)		
FRIDAY	III(A)	BTD	MAT-III		MOM**	MS		MS*	MOM*	CAMD*
	III(B)	M&M	BTD		MAT-III	MOM**		M & M Lab(B1) / M/c Shop (B3)		
	V(A)	MP-III	M&E		DOM	TM		DOM*	MP-III*	TM*
	V(B)	EE	MP-III		DOME-I	TM		ECE LAB(B2)/FM&M LAB(B3)		
	VII(A)	MV	PLCM		H&P	NCES/TQM				
	VII(B)	H&P	EE		MV	NCES/TQM		Design Lab(B2)/CIM Lab(B3)		
SATURDAY	III(A)	MS	BTD		MAT-III	MOM		**MATDIP(A & B)**		
	III(B)	MAT-III	M & M Lab (B2) / M/c Shop (B1)							
	V(A)	TM	DOM	Break-11:00AM-11:15AM	EE	M&E		MP-III		
	V(B)	MP-III	DOME-I		M&E	DOM		ECE LAB(B3)/FM&M LAB(B1)		
	VII(A)	PLCM	MV		OR	**TPA**		MV		
	VII(B)	OR	MV		H&P	**TPA**		Design Lab(B3)/CIM Lab(B1)		

* Weekly test

**Tutorial class

Prepared By

HOD
Mechanical Engg.
HIT, Nidasoshi

Details of Academic Work Load of Faculty Members
ODD Sem: 2017-18

S No	Faculty Name	Theory-1	Theory-2	Lab 1	Teaching hours	Teaching units	Other Responsibilities
1	Dr.S.C. Kamate	BTD	TCE(PG)		04	08	Principal
2	Sri.S.B.Yapalaparvi	DOME-1			05	10	HOD
3	Dr.S.A.Alur	TM	NEC (PG)		08	16	PG Coordinator/ Research Centre Head
4	S.N.Topannavar	H & P	AFM	TEM LAB-I (PG)	11	19	Training & Placement Officer/ VII B Class Teacher
5	Sri.G.A.Naik	OR	EE	M & M LAB (2)	14	22	I I I coordinator
6	Sri.D.N.Inamdar	MOM	CAED		07	14	Chief NBA/NIRF Coordinator
7	Sri. R. K. Chitagopkar	RER	EME	M/c SHOP (3)	17	25	I I I coordinator
8	Sri. K.M.Akkoli	BTD	NEC (PG)	FM & M LAB	16	23	Asst. Warden /Dept. NBA Coordinator
9	Sri. G.V Chiniwalar	MOM	EME	DESIGN LAB (1)	13	16	Time Table Coordinator
10	Sri. S.R. Kulkarni	MCW	**PLCM**	WSL	17	25	News Letter/ Coordinator
11	Sri M. S. Futane	PLCM		CIM LAB	13	17	Warden / KSCST / Technical Magazine /e-Yantra Coordinator/
12	Sri.S.A.Goudadi	DOM	FEM (PG)	DESIGN LAB (1)	12	21	Dept. NBA Coordinator
13	Sri M M. Shivashimpi	TM		ENERGY LAB	14	19	AIMSS/VGST Engine Field /PhD.EMS/ News Letter Coordinator
14	Sri. T S Vandali	MV	CAED	CAED LAB	18	24	Technical Seminar Coordinator
15	Sri.M.A.Hipparagi	MTO	NTM	CIM LAB	16	23	Dept.Alumni Coordinator/
16	Sri. A. M. Biradar	EM & E	**EM&E**	DESIGN LAB (2)	16	26	IA Test Coordinator/
17	Sri. S. B. Awade	DOME-1	DOM	MT LAB (2)	16	26	EMS Coordinator
18	Sri. Jagadeesh S A	BTD	MS	FM & M LAB	16	23	Student Activities Coordinator/ V B Class Teacher
19	Sri.N.M.Ukkali	EE	OR	DESIGN LAB (2)	14	22	EMS Coordinator
20	Sri.M. R. Ingalagi	NCES	MS	ENERGY LAB	15	21	Asst AIMSS / EMS Coordinator / VII A Class Teacher
21	Sri. M. I Tanodi	CAMD	MV	CAMD LAB	22	28	Library / Tech Magazine/ Project / III A Class Teacher/ Course Plan/Institute NBA coordinator
22	Sri. K.G Ambli	MS	NTM	CAE LAB	19	26	Dept. Meeting Proceedings/ Institute NBA Coordinator/ III B Class Teacher/
23	Sri. B M Dodamani	M & M		M & M (2) MT (2)	16	20	Faculty / Staff Activities Coordinator/
24	Sri. R V Nyamagoud	H & P	TQM	WSL	17	25	T P Cell Coordinator
25	Sri. S. S. Angadi	RER		F & F LAB/ M/c SHOP(1)	15	18	Asst. T P Cell Coordinator/ V A Class Teacher

HOD
Mechanical Engg.
HIT, Nidasoshi

Details of Academic Work Load of Faculty Members
EVEN Sem: 2017-18

S No	Faculty Name	Theory-1	Theory-2	Lab 1	Teaching hours	Teaching units	Other Responsibilities	Signe
1	Dr.S.C. Kamate	ATD			5	10	Principal	
2	Dr. B M Shrigiri	ATD			5	10	HOD	
3	Dr.S.A.Alur	HMT	AHT		9	18	PG Coordinator/ Research Centre Head	
4	Sri.S.B.Yapalaparvi	DOME-II			5	10	Dept. NBA coordinator	
5	S.N.Topannavar	FM	S&GT	TPP Lab-I	9+3	21	**IV B Class Teacher**	
6	Sri.G.A.Naik	OM	CE	MMM(2)	8+6	22	I I I coordinator	
7	Sri.D.N.Inamdar	DOME-II			05	10	Chief NBA/NIRF Coordinator	
8	Sri. R. K. Chitagopkar	EME	MC&W	M/C SHOP(2)	8+6	22	I I I coordinator, **IV A Class Teacher**	
9	Sri. K.M.Akkoli	PPE	HMT	HMT(1)	9+3	21	Asst. Warden/AICTE coordinator **VIII A Class Teacher**	
10	Sri. G.V Chiniwalar	EME	Total Quality Management		4+3	14	Time Table Coordinator	
11	Sri.S.A.Goudadi	FEM			5	10	Dept. NBA Coordinator	
12	Sri. S.R. Kullkarni	Total Quality Management	Metal Forming	WSL(3)	6+9	21	News Letter/ Tech. Magazine/ Dept. News Letter Coordinator,	
13	Sri. M S Futane	CIM		F & F (4)	4+12	20	Hostel warden/ KSCST / Tech. Magazine Coordinator, **VI B Class Teacher**	
14	Sri M M. Shivashimpi	Engine Flow & Combustion	ATD	HMT(2)	10	20	AIMSS/VGST Engine Field /PhD.EMS/ News Letter Coordinator	
15	Sri. T S Vandali	FEM	CAED	CAED(3)	8+9	25	**VIII B Class Teacher**	
16	Sri.M.A.Hipparagi	Machine Tool & Operation	CIM	M/C SHOP(2)	8+6	22	Insti. News & publicity /Dept. Alumni Coordinator, **VI A Class Teacher**	
17	Sri. A. M. Biradar	Automotive Engg	OM	MT (2)	8+6	22	IA Test/ Robo Vidya /Elective Coordinator	
18	Sri. S. B. Awade	CAMD	KOM	CAMD(4)	7+12	24	EMS, Robo Vidya /Elective Coordinator	
19	Sri. Jagadeesh S A	PPE	APPC	HMT(3)	8+9	25	Student Activities Coordinator	
20	Sri.N.M.Ukkali	CE		CAMA(3)	4+9	17	EMS, Robo Vidya /Elective Coordinator	
21	Sri.M. R. Ingalagi	FM	DHTE	MT (2)	9+6	24	Asst AIMSS / EMS Coordinator	
22	Sri. M. I Tanodi	KOM	Project	CAMA(3)	5+6+9	25	Library /AICTE/CP CLM/ Technical Seminar coordinator	
23	Sri. K.G Ambli	CAED	Metal Forming	CAED(3)	5+9	19	Dept. Meeting Proceedings Coordinator	
24	Sri. B M Dodamani	M&M	Theory of IC Engine	MMM(2)	8+6	22	Faculty / Staff Activities Coordinator/	
25	Sri. R V Nyamagoud	Automotive Engg	Thermal Power Station-1	WSL(3)	7+6	20	T P Cell Coordinator	

HOD
Mechanical Engg.
HIT, Nidasoshi

LESSON PLAN

Course : Heat Transfer Code : 15ME63 Lect. Per week : 05

Sem. : VI B.

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
I	Modes of heat transfer, Basic laws governing conduction	1/2/18	5/2/18		
	Convection, radiation heat transfer.	2/2/18	8/2/18		
	Conductivity, convective heat transfer coefficient.	3/2/18	10/2/18		
	radiation heat transfer, combined heat transfer mechanism	5/2/18	12/2/18		
	Types of boundary conditions general heat conduction equation	6/2/18	12/2/18		
	Derivation of equation in i) cartesian ii) polar & spherical coordi	8/2/18	15/2/18		
	Steady state one dimensional heat conduction problems in cartesian system	9/2/18	17/2/18		
	steady state one dimensional heat conduction problems i) with & without heat generation	10/2/18	19/2/18		
	ii) with & without varying thermal conductivity, Resistance in series	12/2/18	20/2/18		
		15/2/18	22/2/18 23/2/18		
Planned Hrs: 10				Actual Hrs : 12	
II	Critical thickness of insulation concept & derivation	16/2/18	26/2/18		
	Extended surfaces or fins classification, straight rect	17/2/18	27/2/18		
	angular & circular fins temp distribution & HT classification	19/2/18	1/3/18		
	Fin effectiveness efficiency & applications.	20/2/18	3/3/18		
	Transient heat conduction, Definition, Different cases	22/2/18	8/3/18		
	Negligible internal thermal resistance	23/2/18	9/3/18		
	negligible surface resistance	24/2/18	10/3/18		
	comparable internal thermal resistance & surface resistance	26/2/18	12/3/18		
	Lumped body, infinite body & semi infinite body	27/2/18	13/3/18		
	Numerical problems, Heisler & Groober charts.	1/3/18	14/3/18		
Planned Hrs: 10				Actual Hrs : 10	

HOD

PRINCIPAL

LESSON PLAN

Course : Finite Element Analysis Code : 15ME61 Lect. Per week : 05

Sem. : VI - B - div

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
Module-I Introduction to Finite Element Methods	General description of FEM. Engg applications of FEM	05/2/18	5/2/18		
	Boundary conditions: ~~of~~ homogeneous & Non-homogeneous for structural, heat transfer & fluid flow problems	06/2/18	6/2/18		
	Potential Energy method. Rayleigh Ritz method. Galerkin's method.	07/2/18	7/2/18		
	Displacement Method of FE ~~Formulation~~ convergence criteria.	08/2/18	8/2/18		
	Descretisation process. Types of Elements. 1D, 2D & 3D	09/2/18	9/2/18		
	Node numbering, Location of nodes strain displacement relations	12/2/18	12/2/18		
	Stress strain relations, plane stress & plane strain cond^n, temp effects	15/2/18	15/2/18		
	Interpolation models: simplex, complex, multiplex elements	16/2/18	16/2/18		
	Linear interpolation polynomials in terms of global co-ord 1D, 2D, 3D.				
Planned Hrs: 08 hrs				Actual Hrs : 08 hrs	
Module-II One Dimensional Elements. Analysis of bars & Trusses	Linear interpolation models in terms of Local co-ordinates for 1D, 2D Ele-	19/2/18	19/2/18		
	Higher order interpolation function for 1D Quadratic & cubic elements	20/2/18	20/2/18		
	In natural co-ordinates. constant strain Triangle	21/2/18	21/2/18		
	4-noded Tetrahedral Element (TET-4) 8-noded Hexahedral Element (HEXA-8)	22/2/18	22/2/18		
	2D-Isoparametric Element. Lagrange Interpolation function	23/2/18	23/2/18		
	Numerical Integration: Gaussian Quadrature one-point, 2-point form	26/2/18	26/2/18		
	2D-~~Int~~ integrals, Body force, Traction force & point Loads	27/2/18	27/2/18		
	Numerical prob: sol for displacement stress & strain in 1D st-bars	28/2/18	28/2/18		
	stepped bars & Tapered bars using Elimination approach &	01/3/18	01/3/18		
	penalty approach. Analysis of Trusses.	02/3/18	2/3/18		
Planned Hrs: 10 hrs				Actual Hrs 10 hr	

HOD

PRINCIPAL

LESSON PLAN

Course : Metal Forming Code : 15ME653 Lect. Per week : 0

Sem. : VI - B

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
Module-I Introduction to Metal forming	Classification of metal forming processes, adv and Limitations,	5-2-18	5-2-18		
	Stress-Strain relation in elastic and plastic deformation	5-2-18	6-2-18		
	Concept of True stress and true strain	6-2-18	8-2-18		
	Tri axial and Bi axial stresses	8-2-18	9-2-18		
	Determination of flow stresses and principal stresses	12-2-18	15-2-18		
	Yield criteria and their Significance	12-2-18	19-2-18		
	Tresca and von-mises Yield Criteria	13-2-18	20-2-18		
	concepts of plane stress & plane strain	15-2-18	22-2-18		
	Deformation mechanisms	19-2-18	26-2-18		
	Hot and cold working processes and its effects on mech prop.	20-2-18	27-2-18		
Planned Hrs: 10				Actual Hrs :	
Module-II Effects of Parameters / forging	Metallurgical aspects of MF, slip, twinning mechanisms of plastic deformation	15-3-18	1-3-18		
	Effects of Temp., strain rate, friction and lubrication.	19-3-18	12-3-18		
	Hydrostatic pressure in metal working	19-3-18	13-3-18		
	Deformation zone geometry & workability of materials	20-3-18	15-3-18		
	Residual stresses in wrought products.	22-3-18	16-3-18		
	Forging - classification, forging m/c equipments	26-3-18	17-3-18		
	Expression for pressure and load in open/close die by slab analysis	27-3-18	19-3-18		
	concepts of friction Hill and factors affecting it	29-3-18	20-3-18		
	Die design parameters, mat flow lines in forging and forging defects	2-4-18	22-3-18		
	Residual stresses in forging & simple problems	3-4-18	27-3-18		
Planned Hrs: 10				Actual Hrs : 10	

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PRINCIPAL

LESSON PLAN

Subject with code : Material Science 15MF32 Lect. Per week : 04

Class : 3rd

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
01	Introduction to crystal structure; coordination number,	7/8/17	9/8/17		
	APF, simple cubic, FCC, HCP structures, crystal imperfe-ctions, Atomic Diffusion, Fick's laws of diffusion,	9/8/17	10/8/17		
	Factors affecting diffusion. Mechanical Behaviour:	10/8/17	11/9/17		
	Stress-strain diagrams, Engineering & true strains, Linear & non-linear elastic behaviour & properties,	11/8/17	12/8/17		
	Mechanical Properties, stiffness, offset yield strength, Ductility, ultimate Tensile strength, plastic deformation	14/8/17	14/8/17		
	and twinning, Mechanisms of strengthening in metals.	16/8/17	16/8/17		
	Fracture: Type I, Type II, Type III, fatigue: Types of fatigue loading, fatigue properties,	17/8/17	17/8/17		
	S-N diagram, fatigue Testing. Creep: Description of the phenomenon with	18/8/17	18/8/17		
	Examples, 3-stages of creep, creep properties, stress relaxation, concept of fracture toughness,	19/8/17	19/8/17		
	numerical on diffusion, strain & stress relaxation.	21/8/17	21/8/17		21/8
Planned Hrs: 10				Actual Hrs : 10	
02	Concept of formation of alloys: Types of alloys, solid solutions, factors affecting solid solubility,	23/8/17	22/8/17		
	Binary phase diagrams, lever rule, substitutional and interstitial solid solutions, Gibbs phase	24/8/17	23/8/17		
	rule effect of non-equilibrium cooling, coring. Homogenization Iron carbon, Cementite	26/8/17	24/8/17		
	diagram, effect of common alloying elements in steel, common alloy steels,	30/8/17	30/8/17		
	Stainless steel, Tool steel, Solidification:	31/8/17	31/8/17		
	Mechanism of solidification, Homogenous & Heterogeneous nucleation,	01/9/17	01/9/17		
	Crystal growth, cast metal structures	4/9/17	4/9/17		
	Solidification of steels & cast irons.	6/9/17	6/9/17		
	Numerical on lever rule.	7/9/17	7/9/17		
	Numerical on lever rule continued	8/9/17	8/9/17		8/9
Planned Hrs: 10				Actual Hrs : 10	

HOD PRINCIPAL

LESSON PLAN

Subject with code : Mechanics of Materials / 15ME34 Lect. Per week : 05 Hrs

Class : III rd 'B'

MODULE	Details to be Covered	Date		Signature	
		Planned	Actual	Faculty	HOD
1	Stress ~~and Strain~~: Introduction, Concept of Normal Stress, Strain, Hooke's Law	07/08/2017	9/8/2017	[illegible]	
	Determination of stresses in straight, stepped, tapered sections.	09/08/2017	11/8/2017	[illegible]	
	Determination of stresses in composite sections	11/08/2017	12/8/2017	[illegible]	
	Stresses due to temp change and problems on it.	14/08/2017	16/8/2017	[illegible]	
	Concept of shear stress and strain, lateral strain, poisson's Ratio	16/08/2017	18/08/2017	[illegible]	
	Elastic Constants, Relationship between Young's modulus and modulus of Rigidity.	18/08/2017	19/08/2017	[illegible]	
	Relationship between E, G and k.	21/08/2017	22/8/2017	[illegible]	
	Generalised Hooke's law and problems on it.	23/08/2017	23/8/2017	[illegible]	
	Derivation of deformation equations for Tapered, straight bars	25/08/2017	26/8/2017	[illegible]	
	Problems on elastic constants and stress and strain in stepped, straight bars	28/08/2017	29/8/2017	[illegible]	29/8
Planned Hrs: 10				Actual Hrs : 10	
2	Analysis of stress and strain: plane stress system, components of stresses	29/08/2017	30/8/2017	[illegible]	
	Derivation of stress components on inclined plane for uniaxial, biaxial stress system	30/08/2017	01/9/2017	[illegible]	
	Derivation of stress components on inclined plane at a point in a body subjected to General plane stress system, principle stresses, maximum shear stresses	01/09/2017	05/9/2017	[illegible]	
		05/09/2017	6/9/2017	[illegible]	
	Orientation of principle planes and solution by Analytical Method.	06/09/2017	8/9/2017	[illegible]	
	Shear stresses on principle planes and normal stress components on max shear stress	08/09/2017	9/9/2017	[illegible]	
	Mohr's Circle method for plane stress system and problems on it	11/09/2017	12/9/2017	[illegible]	
	Cylinders: Introduction, Thin cylinders, Hoop or Circumferential stresses, longitudinal stress	12/09/2017	13/9/2017	[illegible]	
	Maximum shear stress, circumferential and longitudinal strains, change in volume	13/09/2017	18/9/2017	[illegible]	
	Thick cylinder, Derivation of Lame's Equation, Radial pressure and problems	18/09/2017	19/9/17	[illegible]	19/9
Planned Hrs: 10				Actual Hrs : 10	

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LESSON PLAN

Subject with code : Machine Tools & operations 15ME35B Lect. Per week : 04

Class : III 'B'

Chapter/ Unit	Details to be Covered	Date		Signature	
		Planned	Actual	Faculty	HOD
01	Machine Tools - Introduction	7-8-17	8-8-17		
	Classification of M/C Tools	8-8-17	9-8-17		
	Lathe machine	9-8-17	11-8-17		
	Drilling machine	11-8-17	12-8-17		
	Milling machine	14-8-17	16-8-17		
	Boring machine	16-8-17	16-8-17		
	Broaching machine	18-8-17	18-8-17		
	Shaping machine	21-8-17	19-8-17		
	Planing machine	22-8-17	22-8-17		
	Grinding machine	23-8-17	23-8-17		23/9
Planned Hrs: 10				Actual Hrs : 10	
02	Machining process :- Introduction	29-8-17	23-8-17		
	Types of Motion in machining	30-8-17	29-8-17		
	Turning and boring operation	1-9-17	30-8-17		
	Shaping and planing operation	4-9-17	01-9-17		
	Slotting & thread cutting operation	5-9-17	05-09-17		
	Drilling and reaming operation	6-9-17	06-9-17		
	Milling and broaching operation	8-9-17	06-9-17		
	Gearcutting and grinding operation	11-9-17	08-9-17		
	Machining parameters :- Speed, feed	12-9-17	09-9-17		
	D.O.C M/c time, related quantities.	13-9-17	10-9-17		10/9
Planned Hrs: 10				Actual Hrs : 10	

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Mech. Engg. Dept
Syllabus coverage
2017-18 (Even)

1

Syllabus Covered from **05/02/2018 TO 07/04/2018** (Up to week No.:09)

Sl.No.	SEM	Subject	Sub Code	Faculty	No. of Classes as per Time table	Actual No. of Classes Engaged	Cumulative, % of Syllabus coved	%	Sign.
1	IV A	Engineering Mathematics-IV	15MAT41	Mrs.J.B.Patil	30	26	55	45.5	
2		Kinematics of Machines	15ME42	Mr.S.B.Awade	38	37	48.1		
3		Applied Thermodynamics	15ME43	Dr.S.C.Kamate/ Dr.B.M.Shrigiri	32	34	40%		
4		Fluid Mechanics	15ME44	Mr.M.R.Ingalagi	35	31	48%		
5		Machine Tools and Operations	15ME45B	Mr.M.A.Hipparagi	28	24	42%		
6		Mechanical Measurements and Metrology	15ME46B	Mr.B.M.Dodamani	32	29	40%		
1	IV B	Engineering Mathematics-IV	15MAT41	Mr.S.I.Shivamog math	24	23	50	56.5	
2		Kinematics of Machines	15ME42	Mr.M.I.Tanodi	33	37	62%		
3		Applied Thermodynamics	15ME43	Mr.M.M.Shivashimpi	41	41	50%		
4		Fluid Mechanics	15ME44	Mr.S.N.Topannavar	44	49	64%		
5		Metal Casting and Welding	15ME45A	Mr.R.K.Chitgopkar	36	32	68		
6		Computer Aided Machine Drawing	15ME46A	Mr.S.B.Awade	18	17	45		
1	VI A	Finite Element Analysis	15ME61	Mr.S.A.Goudadi	36	36	50	56.5%	
2		Computer integrated Manufacturing	15ME62	Mr.M.A.Hipparagi	32	28	50%		
3		Heat Transfer	15ME63	Mr.S.A.Alur	33	29	46		
4		Design of Machine Elements -II	15ME64	Mr.S.B.Yapalaparvi	30	26	40%		
5		Metal Forming	15ME653	Mr.K.G.Ambli	21	20	40%		
6		Total Quality Management	15ME664	Mr.S.R.Kulkarni	21	21	52%		
1	VI B	Finite Element Analysis	15ME61	Mr.T.S.Vandali	37	35	40	48.33%	
2		Computer integrated Manufacturing	15ME62	Mr.M.S.Futane	22	31	50%		
3		Heat Transfer	15ME63	Mr.K.M.Akkoli	34	31	50		
4		Design of Machine Elements -II	15ME64	Mr.D.N.Inamdar	32	29	45%		
5		Metal Forming	15ME653	Mr.S.R.Kulkarni	24	24	55%		
6		Total Quality Management	15ME664	Mr.G.V.Chiniwalar	20	20	50%		
1	VII A	Operations Management	10ME81	Mr.G.A.Naik	24	24	46	47%	
2		Control Engineering	10ME82	Mr.N.M.Ukkali	26	24	50		
3		Power Plant Engineering	10ME833	Mr.K.M.Akkoli	25	20	50		
4		Automotive Engineering	10ME844	Mr.A.M.Biradar	24	22	42		
1	VII B	Operations Management	10ME81	Mr.A.M.Biradar	24	23	44	49.25%	
2		Control Engineering	10ME82	Mr.G.A.Naik	24	24	46		
3		Power Plant Engineering	10ME833	Mr.Jagadeesh. S. A	25	26	55%		
4		Automotive Engineering	10ME844	Mr.R.V.Nyamagoud	29	27	52%		

LABORATORIES:

Sl.No	Faculty	SEM	Sub Code	LAB NAME	Total no. of Expts.	No. of expts. completed	Sign.
1	Mr.G.A.Naik(A1&A2)/ Mr.B.M.Dodamani(A3&A4)	IV A	15MEL47B	Mechanical Measurements and Metrology Lab	13 13	08 06	
2	Mr.R.K.Chitagopkar (A1&A2) / Mr.M.A.Hipparagi(A3&A4)		15MEL48B	Machine Shop	06	03	
1	Mr.S.B.Awade	IV B	15ME46A	Computer Aided Machine Drawing	13	6	
2	Mr.M.R.Ingalagi (B1&B2)/ Mr.A.M.Biradar (B3&B4)		15MEL47A	Material Testing Lab	16 16.	09 07	
3	Mr.M.S.Futane		15MEL48A	Foundry and Forging Lab	11	8	
1	Mr.Jagadeesh. S. A	VI A	15MEL67	Heat Transfer Lab	14	8	
2	Mr.M.I.Tanodi		15MEL68	Modeling and Analysis Lab	10	7	
1	Mr.K.M.Akkoli (B3)/ Mr.M.M.Shivashimpi (B1&B2)	VI B	15MEL67	Heat Transfer Lab	14 14	8 8	
2	Mr.N.M.Ukkali		15MEL68	Modeling and Analysis Lab	42	09	

9/4/18
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Mechanical Engg.
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Mech. Engg. Dept
Syllabus coverage
2017-18 (Even)

Syllabus Covered from **05/02/2018 TO 10 /03/2018** (Up to week No.:05) 2

Sl.No.	SEM	Subject	Sub Code	Faculty	No. of Classes as per Time table	Actual No. of Classes Engaged	Cumulative, % of Syllabus coved	Sign.
1	IV A	Engineering Mathematics-IV	15MAT41	Mrs.J.B.Patil	15	13	30	
2		Kinematics of Machines	15ME42	Mr.S.B.Awade	21	20	27	
3		Applied Thermodynamics	15ME43	Dr.S.C.Kamate/ Dr.B.M.Shrigiri	15	14	18	
4		Fluid Mechanics	15ME44	Mr.M.R.Ingalagi	22	17	28%	
5		Machine Tools and Operations	15ME45B	Mr.M.A.Hipparagi	14	12	30	
6		Mechanical Measurements and Metrology	15ME46B	Mr.B.M.Dodamani	17	16	30	
1	IV B	Engineering Mathematics-IV	15MAT41	Mr.S.I.Shivamogimath	14	12	30	
2		Kinematics of Machines	15ME42	Mr.M.I.Tanodi	20	22	40	
3		Applied Thermodynamics	15ME43	Mr.M.M.Shivashimpi	18	18	30	
4		Fluid Mechanics	15ME44	Mr.S.N.Topannavar	22	26	40	
5		Metal Casting and Welding	15ME45A	Mr.R.K.Chitgopkar	14	12	27	
6		Computer Aided Machine Drawing	15ME46A	Mr.S.B.Awade	9	10	27	
1	VI A	Finite Element Analysis	15ME61	Mr.S.A.Goudadi	15	17	32	
2		Computer integrated Manufacturing	15ME62	Mr.M.A.Hipparagi	17	16	30	
3		Heat Transfer	15ME63	Mr.S.A.Alur	20	16	30%	
4		Design of Machine Elements -II	15ME64	Mr.S.B.Yapalaparvi	20	18	25%	
5		Metal Forming	15ME653	Mr.K.G.Ambli	13	11	30%	
6		Total Quality Management	15ME664	Mr.S.R.Kulkarni	11	11	30%	
1	VI B	Finite Element Analysis	15ME61	Mr.T.S.Vandali	20	20	20%	
2		Computer integrated Manufacturing	15ME62	Mr.M.S.Futane	17	16	30%	
3		Heat Transfer	15ME63	Mr.K.M.Akkoli	18	17	34%	
4		Design of Machine Elements -II	15ME64	Mr.D.N.Inamdar	19	19	25%	
5		Metal Forming	15ME653	Mr.S.R.Kulkarni	11	11	30%	
6		Total Quality Management	15ME664	Mr.G.V.Chiniwalar	12	11	30%	
1	VIII A	Operations Management	10ME81	Mr.G.A.Naik	15	15	29%	
2		Control Engineering	10ME82	Mr.N.M.Ukkali	15	14	26	
3		Power Plant Engineering	10ME833	Mr.K.M.Akkoli	16	12	25%	
4		Automotive Engineering	10ME844	Mr.A.M.Biradar	16	14	27%	
1	VIII B	Operations Management	10ME81	Mr.A.M.Biradar	16	15	29%	
2		Control Engineering	10ME82	Mr.G.A.Naik	15	15	29	
3		Power Plant Engineering	10ME833	Mr.Jagadeesh. S. A	16	13	25%	
4		Automotive Engineering	10ME844	Mr.R.V.Nyamagoud	16	14	30%	

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LABORATORIES:

SEM	Sl.No	Faculty	Sub Code	LAB NAME	Total no. of Expts.	No. of expts. completed	Sign.
III A	1	Mr.M.I.Tanodi	15ME36 A	Computer Aided M/c Drawing	10	09	
	2	Mr.C.I.Ambiger(A2&A4)/ Mr.S.B.Awade(A1&A3)	15MEL37A	Material Testing Lab	12	10	
	3	Mr.S.S.Angadi	15MEL38A	Foundry and Forging Lab	12	11	
III B	1	Mr.G.A.Naik (B1 & B4)/ Mr.B.M.Dodamani (B2 & B3)	15MEL37B	Mechanical Measurements and Metrology Lab	14 14	12 12	
	2	Mr.R.K.Chitgopkar	15MEL38B	Machine Shop	5	4	
V A	1	Mr.K.M.Akkoli(A4) /Mr.B.M.Dodamani (A1,A2 &A3)	15MEL57	Fluid Mechanics and Machinery Lab	12 14	11 13	
	2	Mr.M.M.Shivashimpi	15MEL58	Energy Lab	15	13	
V B	1	Mr.Jagadish.S.A	15MEL57	Fluid Mechanics and Machinery Lab	12	11	
	2	Mr.M.R.Ingalagi	15MEL58	Energy Lab	12	11	
VII A	1	N.M.Ukkali (A1&A2)/ Mr. A.M.Biradar (A3&A4)	10MEL77	Design Lab	12 12	11 11	
	2	Mr.M.S.Futane	10MEL78	CIM and Automation Lab	12	08	
VII B	1	Mr.S.A.Goudadi(B3&B4)/ Mr.G.V.Chiniwalar(B1&B2)	10MEL77	Design Lab	11 12	09 11	
	2	Mr. M.A.Hipparagi(B2,B3 &B4)/ Mr.M.S.Futane(B1)	10MEL78	CIM and Automation Lab	12	10	

30/10/17

HOD
Mechanical Engg.
HIT, Nidasoshi

WEEKLY SYLLABUS COVERAGE STATUS FROM 07/08/2017 TO 28/10/2017 (11 WEEK)

SEM	Sl.No.	Faculty	Sub Code	Subject	No. of Classes as per Time table	Actual No. of Classes Engaged	Cumulative, % of Syllabus coved	Sign.
III A (70)	1	Mr.S.S.Thabaj	15ME31	Engineering Mathematics-III	35	40	80%	
	2	MRI/JSA	15ME32	Material Science	40	30	50%	
	3	Dr.S.C.Kamate/ Mr.K.M.Akkoli	15ME33	Basic Thermodynamics	38	40	65%	
	4	Mr.D.N.Inamdar	15ME34	Mechanics of Materials	38	34	60%	
	5	Mr.S.R.Kulkarni	15ME35A	Metal Casting and Welding	40	40	80%	
	6	Mr.M.I.Tanodi	15ME36A	Computer Aided Machine Drawing	18	24	90%	
III B	1	Mr.S.A.Patil	15ME31	Engineering Mathematics-III	37	37	80%	
	2	Mr.K.G.Ambli	15ME32	Material Science	36	35	67%	
	3	Mr.Jagadish.S.A	15ME33	Basic Thermodynamics	39	44	70%	
	4	Mr.G.V.Chiniwalar	15ME34	Mechanics of Materials	36	36	60%	
	5	Mr.M.A.Hipparagi	15ME35B	Machine Tools and Operations	36	35	75%	
	6	Mr.B.M.Dodamani	15ME36B	Mechanical Measurements and Metrology	39	36	68%	
V A	1	Mr.A.M.Biradar	15ME51	Engineering Management &Economics	36	33	66%	
	2	Mr.S.B.Awade	15ME52	Dynamics of Machines	31	36	68%	
	3	Mr.M.M.Shivashimpi	15ME53	Turbo Machine	50	50	76%	
	4	Mr.S.B.Yapalaparvi	15ME54	Design of Machine Elements-I	39	39	60%	
	5	Mr. M.A.Hipparagi	15ME554	Non Traditional Machining	27	26	70%	
	6	Mr.S.S.Angadi	15ME562	~~Energy & Environment~~ (RES)	35	22	65%	
V B (62)	1	Mr.C.I.Ambiger	15ME51	Engineering Management &Economics	36	22	50%	
	2	Mr.S.A.Goudadi	15ME52	Dynamics of Machines	38	38	68	
	3	Dr.S.A.Alur.	15ME53	Turbo Machine	38	34	80	
	4	Mr.S.B.Awade	15ME54	Design of Machine Elements-I	41	40	68	
	5	Mr.K.G.Ambli	15ME554	Non Traditional Machining	27	26	62%	
	6	Mr.R.K.Chitgopkar	15ME562	~~Energy & Environment~~ (RES)	26	27	61%	
VII A	1	N.M.Ukkali	10ME71	Engineering Economics	39	36	80	
	2	Mr.M.I.Tanodi	10ME72	Mechanical Vibrations	40	44	79%	
	3	Mr. R.V.Nyamagoud	10ME73	Hydraulics and Pneumatics	42	37	80%	
	4	Mr.G.A.Naik	10ME74	Operation Research	36	35	67	
	5	Mr.M.R.Ingalagi	10ME754	Non- Conventional Energy Sources	27	27	60%	
	6	Mr. A.M.Biradar	10ME758	Total Quality Management	42	37	78%	
	7	Mr.M.S.Futane	10ME769	Product Life Cycle Management	40	32	75%	
VII B (74)	1	Mr.G.A.Naik	10ME71	Engineering Economics	36	36	69	
	2	Mr.T.S.Vandali	10ME72	Mechanical Vibrations	42	40	70	
	3	Mr.S.N.Topannavar	10ME73	Hydraulics and Pneumatics	37	37	71%	
	4	N.M.Ukkali	10ME74	Operation Research	38	37	70	
	5	Mr.M.R.Ingalagi	10ME754	Non- Conventional Energy Sources	27	27	65	
	6	Mr. R.V.Nyamagoud	10ME758	Total Quality Management	42	37	78%	
	7	Mr.S.R.Kulkarni	10ME769	Product Life Cycle Management	40	33	75%	

LABORATORIES:

SEM	Sl.No	Faculty	Sub Code	LAB NAME	Total no. of Expts.	No. of expts. completed	Sign.
III A	1	Mr.M.I.Tanodi	15ME36 A	Computer Aided M/c Drawing	10	09	
	2	Mr.C.I.Ambiger(A2&A4)/ Mr.S.B.Awade(A1&A3)	15MEL37A	Material Testing Lab	12	10	
	3	Mr.S.S.Angadi	15MEL38A	Foundry and Forging Lab	12	11	
III B	1	Mr.G.A.Naik (B1 & B4)/ Mr.B.M.Dodamani (B2 & B3)	15MEL37B	Mechanical Measurements and Metrology Lab	14 14	12 12	
	2	Mr.R.K.Chitgopkar	15MEL38B	Machine Shop	5	4	
V A	1	Mr.K.M.Akkoli(A4) /Mr.B.M.Dodamani (A1,A2 &A3)	15MEL57	Fluid Mechanics and Machinery Lab	12 14	11 13	
	2	Mr.M.M.Shivashimpi	15MEL58	Energy Lab	15	13	
V B	1	Mr.Jagadish.S.A	15MEL57	Fluid Mechanics and Machinery Lab	12	11	
	2	Mr.M.R.Ingalagi	15MEL58	Energy Lab	12	11	
VII A	1	N.M.Ukkali (A1&A2)/ Mr. A.M.Biradar (A3&A4)	10MEL77	Design Lab	12 12	11 11	
	2	Mr.M.S.Futane	10MEL78	CIM and Automation Lab	12	08	
VII B	1	Mr.S.A.Goudadi(B3&B4)/ Mr.G.V.Chiniwalar(B1&B2)	10MEL77	Design Lab	11 12	09 11	
	2	Mr. M.A.Hipparagi(B2,B3 &B4)/ Mr.M.S.Futane(B1)	10MEL78	CIM and Automation Lab	12	10	

HOD
Mechanical Engg.
HIT, Nidasoshi

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WEEKLY SYLLABUS COVERAGE STATUS FROM 07/08/2017 TO 22/09/2017 (6 WEEK)

SEM	Sl.No.	Faculty	Sub Code	Subject	No. of Classes as per Time table	Actual No. of Classes Engaged	Cumulative, % of Syllabus coved	Sign.
III A	1	Mr.S.S.Thabaj	15ME31	Engineering Mathematics-III	23	24	38%	
	2	Mr.C.I.Ambiger	15ME32	Material Science	23	25	32%	
	3	Dr.S.C.Kamate/ Mr.K.M.Akkoli	15ME33	Basic Thermodynamics	27	27	30%	KMA
	4	Mr.D.N.Inamdar	15ME34	Mechanics of Materials	23	19	30%	
	5	Mr.S.R.Kulkarni	15ME35A	Metal Casting and Welding	24	24	40%	
	6	Mr.M.I.Tanodi	15ME36A	Computer Aided Machine Drawing	09	12	50%	
III B (35)	1	Mr.S.A.Patil	15ME31	Engineering Mathematics-III	20	20	38%	
	2	Mr.K.G.Ambli	15ME32	Material Science	23	23	37%	
	3	Mr.Jagadish.S.A	15ME33	Basic Thermodynamics	27	25	30%	
	4	Mr.G.V.Chiniwalar	15ME34	Mechanics of Materials	23	23	35%	
	5	Mr.M.A.Hipparagi	15ME35B	Machine Tools and Operations	19	19	42%	
	6	Mr.B.M.Dodamani	15ME36B	Mechanical Measurements and Metrology	19	18	33%	
V A (32)	1	Mr.A.M.Biradar	15ME51	Engineering Management &Economics	20	19	38%	
	2	Mr.S.B.Awade	15ME52	Dynamics of Machines	22	22	32	
	3	Mr.M.M.Shivashimpi	15ME53	Turbo Machine	35	35	42%	
	4	Mr.S.B.Yapalaparvi	15ME54	Design of Machine Elements-I	22	17	33%	
	5	Mr. M.A.Hipparagi	15ME554	Non Traditional Machining	18	17	37%	
	6	Mr.S.S.Angadi	15ME562	~~Energy & Environment~~ (RES)	18	12	38%	
V B	1	Mr.C.I.Ambiger	15ME51	Engineering Management &Economics	18	14	30%	
	2	Mr.S.A.Goudadi	15ME52	Dynamics of Machines	21	23	32	
	3	Dr.S.A.Alur.	15ME53	Turbo Machine	21	16	30	
	4	Mr.S.B.Awade	15ME54	Design of Machine Elements-I	24	24	30	
	5	Mr.K.G.Ambli	15ME554	Non Traditional Machining	18	18	38%	
	6	Mr.R.K.Chitgopkar	15ME562	Energy & Environment	18	16	30	
VII A (28)	1	N.M.Ukkali	10ME71	Engineering Economics	18	12	35%	
	2	Mr.M.I.Tanodi	10ME72	Mechanical Vibrations	20	24	40%	
	3	Mr. R.V.Nyamagoud	10ME73	Hydraulics and Pneumatics	21	17	35%	
	4	Mr.G.A.Naik	10ME74	Operation Research	218	18	38%	
	5	Mr.M.R.Ingalagi	10ME754	Non- Conventional Energy Sources	23	16	32%	
	6	Mr. A.M.Biradar	10ME758	Total Quality Management	20	15	31%	
	7	Mr.M.S.Futane	10ME769	Product Life Cycle Management	21	20	38%	
VII B	1	Mr.G.A.Naik	10ME71	Engineering Economics	21	18	38%	
	2	Mr.T.S.Vandali	10ME72	Mechanical Vibrations	23	23	38%	
	3	Mr.S.N.Topannavar	10ME73	Hydraulics and Pneumatics	21	21	42	
	4	N.M.Ukkali	10ME74	Operation Research	17	11	30%	
	5	Mr.M.R.Ingalagi	10ME754	Non- Conventional Energy Sources	23	16	32%	
	6	Mr. R.V.Nyamagoud	10ME758	Total Quality Management	20	18	38%	
	7	Mr.S.R.Kulkarni	10ME769	Product Life Cycle Management	21	21	40%	

LABORATORIES:

SEM	Sl.No	Faculty	Sub Code	LAB NAME	Total no. of Expts.	No. of expts. completed	Sign.
III A	1	Mr.M.I.Tanodi	15ME36 A	Computer Aided M/c Drawing	10	05	
	2	Mr.C.I.Ambiger(A2&A4)/ Mr.S.B.Awade(A1&A3)	15MEL37A	Material Testing Lab	12	05 06	
	3	Mr.S.S.Angadi	15MEL38A	Foundry and Forging Lab	12	07	
III B	1	Mr.G.A.Naik (B1 & B4)/ Mr.B.M.Dodamani (B2 & B3)	15MEL37B	Mechanical Measurements and Metrology Lab	14 14	08 08	
	2	Mr.R.K.Chitgopkar	15MEL38B	Machine Shop	5	2.5	
V A	1	Mr.K.M.Akkoli(A4) /Mr.B.M.Dodamani (A1,A2 &A3)	15MEL57	Fluid Mechanics and Machinery Lab	11	08	
	2	Mr.M.M.Shivashimpi	15MEL58	Energy Lab	15	08	
V B	1	Mr.Jagadish.S.A	15MEL57	Fluid Mechanics and Machinery Lab	12	08	
	2	Mr.M.R.Ingalagi	15MEL58	Energy Lab	12	06	
VII A	1	N.M.Ukkali (A1&A2)/ Mr. A.M.Biradar (A3&A4)	10MEL77	Design Lab	11 11	06 06	
	2	Mr.M.S.Futane	10MEL78	CIM and Automation Lab	12	06	
VII B	1	Mr.S.A.Goudadi(B3&B4)/ Mr.G.V.Chiniwalar(B1&B2)	10MEL77	Design Lab	11 11	07 07	
	2	Mr. M.A.Hipparagi(B2,B3 &B4)/ Mr.M.S.Futane(B1)	10MEL78	CIM and Automation Lab	12	06	

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Academic Excellence in Vibrant & Proactive Youth Culture

Approved by AICTE and Affiliated to VTU Belgaum.

FIRST YEAR
Academic
Consolidated TT
2017-18 (Odd)

CONSOLIDATED TIME TABLE

w.e.f.: 14/08/2017

DAY	TIME / SEM	9.00AM-10.00AM	10.00AM-11.00AM		11.15AM-12.15PM	12.15PM-01.15PM		02.00PM-03.00PM	03.00PM-04.00PM	04.00PM-05.00PM
MONDAY	I(A)	EME(RKC)	MAT(JBP)	Break-11:00AM-11:15AM	ELE(SBP)	PHY(VMB)	Lunch break-1.015pm To 2.00pm	A1– PHYL(LBK)	A2– WSL(SAM)	A3- ENGL(YSP)
	I(B)	PHY(LBK)	CIV(KSK)		MAT(SST)	EME(GVC)		ELE(SBP) *	PHY(LBK) *	ENG(YSP)
	I(C)	MAT(SAP)	PCD(RRP)		ENG(YSP)	CHE(MSH)		C1- CHEL(MSH)	C2- CPL(SRM)	C3- CEDL(TSV)
	I(D)	ELN(MMG)	MAT(SIS)		ES(KSK)	PCD(RRP)		CHE(SJW) *	CAED(DNI)	
Tuesday	I(A)	MAT(JBP)	PHY(VMB)		EME(RKC)	CIV(KSK) *		A2– PHYL(LBK)	A3– WSL(SAM)	A1- ENGL(YSP)
	I(B)	CIV(KSK)	PHY(LBK)		ELE(SBP)	MAT(SST)		CPH(RKH)	EME(GVC)*	CPH(RKH)
	I(C)	CHE(MSH)	PCD(RRP)		ELN(VGK)	CAED(TSV)		C2- CHEL(MSH)	C3- CPL(SRM)	C1-CEDL(TSV)
	I(D)	CHE(SJW)	ELN(MMG)		ENG(YSP)	PCD(RRP)		CHE(SJW)	ES(KSK)	ENGL(YSP)-D2*
Wednesday	I(A)	CIV(KSK)	ENG(YSP)		MAT(JBP)	ELE(SBP)		A3– PHYL(LBK)	A1– WSL(SAM)	A2- ENGL(YSP)
	I(B)	EME(GVC)	ELE(SBP)		CIV(KSK)	ENG(YSP)		MAT(SST) *	CIV(KSK) *	KAN
	I(C)	PCD(RRP)	CHE(MSH)		MAT(SAP)	ELN(VGK)*		C3- CHEL(MSH)	C1- CPL(SRM)	C2- CEDL(TSV)
	I(D)	ELN(MMG)	CHE(SJW)		CAED(DNI)	MAT(SIS) *		ELN(MMG) *	PCD(RRP) *	ENGL(YSP)D*
Thursday	I(A)	EME(RKC)	CIV(KSK)		ELE(SBP)	PHY(VMB)		CPH(RKH)	MAT(JBP) *	CPH(RKH)
	I(B)	ELE(SBP)	EME(GVC)		PHY(LBK)	CIV(KSK)		B1– PHYL(VMB)	B2– WSL(SRK)	B3- ENGL(YSP)
	I(C)	ELN(VGK)	ENG(YSP)		PCD(RRP)	MAT(SAP)*		ES(KSK)	CHE(MSH)	ENGL(YSP)C*
	I(D)	PCD(RRP)	MAT(SIS)		CHE(SJW)	ENG(YSP)		D1- CHEL(SJW)	D2- CPL(MRP)	D3- CEDL (DNI)
Friday	I(A)	PHY(VMB)	EME(RKC)		CIV(KSK)	MAT(JBP)		ELE(SBP) *	PHY(VMB) *	KAN
	I(B)	PHY(LBK)	MAT(SST)		EME(GVC)	ELE(SBP)		B2– PHYL(VMB)	B3– WSL(SRK)	B1- ENGL(YSP)
	I(C)	ES(KSK)	ELN(VGK)		MAT(SAP)	CHE(MSH)*		CAED(TSV)	ENGL(YSP)C2*	
	I(D)	PCD(RRP)	MAT(SIS)		ELN(MMG)	CAED(DNI)		D2- CHEL(SJW)	D3- CPL(MC)	D1- CEDL (DNI)
Saturday	I(A)	ELE(SBP)	CIV(KSK)		EME(RKC)*	ENG(YSP)				
	I(B)	MAT(SST)	B3– PHYL(VMB) B1– WSL(SRK) B2-ENGL(YSP)							
	I(C)	MAT(SAP)	CAED(TSV)		ELN(VGK)	PCD(RRP) *				
	I(D)	MAT(SIS)	D3–CHEL D1-CPL(SMM) D2-CEDL (DNI)							

Time Table Coordinator

HOD

CO-ORDINATOR
First Year Engineering
Hirasugar Institute of Technology,
NIDASOSHI-591 236

PRINCIPAL

Nidasoshi-591 236, Taq: Hukkeri, Dist: Belagavi, Karnataka, India.
Phone: +91-8333-278887, Fax: 278886, Web: www.hsit.ac.in, E-mail: principal@hsit.ac.in

Name of the Faculty member	Qualification	Designation	Date of joining the institution	Department with which associated	Distribution of teaching load (%)					
					1st year		UG		PG	
					Hrs	%	Hrs	%	Hrs	%
Smt. J. B. Patil	M.Sc. (Ph.D)	Assoc. Prof.	28-01-1997	Engg. Mathematics	04	57	03	43	-	-
Shri. S. S. Thabaj	M.Sc , B.Ed	Assist. Prof.	15-07-2013	Engg. Mathematics	04	40	06	60	-	-
Shri. S. A. Patil	M.Sc. , B.Ed	Assist. Prof.	03-02-2014	Engg. Mathematics	04	40	60	60	-	-
Shri S. I. Shivamoggimath	M.Sc. , B.Ed	Assist. Prof.		Engg. Mathematics	04	50	04	50	-	-
Dr. M. S. Hanagadakar	M.Sc Ph.D	Assoc. Prof.	19-02-2007	Engg. Chemistry	08	100	-	-	-	-
Shri. S. J. Walki	M.Sc (Ph.D)	Assist. Prof.	05-02-2016	Engg. Chemistry	08	100	-	-	-	-
Shri. V. M. Bhumannavar	M.Sc. , B.Ed (Ph.D)	Assist. Prof.	24-01-2014	Engg. Physics	08	100	-	-	-	-
Smt. L. B. Kankanwadi	M.Sc , B.Ed	Assist. Prof.	16-08-2012	Engg. Physics	08	100	-	-	-	-
Miss. Kavya Kallimani	M. Tech	Assist. Prof.	13-07-2015	Environmental Studies and Civil Engg	12	100	-	-	-	-
Shri R. Chitagopkar	M. Tech	Assist. Prof.	26/8/2008	Mechanical Engineering	04	29	10	71	-	-
Shri. S. B. Patil	M. Tech	Assist. Prof.	04-08-2008	Electrical and Electronic Engg.	08	100	-	-	-	-
Shri. S. R. Kulkarni	M. Tech	Assist. Prof.	21-08-2007	Mechanical Engineering	09	60	06	40	-	-
Shri. G. V. Chinivalar	M. Tech	Assist. Prof.	03-08-2007	Mechanical Engineering	04	50	04	50	-	-
Shri. R. V. Nyamgoudar	M.Tech	Assist. Prof.	02-02-2015	Mechanical Engineering	09	53	04	24	04	23
Shri T. S. Vandali	M.Tech	Assist. Prof.	02/08/2010	Mechanical Engineering	03	100	-	-	-	-
Shri. D. N. Inamadhar	M.Tech	Assist. Prof.	01-09-2005	Mechanical Engineering	03	100	-	-	-	-
Shri R. R. Patil	M.Tech	Assist. Prof.	11/7/16	Computer Science	08	100	-	-	-	-

Dr. V. G. Kasabegoudar	M.Tech	Prof.		Electronic and communication Engg.	05	56	04	44	-	-
Shri K. K. Kamati	M. Tech	Assist. Prof.	28/01/2017	Computer Science	03	100	-	-	-	-
Mr. M. M. Gadag	M. Tech	Assist. Prof.		Electronic and communication Engg.	05	100	-	-	-	-
Shri. S. R. Koteppagol	M.A.	Lecturer	05-09-2002	Kannada	04	100	-	-	-	-
Smt. Y. S. Patil	M.A.	Sr. Lecturer	09-03-2015	English	04	100	-	-	-	-

Prepared by
(S. J. Walki)

NBA Co-ordinator

HOD(First Year) 4/4/18

LESSON PLAN

Subject with code : Engg. Mathematics-I, 15MAT11/17MAT11 Lect. Per week : 04

Class : BE I sem (Div-A) EC

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
3	1) Vector Calculus: Derivative of vector valued function	20/9/17	13/9/17		
	2) velocity, Acceleration and related problems.	22/9/17	18/9/17		
	3) Problems	25/9/17	22/9/17		22/9
	4) Scalar and vector point functions	26/9/17	25/9/17		
	5) Definition of gradient, Divergence and curl.	27/9/17	26/9/17		
	6) Problems.	3/10/17	27/9/17		
Planned Hrs:				Actual Hrs :	
	7) Solenoidal and Irrotational vector fields	4/10/17	28/9/17		
	8) Problems	6/10/17	3/10/17		3/10
	9) Vector Identities - div(ØA), Curl(ØA), Curl(Ø) div(∇×A)	9/10/17	9/10/17		
	10) Problems.	10/10/17	10/10/17		
Planned Hrs: 10				Actual Hrs : 10	

HOD

PRINCIPAL

LESSON PLAN

Subject with code : Engg. Chemistry [17CHE12] Lect. Per week : 4+1

Class : I Sem (D)

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
	Electrochemistry & Battery Technology				
	1) Introduction, Derivation of Nernst eqn. for	7/8/17	9/8/17		
01	electrode potential.				
	2) Reference electrode: Introduction, constru	8/8/17	9/8/17		
	-ction, Working and application of calo				
	-mel electrode.		10/8/17		
	3) Ag/AgCl electrode. Measurment of electro	8/8/17	11/8/17		12/8
	-de potential using calomel electrode.				
	4) Ion-Selective electrode: Introduction, de	9/8/17	16/8/17		
	termination of pH using glass electrode	-			
Planned Hrs:				Actual Hrs :	
	and its construction, working				
	5) Concentration Cell: electrolyte concen	10/8/17	16/8/17		
	-tration cell, numerical problems.		18/8/17		19/8
	Battery Techonology				
	6) Introduction, classification - primary	16/8/17	21/8/17		
	Secondary and reserve batteries.		22/8/17		
	7) characterstics - cell potential, Current	17/8/17	23/8/17		
	capacity, electricity storage density,				
	energy efficency cycle life and		23/8		
	Shell life.				
Planned Hrs:				Actual Hrs :	

HOD

PRINCIPAL

LESSON PLAN

Subject with code : Elements of civil Engg & Mech (15CIV13) Lect. Per week : 4+1

Class : I Sem DIV-A

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
Module I.	* Introduction to civil Engg & mechanics – scope of civil	8/8/17	8/8/17	[illegible]	
	Infrastructure, Types of Infrastructure	9/8/17	9/8/17	[illegible]	
	* Role of civil Engg in infrastructure				
	Roads, functions, comparision				
	of Pavement. Bridges. Types of bridges				
	* Dams, Types of dams. culverts	10/8/17	10/8	[illegible]	
	different types of culverts				
	* Structural behaviour &	11/8/17	11/8	[illegible]	[illegible] 12/8
	functions.				
Planned Hrs:				Actual Hrs :	
I.	* Introduction to Engg Mechanics	16/8/17	16/8	[illegible]	
	Basic Idealization, particle,	1			
	Continuum, Rigid, Newton's law				
	characteristics and gravitation				
	* Principle of forces, support	17/8/17	17/8/17	[illegible]	
	position, independance, Transmissibility				
	* Couple, characteristics and	18/8/17	18/8	[illegible]	
	Equivalent force-couple system				
	* Moment couple system	19/8/17	18/8/17	[illegible]	[illegible] 19/8
	* Resultant force	22/8/17	19/8/17		
	* Numerical problems.	23/8/17	23/8/17	[illegible]	
Planned Hrs:	10			Actual Hrs :	10

HOD PRINCIPAL

LESSON PLAN

Subject with code : Engg. Physics 15PHY12 Lect. Per week : 04

Class : DIV - B (Sem - I)

Chapter/ Unit	Details to be Covered	Date Planned	Date Actual	Signature Faculty	Signature HOD
	MODERN PHYSICS AND QUANTUM				
	MECHANICS				
	Introduction to black body radiation	7/8/17	8/8/17	JBK	
	Spectrum. Assumption & law of black				
	body radiation. wave particle dualism,	8/8/17	8/8/17	JBK	
	de-Broglie hypothesis, matter waves &	10/8/17	9/8/17	JBK	
	its characteristics properties. Phase				
	velocity, group velocity & Relation betn	11/8/17	10/8/17	JBK	
	these velocities, Heisenberg's uncertainty				
	Principle.	14/8/17	11/8/17	JBK	
Planned Hrs:				Actual Hrs :	
	Wave function, Properties & Physical	14/8/17	14/8/17	JBK	
	significances, Probability density &				
	Normalization, Schrodinger's time	16/8/17	17/8/17	JBK	
	independent equation. Eigen value &				
	Eigen functions. Application of	18/8/17	18/8/17	JBK	
	Schrodingers equation of a free				
	particle in a potential well of infinite	21/8/17	21/8/17	JBK	
	depth.		21/8/17	JBK	
	Problems	22/8/17	22/8/17	JBK	
	Problems				
Planned Hrs: 10				Actual Hrs : 11	

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FIRST YEAR DEPT
Academic
Consolidated Time Table RV00
2017-18 (EVEN)

w.e.f.:05-02-2018

DAY	Div	9.00AM-10.00AM	10.00AM-11.00AM		11.15AM-12.15PM	12.15PM-01.15PM		02.00PM-03.00PM	03.00PM-04.00PM	04.00PM-05.00PM
MON	A	ELN	MAT-II	Break-11:00AM-11:15AM	CHE	ELN	Lunch break-1.15pm To 2.00pm	PCD	ENG	ENGLA1
	B	MAT-II	CHE		ELN	CHE		CHEL B1,CAEDLB3,CPLB2		
	C	EME	PHY		ELE	CIV		PHYLC3, WSLC2, ENGLC1		
	D	CIV	MAT-II		EME	PHY		ELE		
TUE	A	CAED	ELN		CHE	ELN		MAT-II	ES	
	B	CHE	ELN		CHE	ENG		CHEL B2,CAEDLB1,CPLB3		
	C	MAT-II	CIV		PHY	EME		PHYLC1,WSLC3,ENGLC2		
	D	PHY	ELE		PHY	CIV		MAT-II	EME	ENG
WED	A	ELN	CHE		PCD	MAT-II		ES	CAED	ENGLA2
	B	PCD	CHE		MAT-II	ELN		CHEL B3,CAEDLB2,CPLB1		
	C	CIV	ELE		PHY	MAT-II		PHYLC2, WSLC1, ENGLC3		
	D	PHY	ELE		CIV	PHY		MAT-II	ENG	
THU	A	CHE	MAT-II		CAED	PCD		CHEL A1,CAEDLA3,CPLA2		
	B	ELN	CAED		PCD	MAT-II		PCD	ENGLB1	
	C	ELE	CIV		PHY	ENG		MAT-II	EME	
	D	EME	ELE		EME	CIV		PHYLD1, WSLD2, ENGLD3		
FRI	A	PCD	CHE		ENG	PCD		CHEL A2,CAEDLA1,CPLA3		
	B	ES	ELN		MAT-II	CAED		PCD	ENG	ENGLB2
	C	PHY	MAT-II		CIV	EME		ELE		
	D	EME	MAT-II		ELE	MAT-II		PHYLD2, WSLD3, ENGLD1		
SAT	A	MAT-II	CHEL A3,CAEDLA2,CPLA1							
	B	CAED	MAT-II		ES	PCD				
	C	ELE	MAT-II		EME	ENG				
	D	CIV	PHYLD3, WSLD1, ENGLD2							

*Tutorial class /**ADDITIONAL MATHEMATICS

Faculty details

II-Semester /DIV-A			II- Semester/ DIV-B		
Staff Name	Sub.	Code	Staff Name	Sub.	Code
Mr. S. I. Shivamoggimath	MAT-II	17MAT21	Mr.S.L.Patil	MAT-II	17MAT21
Mr. S. J. Walki	CHE	17CHE22	Dr. M.S. Hanagadakar	CHE	17CHE22
Mr.C.R.Belavi	PCD	17PCD23	MR.R.R.Patil	PCD	17PCD23
Mr. K.G.Ambli	CED	17CED24	Mr.T.S.Vandali	CED	17CED24
Mr. K.G.Ambli/Mr.S.R.Nakadi	CED Lab	17CED24	Mr.T.S.Vandali	CEDLab	17CED24
Mr.M.M.Gadag	ELN	17ELN25	Dr.S.B.Shrigiri	ELN	17ELN25
Mr.M.A.Gholap	CPL	17CPL26	Mr. K.K. Kamati	CPL	17CPL26
Mr. S. J. Walki	CHEL	17CHEL27	Mr. S. J. Walki	CHEL	17CHEL27
Mr.M.M.Nagarmunnoli	ES	17CIV28	Mr.M.M.Nagarmunnoli	ES	17CIV28
	ENG	ENG		ENG	ENG
	ENGLab	ENGL		ENGLab	ENGL

II-Semester /DIV-C			II- Semester/ DIV-D		
Staff Name	Sub.	Code	Staff Name	Sub.	Code
Mrs.J.B.Patil	MAT-II	17MAT21	Mr.S.S.Thabaj	MAT-II	17MAT21
Mrs.L.B.Kankanawadi	PHY	17PHY22	Mr. V. M. Bhumannavar	PHY	17PHY22
Mr.M.M.Nagarmunnoli	CIV	17CIV23	Mr.M.M.Nagarmunnoli	CIV	17CIV23
Mr.G.V.Chinivalar	EME	17EME24	Mr. R.Chitgopkar	EME	17EME24
Mr.S.B.Patil	ELE	17ELE25	Mrs H.R. Zinage	ELE	17ELE25
Mr.R.V.Nyamagoud	WS Lab	17WSL26	Mr. S.R.Kulkarni	WSLab	17WSL26
Mrs.L.B.Kankanawadi	PHY Lab	17PHYL27	Mrs.L.B.Kankanawadi	PHYLab	17PHYL27
	ENG	ENG		ENG	ENGL
	ENG Lab	ENGL		ENGLab	ENG

Prepared by:

HOD

CO-ORDINATOR
First Year Engineering
Hirasugar Institute of Technology.

01/02/18

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I-Year Engg. Dept.
Work Load
2017-18 (Even)
Rev. 01

S. No.	Faculty Name	Sem./ Div.	Strength	Subject/ Subject Code	Theory Hrs./ Week	Practical/Seminar/Project Hrs./ Week	Work Load (Hrs./Week)	Staff Sign.
1.	Dr. M.S. Hanagadakar	II B(CSE)	41	Engg. Chemistry /17CHE22	05	--	05	
2.	Prof. V. M. Bhumannavar	II D(ME/EEE)	37	Engineering Physics./17PHY22	05	--	05	
3.	Prof. J. B. Patil	II C(ME)	45	Engg. Mathematics –II /17MAT21	05	--	09	
		IV ME (A)	65	Engg. Mathematics –IV /15MAT41	04			
4.	Prof. S.L. Patil	II B (CSE)	41	Engg. Mathematics –II /17MAT21	05	--	09	
		IV EE	55	Engg. Mathematics –IV /15MAT41	04			
5.	Prof. S.A. Patil	IV CS	52	Engg. Mathematics –II /17MAT21	04	--	08	
		IV All branches	45	Additional Maths-II /DIPMAT41	04	--		
6.	Prof. L. B. Kankanawadi	II C (ME)	45	Engineering Physics./17PHY22	05	--	23	
		II C (ME)	45	Engg. Physics Lab /17PHYL27	--	09		
		IID(ME/EEE)	37	Engg. Physics Lab /17PHYL27	--	09		
7.	Prof. S. S. Thabaj	II D(ME/EEE)	37	Engg. Mathematics –II /17MAT21	05	--	09	
		IV (ECE)	41	Engg. Mathematics –IV /15MAT41	04	--		
8.	Prof. S I. Shivamoggimath	II A (ECE)	45	Engg. Mathematics –II /17MAT21	05	--	09	
		IV ME (B)	61	Engg. Mathematics –IV /15MAT41	04	--		
9.	Prof. M.M. Nagarmunnoli	II C (ME)	45	Elements of Civil Engg. & Mechanics/ 17CIV23	05	--	14	
		II D(ME/EEE)	37	Elements of Civil Engg. & Mechanics /17CIV23	05	--		
		II A (ECE)	38	Environmental studies /17CIV28	02	--		
		II B(CSE)	41	Environmental studies /17CIV28	02	--		
10.	Prof. S. J. Walki	II A (ECE)	38	Engg. Chemistry /17CHE22	05	--	23	
		II A (ECE)	38	Engg. Chem Lab /17CHEL27	--	09		
		II B(CSE)	41	Engg. Chem Lab /17CHEL27	--	09		
11.	New Faculty	II A (ECE)	38	English/ English Lab	02	02	18	
		IIB(CSE)	41	English/ English Lab	02	02		
		II C (ME)	45	English/ English Lab	02	03		
		IID(ME/EEE)	37	English/ English Lab	02	03		

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CO-ORDINATOR
First Year Engineering
Hirasugar Institute of Technology,
NIDASOSHI-591 236.

Principal

LESSON PLAN

Course : Engg. Mathematics - II Code : 17MAT21 Lect. Per week : 04

Sem. : BE - II Sem (Div - A)

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
	Differential Equation - I				
I	1) Soln. of II & Heigher order DE's - problems	5/2	5/2	Sub	
	2) Inverse diffn. operator - Type-I → $e^{ax}/f(D)$	7/8	7/2	Sub	
	3) Type - II : $\frac{\sin ax}{f(D^2)}$ or $\frac{\cos ax}{f(D^2)}$	8/2	7/2 8/2	Sub Sub	07/2/20
	4) Type - III : $\phi(x)/f(D)$	10/2	10/2	Sub	
	5) Type - IV : $e^{ax}v/f(D)$	12/2	12/2	Sub	15/2/20
	6) Type - V : $\frac{xv}{f(x)}$, $\frac{x^n v}{f(D)}$	15/2	17/2	Sub	
	7) Type - VI : Mixed type problems	19/2	19/2 21/2	Sub Sub	
	8) Undetermined coefficients	21/2	24/2	Sub	
	9) Problems	22/2	26/2	Sub	
	10) Method of variation of parameters	24/2	26/2	Sub	
Planned Hrs: 10				Actual Hrs : 12	
	Differential Equation - II				
II	1) Soln. of Cauchy's Linear DE's	26/2	28/2	Sub	
	2) Soln. of Legendre's Linear DE's	28/2	28/2	Sub	
	3) Equation Solvable for 'p'	01/03	12/3 12/3	Sub Sub	10/3
	4) Equation Solvable for 'y'	03/3	14/3	Sub	
	5) Equation Solvable for 'x'	08/3	15/3	Sub	
	6) General & Singular Solutions	10/3	19/3	Sub	19/3
	7) Clairaut's equation	12/3	24/3	Sub	
	8) problems	14/3	25/3	Sub	
	9) Eqn. reducible to clairaut's form	15/3	28/3	Sub	
	10) Problems	19/3	2/4	Sub	
Planned Hrs: 10				Actual Hrs : 11	

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PRINCIPAL

LESSON PLAN

Course : Engg. chemistry (17CHE22) Code : 17CHE22 Lect. Per week : 05

Sem. : II - Sem B - Div.

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
IV 1	polymers :- Introduction. Types of	9-4-18	26-2-18		
	Polymer. Addition & condensation polymer				
2	Mechanism of polymer of free radical	9-4-18	26-2-18		
	taking vinylchloride as an example.				
3	mol. wt of polymers. No. of avg &	10-4-18	27-2-18		
	wt avg. Numerical problems				
4	Glass transition tempre: Factors influence		27-2-18		
	rating Tg. Sign of Tg.				
5	str. property relation: crystallinity	16-4-18	28-2-18		
	tensile strength. Elasticity & chemical resistivity.		28-2-18		
Planned Hrs:				Actual Hrs :	
6	Synthesis, prop and application of PMMA	16-04-18	9-3-18		
	Polyurethane and Polycarbonates				
7	Elastomers: Introduction, silicone rubber	17-4-18	12-3-18		
	synthesis prop. and applications.				
8	Adhesives: Introduction. Epoxy resin	18-4-18	12-3-18		
	synthesis prop & applications.				
9	Polymer composites: Introduction,	23-4-18	13-3-18		
	Kevlar fiber synthesis, prop & application				
10	Conducting polymers: Introduction,	23-4-18	13-3-18		
	mechanism in PANI & applications.				
Planned Hrs: 10				Actual Hrs : 14	

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PRINCIPAL

LESSON PLAN

Course : B. Engg. Physics Code : 17PHY22 Lect. Per week : 04

Sem. : B.E. II sem Division 'D' [ME & EEE]

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
	Modern Physics & Quantum Mechanics :				
1	Black body radiation spectrum, Assumptions of quantum theory of radiation	05-02-18	05-02-18		
	Planck's law, Weins law and Rayleigh Jeans law, for shorter and longer wavelength limits.	06-02-18	06-02-18		
	Wave particle dualism, de-Broglie hypothesis. Compton Effect. Matter waves and their characteristic properties.	06-02-18	06-02-18		6/2/18
Planned Hrs:				Actual Hrs :	
	Definition of phase velocity and group velocity, Relation between phase velocity and group velocity	07-02-18	07-02-18		
	Relation between group velocity and particle velocity.	07-02-18	07-02-18		
	Heisenberg's uncertainity principle and its application, (Non-existance of electron in the nucleus)	12-02-18	19-2-18		19/2/18
Planned Hrs: 1				Actual Hrs :	

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LESSON PLAN

Course : Engg. Mathematics-II [17MAT21] Code : 17MAT21 Lect. Per week : 04

Sem. : II [Div-B] CS

Module/ Unit	Details of content to be taught	Date Planned	Date Actual	Signature Faculty	Signature HOD
	Differential Equations-I				
	1. Soln of 2nd & higher order D.Es	5/2	07/2		
	2. Inverse Differential operator $\frac{e^{ax}}{f(D)}$	7/2	08/2		
	3. $\frac{\sin ax}{f(D)}$ or $\frac{\cos ax}{f(D)}$	8/2	10/2		10/2/16
	4. $\frac{x^m}{f(D)}$	9/2	12/2		
I	5. $\frac{e^{ax}V}{f(D)}$	10/2	15/2		15/2/16
	6. $\frac{xV}{f(D)}$, $\frac{x^nV}{f(D)}$	12/2	17/2		
	7. Mixed Type problems	17/2	19/2		
	8. Undetermined coeffn	19/2	20/2		
	9. Method of variation of Parameters	21/2	22/2		
	10. Problems	22/2	23/2		
Planned Hrs: 10				Actual Hrs :	10
	Differential Equations-II				
	1. Soln of Cauchy's Linear D.Es	23/2	24/2		24/2
	2. Soln of Legendre's Linear D.Es	24/2	26/2		
	3. Eqn solvable for P	26/2	01/3		
	4. Eqn solvable for x	28/2	8/3		
II	5. Eqn solvable for y	02/3	9/3		
	6. General & singular solns	03/3	12/3		
	7. Clairauits Eqns	9/3	14/3		
	8. Problems	10/3	15/3		14/3
	9. Eqn reduciable to clairauit's form 10. Problems	12/3 14/3	} 16/3		
Planned Hrs: 10				Actual Hrs :	10

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COURSE PLAN

Subject Title	**ELECTROMAGNETIC FIELD THEORY**		
Subject Code	15EE45	**IA Marks**	20
Number of Lecture Hrs /	04	**Exam Marks**	80
Total Number of Lecture Hrs	50	**Exam Hours**	03
			CREDITS –04

FACULTY DETAILS:		
Name: Prof. B. R. Umarane.	**Designation:** Associate Professor	**Experience:** 21
No. of times course taught: 13	**Specialization:** Digital Techniques & Instrumentation.	

1.0 Prerequisite Subjects:

Sl. No	Branch	Semester	Subject
01	Engineering Mathemetics	I/II	Vector Algebra
02	Engineering Mathemetics	III/(10+2)	Trignometry, Calculas

2.0 Course Objectives

1. To study different coordinate systems for understanding the concept of gradient, divergence and curl of a vector.
2. To study the application of Coulomb's Law and Gauss Law for electric fields produced by different charge configurations.
3. To calculate the energy and potential due to a system of charges.
4. To study the behavior of electric field across a boundary between a conductor and dielectric and between two different dielectrics.
5. To calculate the distribution of electric potential & electric field using poisons & Laplace equation.
6. To find the variation of steady magnetic fields using Biot-Savart's law, Ampere's circuital law
7. To study the magnetic force and magnetic materials.
8. To study the time varying fields and propagation of waves in different media

3.0 Course Outcomes

At the end of the course the student will be able to:

	Course Outcome	Cognitive Level	Pos
C213.1	Use the concept of vectors & different coordinate systems to calculate electric force & electric fields produced by different charge configurations.	L_1, L_2, L_3	PO1,PO2PO3
C213.2	Calculate the energy and potential due to a system of charges & explain the behavior of conductors & dielectrics.	L_1, L_2, L_3	PO1,PO2 PO3
C213.3	Calculate the behavior of electric field distribution using poisons & Laplce equations & distribution of steady magnetic field using Biot-Savart's law, Ampere's circuital law	L_1, L_2, L_3	PO1,PO2 PO3
C213.4	Explain the behavior of force, magnetic materials & magnetic circuit. ~~1,2,3,7~~	L_1, L_2, L_3	PO1,PO2PO3
C213.5	Assess time varying fields and propagation of waves in different media.	L_1, L_2, L_3	PO1,PO2 PO3, PO7
Total Hours of instruction		**50**	

4.0 Course Content

MODULE-1:

Vector Analysis: Scalars and Vectors, Vector algebra, Cartesian co-ordinate system,Vector components and unit vectors. Scalar field and Vector field. Dot product and Cross product, Gradient of a scalar field. Divergence and Curl of a vector field. Co – ordinate systems: cylindrical and spherical, Relation between different coordinate systems. Expression for gradient, Divergence and curl in rectangular, Cylindrical and spherical coordinate systems. Problems.

Electrostatics: Coulomb's law, Electric field intensity and its evaluation for (i) point charge (ii) line charge (iii) surface charge (iv) volume charge distributions. Electric flux density, Gauss law and its applications. Maxwell's first equation (Electrostatics). Divergence theorem. Problems.

10 HOURS

MODULE-2:

Energy and Potential: Energy expended in moving a point charge in an electric field. The line integral. Definition of potential difference and potential. The potential field of a point charge and of a system of charges. Potential gradient. The dipole. Energy density in the electrostatic field. Problems.

Conductor and Dielectrics: Current and current density. Continuity of current. Metallic conductors, Conductor's properties and boundary conditions. Perfect dielectric materials, capacitance calculations. Parallel plate capacitor with two dielectrics with dielectric interface parallel to the conducting plates. Capacitance of two wire line. Problems.

10 HOURS

MODULE-3:

Poisson's and Laplace equations: Derivations and problems, Uniqueness theorem.

Steady magnetic fields: Biot-Savart's law, Ampere's circuital law. The Curl. Stokes theorem. Magnetic flux and flux density. Scalar and vector magnetic potentials. Problems. **10 HOURS**

MODULE-4:

Magnetic forces: Force on a moving charge and differential current element. Force between differential current elements. Force and torque on a closed circuit. Problems.

Magnetic materials and magnetism: Nature of magnetic materials, Magnetization and permeability. Magnetic boundary conditions. Magnetic circuit, Inductance and mutual inductance. Problems. **10 HOURS**

MODULE-5:

Time varying fields and Maxwell's equations: Faraday's law, Displacement current. Maxwell's equations in point form and integral form. Problems.

Uniform plane wave: Wave propagation in free space and in dielectrics. Pointing vector and power considerations. Propagation in good conductors, skin effect. Problems. **10 HOURS**

5.0 Relevance to future subjects

Sl No	Semester	Subject	Topics
01	VII	High voltage Engineering	Time varying fields, Maxwell's equations
02	V	Transmission & distribution	Gauss law, electric flux, Electric field, Magnetic field.
03	III, IV	Electric motors, Generators, Transformers	Magnetic field, Magnetic flux density

6.0 Relevance to Real World

Sl. No	Real World Mapping
01	Communication Systems
02	Radars, Magneto hydrodynamic generators, Electric motors , Electric generators, Cathode Ray Tubes many more.

7.0 Gap Analysis and Mitigation

Sl. No	Delivery Type	Details
01	Tutorial	Assignment problems be discussed in the tutorial classes to clear the concepts usage of appropriate formulas.
02	NPTEL	Video Lectures will be used to clear the concepts.

8.0 Books Used and Recommended to Students

Text Books/Reference Books

1. Engineering Electromagnetics William H Hayt et al Mc Graw Hill 8thEdition, 2014
2. Principles of Electromagnetics Matthew N. O. Sadiku Oxford University Press 6th Edition, 2015
3. Fundamentals of Engineering Electromagnetics David K. Cheng Pearson 2014
4. Electromagnetism - Theory (Volume -1) -Applications (Volume-2) Ashutosh Pramanik PHI Learning 2014
5. Electromagnetic Field Theory Fundamentals Bhag Guru et al Cambridge University press 2005
6. Electromagnetic Field Theory Rohit Khurana Vikas Publishing 1st Edition,2014
7. Electromagnetics J. A. Edminister Mc Graw Hill 3rd Edition, 2010
8. Electromagnetic Field Theory and Transmission Lines Gottapu Sasibhushana Rao Wiley 1st Edition, 2013

Additional Study material & e-Books

1. VTU Question papers

9.0 Relevant Websites (Reputed Universities and Others) for Notes /Animation / Videos Recommended

Website and Internet Contents References
1. NPTEL Videos 2. www.wikipedia.com

10.0 Magazines/Journals Used and Recommended to Students

Sl.No	Magazines/Journals	Website
1	Journal of Electromagnetic Analysis and Applications	http://www.scirp.org/journal/JEMAA/
		https://www.physicsforums.com

11.0 Examination Note

Internal Assessment: 20 Marks(15 Marks Internal Assessment + 5 Marks Assignment) :
Internal Assessment is conducted for 25 Marks and is scaled down to 15 Marks
Scheme of Evaluation for Internal Assessment (25 Marks)
Student has to answer two full questions as per the format shown below.

Q.1 a	13	Q.3 a	12
b		b	
OR		OR	
Q.2 a	13	Q.4 a	12
b		b	

SCHEME OF EXAMINATION (80 Marks):

1. The question paper will have ten questions.
2. Each full question is for 16 marks.
3. There will be 2 full questions (with a maximum of four sub questions in one full question) from each module.
4. Each full question with sub questions will cover the contents under a module.
5. Students will have to answer 5 full questions, selecting one full question from each module

12.0 Course Delivery Plan

Module	Lecture No.	Content of Lecturer	% of Portion
1	1	**Vector Analysis:** Scalars and Vectors, Vector algebra, Vector components and unit vectors, Scalar field and Vector field,Dot product and Cross product	20
	2,3	Co – ordinate systems: Cartesian co-ordinate system, cylindrical and spherical, Relation between different coordinate systems.	
	4,5	Gradient of a scalar field. Divergence and Curl of a vector field, problems.	
	6,7	Electrostatics: Coulomb's law, Electric field intensity and its evaluation for (i) point charge (ii) line charge (iii) surface charge (iv) volume charge distributions.	
	8	Problems on Coulombs law & Electric field Intensity	
	9	Electric flux density, Gauss law and its applications. Maxwell's first equation (Electrostatics).	
	10	Divergence theorem. Problems.	
2	11	**Energy and Potential:** Energy expended in moving a point charge in an electric field. The line integral. Definition of potential difference and potential.	20
	12	Problems on work done	
	13	The potential field of a point charge and of a system of charges. Potential gradient. The dipole.	
	14,15	Energy density in the electrostatic field. Problems.	
	16	**Conductor and Dielectrics:** Current and current density. Continuity of current. Metallic conductors, Conductor's properties	
	17	Boundary conditions. Perfect dielectric materials problems	
	18,19	capacitance calculations. Parallel plate capacitor with two dielectrics with dielectric interface parallel to the conducting plates	
	20	Capacitance of two wire line. Problems.	
3	21	**Poisson's and Laplace equations:** Derivations and problems, Uniqueness theorem.	20
	22,23	Problems	

	24,25	**Steady magnetic fields:** Biot - Savart's law problems, Ampere's circuital law.	
	26,27	The Curl. Stokes theorem, problems	
	28	Magnetic flux and flux density. Scalar and vector magnetic potentials.	
	29,30	Problems	
4	31	**Magnetic forces:** Force on a moving charge and differential current element Force between differential current elements	20
	32	Force and torque on a closed circuit	
	33,34	Problems.	
	35	Nature of magnetic materials, Magnetization and permeability.	
	36,37	Magnetic boundary conditions, Problems	
	38	Magnetic circuit, Inductance and mutual inductance. Problems.	
	39,40	Problems.	
5	41	**Time varying fields and Maxwell's equations:** Faraday's law, Displacement current.	20
	42	Maxwell's equations in point form and integral form.	
	43,44	Problems.	
	45,46	**Uniform plane wave:** Wave propagation in free space and in dielectrics.	
	47	Pointing vector and power considerations.	
	48	Propagation in good conductors, skin effect.	
	49,50	Problems.	

13.0 Assignments, Quiz

Sl. No.	Title	Outcome expected	Allied study	Week No.	Individual / Group activity	Reference: book/website /Paper
1	Assignment-I: Questions on Coordinate geometry, Coulomb's Law, Gauss law & Divergence	Student will be able to understand coordinate geometry, divergence, curl & apply to solve the problems on various charges.	Module-1	4	Individual	1 & 7
2	Assignment-II: Problems on Energy & potential, Conductors & Dielectrics.	Understand the concept of Energy & potential ,Conductors & dielectrics & solve the problems	Module-2	6	Individual	1 & 7
3	Assignment-III: Problems on Poisson's & Laplace equations , Steady magnetic fields.	Student will be able to solve the problems on potential distribution using Poisson's & Laplace equations, understand the concept steady magnetic field & solve the problems.	Module-3	9	Individual	1 & 7
4	Assignment-IV: Problems on Magnetic forces, Magnetic materials and magnetism	Student will be able to solve the problems on magnetic force,materials, boundary conditions & magnetic circuits	Module-4	12	Individual	1 & 7
5	Assignment-V: Problems on Time varying fields & wave propagations.	Student will be able to numerical on time varying magnetic fields & wave propagations in different mediums.	Module-5	15	Individual	1 & 7

14.0 QUESTION BANK

MODULE-1

1. Define i) vector ii) Scalar iii) Unit vector.
2. Define Dot product & cross product. List their properties.
3. What is divergence & curl of a vector?
4. Obtain the expression for incremental length, surface & volume in rectangular coordinate system.

5. Obtain the expression for incremental length, surface & volume in cylindrical coordinate system.
6. Obtain the expression for incremental length, surface & volume in spherical coordinate system.
7. Derive the relationship between i) Rectangular & cylindrical Coordinates ii) Rectangular & Spherical Coordinate system.
8. Define gradient of a scalar field? List the expression for gradient in rectangular, cylindrical & spherical Coordinate system.
9. Define divergence of a vector field. Derive an expression for divergence of a vector field in rectangular, cylindrical & spherical coordinate system.
10. Explain the physical significance of divergence of a vector field.
11. What is curl of a vector field? Derive an expression for 'curl' of a vector field in rectangular, cylindrical & spherical coordinate system.
12. State and explain coulombs law in vector form. Derive an expression for the electric field intensity due to 'n' no. of charges.July-2009,July-2006,Jan-2004,Aug.2003,Mar.-2001,Jan-2015
13. State and explain coulombs law in vector form. Mention the units of each terms involved.July-2008.
14. State and explain inverse square law for electric field in the vector form. Mention the unit of each term involved.
15. Derive an expression for EFI due to an infinite line charge of linear charge density. Dec-2004,Jan-2016,July-2008.
16. Define EFI due to point charge in vector form. With usual notation, derive the expression for field due to many charges -- Dec-2009,Jan-2016,Jan-2013,June-2012.
17. Find EFI at $(0,\phi, h)$ in cylindrical co-ordinates due to uniformly charged disk for $r=a$, $z=0$. Dec-2009.
18. Consider a uniform ring charge of radius 'a'. Derive the general expression for filed electric field vector 'E' at a height 'h' mt ($h<a$), along the axis of the ring charge and normal to its plane.Jan-2007.
19. Charge is uniformly distributed along an∞ straight line with constant density ρl. develop the expression for E at the general point P.
20. Use Gauss law to determine the EFI due to infinite line charge. Jan-2016.
21. Derive Maxwell's first equation in electrostatics.Jan-2016.
22. What is divergence of a vector field? Obtain the point form of Gauss law.June-2012.
23. What is divergence of a vector field? What do positive & negative divergence represent?June-2010.
24. Derive an expression for EFI due to an infinite line charge density using Gauss Law.
25. State and prove divergence theorem or Maxwell's divergence theorem.
26. State and represent Gauss law in mathematical form for the charge enclosed by a sphere.
27. Write the expressions for div D in three co-ordinate systems. What is the physical significance of divergence?
28. State and prove Gauss law applied to an electric field.Jan-2016,Jan-2009,Jul-2015.
29. State and prove Gauss law. How are the Gaussian surfaces chosen?Jan-2008,Feb-2002,Aug.-2001.
30. State Gauss theorem. Mention the nature of Gaussian surface. July-2007.
31. From Gauss law prove $\nabla D=\rho_v$July-2008.
32. Using Gauss law, determine EFI everywhere due to a hollow sphere of charge.July-2009.
33. State and explain Gauss law, obtain the relation between D& E. Indicate the nature of Gaussian surface.
34. State Gauss law, using Gauss law obtain an expression for electric field intensity E due to a uniformly charged infinite plane sheet. July-2008.
35. Obtain an expression for EFI due to solid sphere of charge.
36. State and prove Gauss's law in point form.
37. Define a) electric flux, b) Electric flux density, mention their units.
38. There exists a spherical volume charge of radius 'a' with uniform charge density ρ_v, obtain the electric field intensity E and sketch it as a function of r. verify the divergence theorem for $r<a$.
39. Explain the terms i) Electric field intensity ii) Electric potential. Also bring out the relation between them.July-2006.
40. State & explain the Gauss Divergence theorem. Aug.-2005,Jan-2004,Jan-2013,Jun.-2013.

MODULE- 2

1. Explain the concept of work& potential as applied to an electric field & hence obtain the an expression for the potential difference between two points in an electric field produced by a point charge. July-2006
2. Explain the concept of work & potential as applied to an electric field & charge & hence derive a quantified form of work done from the first principle. July-2002.
3. State & explain the point form of Ohm's law, in the presence of an electric field; also find the current density vector.July-2002.
4. Obtain the point form of continuity equation for current. Comment on the result that free charge cannot remain within a conductor, instead is distributed evenly over the conductor surface.July-2005.
5. Explain the idea of continuity of current equation& hence obtain the an expression for the field equivalent of Kirchhoff's law current law.Aug-2002.
6. Show that the energy required to assemble n number of charges is $W_E=(1/2)\Sigma Q_m V_m$ & hence derive expression for energy in electric field in terms of field quantities $\vec{D}$ & $\vec{E}$ Jan-2016,June-2010
7. Derive an expression for electric potential due to infinite line charges using Gauss law.March-2001, Jan-2016.
8. Derive boundary conditions for conductor & Dielectric interface.Jan-2016,June-2012,Feb-2004.

9. Derive an equation for energy stored in terms of D & E.Feb-2004.
10. Show that the electric field intensity E can be expressed as a negative gradient of scalar potential. June-2012.
11. Show that $\vec{E} = -\nabla$ Feb-2004,July-2003,Aug-2001.
12. Derive an expression for energy expended in moving a point charge in an electric field. June-2012,Jan-2009.
13. Discuss the boundary conditions between two perfect dielectrics. July-2008,July2013.
14. Obtain boundary conditions for dielectric-dielectric boundary July-2009.
15. Obtain the conditions on the tangential & normal components of field intensity & electric flux density at the boundary between two dielectric media.Aug.2001.
16. Derive an expression for the potential of co-axial cable in the dielectric space between inner & outer conductor.March-2001.
17. Discuss with relevant equations the potential of a system of charges & obtain the potential field of a ring of a ring of a uniform line charge density. July 2013.
18. Discuss the current & current density & derive the expression for continuity equation. July2013.
19. Derive an expression relating convection current density , volume charge density & velocity of the charge element.Jan-2009.
20. Show that the electric field at any point is given by the negative of the gradient of potential at the point. July 2013.
21. With usual notations prove $\nabla = -\delta\rho_v/\delta t$ Jul.2007,Jul-2008,July-2002.
22. Define & explain the following terms i) Electric field intensity ii) Electric flux density

    iii) Gaussian surface July-2003.
23. Explain the concept of capacitance/capacity of a dielectric medium separated by conducting zones & hence obtain the expression energy stored in a capacitor with the usual notations $W=(1/2)CV^2$.
24. Derive Poisson's & Laplace equations – Dec 2009,July-2013.
25. State & prove uniqueness theorem – Dec 2009, july-2009,Jan-2013,July-2013,Dec-2010,Jan2010,Jan-2008.

MODULE- 3:

1. Derive Poisson's & Laplace equations starting from point form of Gauss's law.-Dec 2007.
2. Using Laplace equation, derive the expression for potential & electric filed distributions for a parallel plate capacitor. Apply proper boundary conditions.
3. Starting from Gauss's law in integral form , derive Laplace & Poisson's equations. Write Laplace's equation in all the coordinate systems.June-2012,Jan-2010
4. Starting with deduce Poisson's & Laplace equation. - Aug-2003,
5. Obtain Poisson's & Laplace equation from Maxwell's first equation.Jan-2013.
6. Obtain Poisson's & Laplace equation; write Laplace equation in explicit form in Cartesian cylindrical & Spherical co-ordinate system. Jan- 2010, July-2006
7. Using Laplace equation derive an expression for the capacitance of a concentric spherical capacitor- Jan-2010
8. Using Laplace equation derive an expression for capacitance of co-axial cable.Jan-2013
9. Write expression for Laplace equation in rectangular, Cylindrical & Spherical coordinate system.Jan-2015.
10. A large spherical cloud of radius b has a uniform volume charge distribution of ρ_v C/m^3, find the potential distribution & electric field intensity at any point in space using Laplace equation, Jan-2013
11. Use Laplace's equation; determine the distribution of potential & electric field intensity between two spherical conductors separated by a dielectric. The inner conductor is at potential V_0 while the outer conductor is grounded.
12. Using Laplace equation, derive an expression for the capacitance of a concentric spherical capacitor. Jan-201
13. State and prove Biot-savart Law. Mention the unit of each quantity involved.
14. Obtain an expression for magnetic field intensity at any point on the axis of a circular coil.
15. State and Prove Stokes theorem.
16. Co axial cable with radius of inner conductor 'a' meter, inner radius of outer conductor 'b' meter and outer radius 'c' meter carries a current of 'I' ampere at inner conductor and –I at outer conductor. Determine and sketch variation of 'H' against 'r' for

    a) $r < a$ b) $a < r < b$ c) $b < r < c$ d) $r > c$
17. State and Explain amperes circuital law. Write in point form.
18. Derive the Gauss Law for magnetic field in point form. Hence show that scalar magnetic potential follows Laplace equation
19. Explain the concept of vector magnetic potential.
20. Using Biot-savrt Law find an expression for magnetic field of a straight filamentary conductor carrying current I in the Z- direction.
21. Clearly differentiate between scalar and vector magnetic potential.
22. Obtain an expression for vector magnetic potential.

23. State and prove Ampere's Circuit Law and apply it to a infinite long conductor to calculate magnetic field intensity.
24. Find vector magnetic potential 'A' at a point due to a straight current carrying conductor of length 2l meter and hence find flux density

MODULE-4:

1. With usual notations, derive the equation for magnetic force between two differential current elements (force between two loop current circuit)
2. Define self inductance and mutual inductance with suitable formulae.
3. Find the expression from the force on differential current carrying elements.
4. Derive an expression for self inductance of a co-axial cables.
5. Obtain an expression of magnetic force between two current elements and hence for current loops.
6. Discuss the boundary conditions to apply B& H at the interface between two different magnetic materials.
7. Define self and mutual inductance of coil.
8. Derive the equation for energy density in a magnetic field.
9. Obtain the Lorenz force equation for force on an electric charge moving in a uniform magnetic field.
10. Derive an expression for the force on a differential current element placed in magnetic field.
11. Derive an expression for force between two circular current carrying conductors
12. Derive the expression for the inductance of toroidal ring with N turns and carrying I amp current.
13. Derive an expression for force between two current loops.
14. Derive an expression for force between two parallel conductors.
15.
16. Derive the boundary conditions on H & B.
    OR. Discuss the boundary conditions at the interface between two media of different

MODULE -5:

1. Derive the Maxwell's equation from Ampere's law.July-2005
2. State and explain Faraday's laws for emf when a closed conductor single loop circuit is placed in time varying magnetic field.
3. Write Maxwell's equations for free space in point and integral form.July-2009
4. ~~Write a short note on retarded potential. Derive the expressions for the same.~~
5. List he Maxwell's equations in differential and integral form as applied to time varying fields.July-2014
6. Explain the transformer and motional induced emf's.Aug-2001,June-2009
7. Write the Maxwell's equations in point form for static fields and in integral form for time varying fields.
8. Obtain the expression for emf induced in Faraday's disc generator. Indicate the type of emf induced.
9. Show that the conduction current in the wire is equal to the displacement current in the dielectric of a capacitor subjected to a time varying filed.Feb-2004,Dec-2010
10. What is meant by displacement current? Show that for a harmonically varying electric field, the conduction and displacement currents are in time phase quadrature.
11. Write the Maxwell's equations in integral form derived from Ampere's law & Gauss Law for good conductor medium and for free space.
12. What is the inconsistency of Ampere's law with the equation of continuity? Derive the modified form of Ampere's law of Maxwell.July-2009
13. Explain Faraday's laws applied to i) Stationary path changing field, ii) Steady field moving circuit.
14. ~~Write a note on retarded potential.July-2014~~
15. Develop from first principles the four Maxwell's equations in both point & integral form. July-2002
16. Enumerate the Maxwell's equations for time varying electric & Magnetic fields.Aug-2003,Feb-2004
17. Prove that $\nabla \times E = -\delta B/\delta t$Dec-2010.
18. Explain the interpretation of Faraday's law applicable to time-varying magnetic field & derive the expression for Transformer emf& motional emf.July-2011
19. State the integral forms of Maxwell's equations applicable to time-varying magnetic fields.Give the wordy description of each statement also. June-2011
20. Derive the continuity equation from Maxwell's equation.June-2010
21. Obtain the relation between electric filed intensity (E) and magnetic field intensity (H) in a perfect dielectric medium.
22. State & prove Poynting theorem July-2008
23. Define depth of penetration. Show that depth of penetration of wave in a conductor decreases with increase in frequency
24. Derive electromagnetic wave equation for a homogeneous medium
25. What do you mean by depth of penetration?
26. Explain electromagnetic wave propagation in perfect dielectric.
27. Prove that $E/H = \sqrt{(\mu/\varepsilon)}$
28. Starting from Maxwell's equation, derive the wave equations for a uniform plane wave traveling in free space.
29. Obtain wave equation of E & H.
30. Discuss the uniform plane wave propagation in good conducting medium.
31. With usual notation, derive equation for infinite impedance for lossy medium.
32. What is uniform plane wave? Derive wave equations for free space in terms of E &H .

33. Define wave equation? Derive wave equation in a general medium.July-2009
34. Show that uniform plane electromagnetic wave is transverse in nature.
35. Show that at any point in a space the cross product of electric and magnetic field intensity vector is a measure of rate of energy flow per unit area at that point.
36. Derive the general expression for EM wave; modify it to suit for i) good conductor, ii) Lossy dielectric, iii) perfect dielectric.
37. For an electromagnetic wave propagating in free space , prove that i) $|E|/|H|=\eta$ ii)E & H are mutually perpendicular.July-2009

16.0 University Result

Examination	FCD/S+,S,A	FC/B	SC/C,D,E	% Passing
July 2017	3	06	45	75.42

Prepared by	Checked by		
Prof. B. R. Umarane	Prof. O B Heddurashetti	HOD	Principal

LAB MANUAL

CONTENT

Course: Microcontroller Laboratory-I	Course Code:15EEL57
Sl No.	**Experiment list**
1	**Data transfer – Program for block data movement, sorting, exchanging, finding largest element in an array**
1a)	Write an ALP to move a block of data from one internal memory location to other.
1b)	Write an ALP to move a block of data from one external memory location to other
1c)	Write an ALP to exchange a block of data from one internal memory location to other.
1d)	Write an ALP to exchange a block of data from one external memory location to other
1e)	Write an ALP to find smallest number in the array.
1f)	Write an ALP to find largest number in an array.
1g)	Write an ALP to arrange numbers in ascending order.
1h)	Write an ALP to arrange numbers in descending order.
2	**Arithmetic instructions: Addition, subtraction, multiplication and division. Square and cube operations for 16 bit numbers.**
2a)	Write an ALP to find addition of two 8 bit numbers.
2b)	Write an ALP to find subtraction of two 8 bit numbers.
2c)	Write an ALP to find multiplication of two 8 bit numbers.
2d)	Write an ALP to find division of two 8 bit numbers.
2e)	Write an ALP to square of a 8 bit numbers.
2f)	Write an ALP to cube of a 8 bit numbers.
2g)	Write an ALP to find addition of two 16 bit numbers.
2h)	Write an ALP to find subtraction of two 16 bit numbers.
2i)	Write an ALP to square of a 16 bit numbers.
3	**Counter**
3a)	Write an ALP to generate Hex up counter.
3b)	Write an ALP to generate Hex down counter.
3c)	Write an ALP to generate BCD up counter
3d)	Write an ALP to generate BCD down counter.
4	**Boolean and logical instructions (bit manipulation).**
4a)	Write an ALP to compute the following. IF X=0; THEN NUM1 (AND) NUM2, IF X=1; THEN NUM1 (OR) NUM2, IF X=2; THEN NUM1 (XOR) NUM2, ELSE RES =00, RES IS 23H LOCATION Using logical instructions in byte level.
5.	**Conditional call and return instructions.**
5a)	Write a program to toggle all the bits of port 1 continuously by sending the values 55H and AAH using call and return instructions.
5b)	Write an ALP to find factorial of a number using call and return instructions.

Sl No.	Experiment list
6	**Code conversion programs – BCD to ASCII, ASCII to BCD, ASCII to decimal, Decimal to ASCII, Hexa decimal to and Decimal to hexa.**
6a)	Write an ALP to convert hexadecimal number to decimal number.
6b)	Write an ALP to convert decimal number to hexadecimal number.
6c)	Write an ALP to convert packed BCD number to ASCII number.
6d)	Write an ALP to convert ASCII number to BCD number
7	**Programs to generate delay, Programs using serial port and on-chip timer/counters.**
7a)	Write an ALP to toggle the content of port 0 continuously using timer delay in between.
7b)	Write an ALP to transmit characters to a PC HyperTerminal using the serial port and display on the serial window.
	Interfacing
8	**Stepper motor interface.**
8a)	Write a C program to rotate stepper motor in clockwise/anticlockwise direction.
9	**DC motor interface for direction and speed control using PWM.**
9a)	Write a C program to show the on off control of DC motor.
10	**Alphanumerical LCD panel interface.**
10a)	Write a C program to send letters to LCD using delays.
11	**Generate different waveforms: Sine, Square, Triangular, Ramp using DAC interface.**
11a)	Write a C program to generate Square wave using DAC interface to 8051.
11b)	Write a C program to generate Ramp wave using DAC interface to 8051.
11c)	Write a C program to generate triangular wave using DAC interface to 8051.
11d)	Write a C program to generate Sine wave using DAC interface to 8051.
12	**Elevator interface.**
12a)	Write a C program to show control and operation of elevator using 8051.

Sl No.	Experiment Name
1	**Data transfer – Program for block data movement, sorting, exchanging, finding largest element in an array**
1a)	Write an ALP to move a block of data from one internal memory location to other.
1b)	Write an ALP to move a block of data from one external memory location to other
1c)	Write an ALP to exchange a block of data from one internal memory location to other.
1d)	Write an ALP to exchange a block of data from one external memory location to other
1e)	Write an ALP to find smallest number in the array.
1f)	Write an ALP to find largest number in an array.
1g)	Write an ALP to arrange numbers in ascending order.
1h)	Write an ALP to arrange numbers in descending order.

1a)	Write an ALP to move a block of data from one internal memory location to other.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov r0,#30h	;r0=30h i.e. initial block memory location
	mov r1,#40h	;r1=40h i.e. block memory location where data has to transfer
	mov r2,#05h	; load counter as 05h (n) in register r2
NEXT:	mov a,@r0	;copy the contents of memory location pointed by register r0 into A
	mov @r1,a	;copy the contents of register A into memory location pointed by reg r1
	inc r0	;increment register r0
	inc r1	;increment register r1
	djnz r2, NEXT	;decrement register r2 if not equal to zero jump to next
	lcall 0003h	;end of asm file

INPUT	OUTPUT

1b)	Write an ALP to move a block of data from one external memory location to other

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov r0,#04h	;load the counter as 04h (n) in register r0
	mov r1,#81h	;higher byte of initial block=81h in register r1
	mov r2,#85h	;higher byte of memory location where data has;to transfer=85h in reg r2
	mov r3,#00h	;lower byte of both initial and final block=00h in register r3
UP:	mov dph,r1	;DPH=contents of register r1
	mov dpl,r3	;DPL=contents of register r3
	movx a,@dptr	;copy the contents of memory location pointed by DPTR into reg A
	mov dph,r2	;dph=contents of register r2
	movx @dptr,a	;copy the contents of reg A into memory location pointed by reg dptr
	inc r3	;increment register r3
	djnz r0,UP	;decrement register r0 if not equal to zero jump to up
	lcall 0003h	;end of asm file

INPUT	OUTPUT

1c)	Write an ALP to exchange a block of data from one internal memory location to other.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov r0,#30h	;r0=30h i.e. initial block memory location
	mov r1,#40h	;r1=40h i.e. block memory location where data has to transfer
	mov r4,#04h	; load counter as 04h (n) in register r4
UP:	mov a,@r0	;copy the contents of memory location pointed by register r0 into A
	mov r6,a	;copy the contents of register A into memory location pointed by reg r6
	mov a,@r1	;copy the contents of memory location pointed by register r1 into reg A
	mov @r0,a	;copy the contents of register A into memory location pointed by reg r0
	mov a,r6	;copy the contents of r6 in register A
	mov @r1,a	;copy the contents of register A into memory location pointed by reg r1
	inc r0	;increment register r0
	inc r1	;increment register r1
	djnz r4,UP	;decrement register r4 if not equal to zero jump to up
	lcall 0003h	;end of asm file

INPUT	OUTPUT

1d)	Write an ALP to exchange a block of data from one external memory location to other

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov r0,#04h	;load the counter as 04h (n) in register r0
	mov r1,#81h	;load higher byte of initial block=81h in register r1
	mov r2,#85h	;load higher byte of memory location where data has to transfer i.e 85h in reg r2
	mov r3,#00h	;load lower byte of both initial and final block=00h in register r3
UP:	mov dph,r1	;DPH=contents of register r1
	mov dpl,r3	;DPL=contents of register r3
	movx a,@dptr	;copy the contents of memory location pointed by DPTR into reg A
	mov b,a	; move the content of a into b register
	mov dph,r2	;DPH=contents of r2
	movx a,@dptr	;store the contents of register a in memory location pointed by dptr
	mov dph,r1	;DPH=contents of r1
	movx @dptr,a	;store the content of a into memory location pointed by r1
	mov dph,r2	;DPH=contents of r2
	mov a,b	;move the content of B into A register
	movx @dptr,a	;copy the contents of register a into memory location pointed by reg dptr
	inc r3	;increment register r3
	djnz r0,UP	;decrement register r0 if not equal to zero jump to up
	lcall 0003h	;end of asm file

INPUT	OUTPUT

1e)	Write an ALP to find smallest number in the array.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	Mov dptr,#8500h	;dptr=8500h
	mov r0,#04h	;load counter as 04h (n-1) in register r0
	movx a,@dptr	;contents of memory location pointed by DPTR are copied in reg A
	mov b,a	; move the content of A into B register
Again:	inc dptr	; increment DPTR
	movx a,@dptr	;contents of memory location pointed by DPTR are copied in reg A
	cjne a,b,Next	;compare a and b if not equal jump to next
	ajmp Skip	;absolute jump to skip
Next:	jnc skip	;jump if no carry to skip
	mov b,a	;if no carry then copy contents of register A in B
Skip:	djnz r0,Again	;decrement register r0 if not equal to zero jump to again
	mov a,b	;copy the contents of register B in A
	mov dptr,#9000h	;dptr=9000h
	movx @dptr,a	;store the smallest number in 9000h
	lcall 0003h	;end of asm file

INPUT	OUTPUT

1f)	Write an ALP to find largest number in an array.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500h	;dptr=8500h
	mov r0,#04h	;load counter as 04h (n-1) in register r0
	movx a,@dptr	;contents of memory location pointed by DPTR are copied in reg A
	mov b,a	; move the content of A into B register
Again:	inc dptr	; increment DPTR
	movx a,@dptr	contents of memory location pointed by DPTR are copied in reg A
	cjne a,b,Next	;compare a and b if not equal jump to next
	ajmp Skip	;absolute jump to skip
Next:	jc skip	;jump if carry to skip
	mov b,a	;if no carry then copy contents of register A in B
Skip:	djnz r0,Again	;decrement register r0 if not equal to zero jump to again
	mov a,b	;copy the contents of register B in A
	mov dptr,#9000h	;dptr=9000h
	movx @dptr,a	;store the largest number in 9000h
	lcall 0003h	;end of asm file

INPUT	OUTPUT

1g)	Write an ALP to arrange numbers in ascending order.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov R0,#04h	;load the outer counter as 04h (n-1) in register r0
Again:	mov DPTR,#8500h	;dptr=8500h
	mov R1,#04h	;load the inner counter as 04h (n-1) in register r1
Back:	mov R2,dpl	;dptr=contents of r2
	movx A,@dptr	;copy the contents of memory location pointed by reg DPTR into reg A
	mov B,A	;copy the contents of register A in register B
	inc DPTR	;increment the dptr
	movx A,@DPTR	;copy the contents of memory location pointed by dptr into register a
	cjne A,B,Next	;compare a and b if equal jump to next
	ajmp Skip	;absolute jump to skip
Next:	jnc Skip	;jump if no carry to skip
	mov DPL,R2	;DPL=contents of register r2
	movx @DPTR,A	;copy the contents of reg A into memory location pointed by reg DPTR
	inc dptr	;increment DPTR
	mov A,B	;copy the contents of register B in register A
	movx @DPTR,A	;copy the contents of register a into memory location pointed by DPTR
Skip:	djnz R1, Back	;decrement register r1 if not equal to zero jump to Back
	djnz R0,Again	;decrement register r0 if not equal to zero jump to again
	lcall 0003h	;end of asm file

INPUT	OUTPUT

1h)	Write an ALP to arrange numbers in descending order.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov R0,#04h	;load the outer counter as 04h (n-1) in register r0
Again:	mov DPTR,#8500h	;dptr=8500h
	mov R1,#04h	;load the inner counter as 04h (n-1) in register r1
Back:	mov R2,dpl	;dptr=contents of r2
	movx A,@dptr	;copy the contents of memory location pointed by DPTR into reg A
	mov B,A	;copy the contents of register A in register B
	inc DPTR	;increment the dptr
	movx A,@DPTR	;copy the contents of memory location pointed by reg DPTR into reg A
	cjne A,B,Next	;compare a and b if equal jump to next
	ajmp Skip	;absolute jump to skip
Next:	jc Skip	;jump if carry to skip
	mov DPL,R2	;DPL=contents of register r2
	movx @DPTR,A	;copy the contents of reg A into memory location pointed by reg DPTR
	inc dptr	;increment DPTR
	mov A,B	;copy the contents of register B in register A
	movx @DPTR,A	;copy the contents of register a into memory location pointed by DPTR
Skip:	djnz R1, Back	;decrement register r1 if not equal to zero jump to Back
	djnz R0,Again	;decrement register r0 if not equal to zero jump to again
	lcall 0003h	;end of asm file

INPUT	OUTPUT

2	**Arithmetic instructions: Addition, subtraction, multiplication and division. Square and cube operations for 16 bit numbers.**
2a)	Write an ALP to find addition of two 8 bit numbers.
2b)	Write an ALP to find subtraction of two 8 bit numbers.
2c)	Write an ALP to find multiplication of two 8 bit numbers.
2d)	Write an ALP to find division of two 8 bit numbers.
2e)	Write an ALP to square of a 8 bit numbers.
2f)	Write an ALP to cube of a 8 bit numbers.
2g)	Write an ALP to find addition of two 16 bit numbers.
2h)	Write an ALP to find subtraction of two 16 bit numbers.
2i)	Write an ALP to square of a 16 bit numbers.

2a)	Write an ALP to find addition of two 8 bit numbers.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500h	;dptr=contents of 8500h
	movx a,@dptr	;get the lower byte from memory location 8103h in register A
	mov r0,a	;store in register r0
	inc dptr	;increment dptr
	movx a,@dptr	;get the lower byte from memory location 8101h in register A
	Addc a, r0	;add first lower byte and second lower byte
	jnc Next	;jump if no carry to here
	inc r5	;if carry then increment register r5
Next:	mov dptr,#9001h	;DPTR=9001h
	movx @dptr,a	;dptr=store the lower byte in memory location 9101h
	mov a,r5	;store the contents of r5(carry) in register A
	dec dpl	;decrement DPL i.e. DPTR=9000h
	movx @dptr,a	;store the carry in memory location in 9000h
	lcall 0003h	;end of asm file

INPUT	OUTPUT

2b)	Write an ALP to find subtraction of two 8 bit numbers.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500h	;dptr=contents of 8500h
	movx a,@dptr	;get the lower byte from memory location 8500h in register A
	mov r0,a	;store in register r0
	inc dptr	; increment dptr
	movx a,@dptr	;get the lower byte from memory location 8501h in register A
	subb a,r0	;sub content of r0 from a
	jnc Next	;jump if no carry to here
	inc r5	;if carry then increment register r5
Next:	mov dptr,#9001h	;DPTR=9001h
	movx @dptr,a	;dptr=store the lower byte in memory location 9001h
	mov a,r5	;store the contents of r5(carry) in register A
	dec dpl	;decrement DPL i.e. DPTR=9100h
	movx @dptr,a	;store the carry in memory location in 9100h
	lcall 0003h	;end of asm file

INPUT	OUTPUT

2c)	Write an ALP to find multiplication of two 8 bit numbers.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500h	;dptr=8500h
	movx a,@dptr	;store the number in register a from memory location 8500h
	mov b,a	;copy the number in register B
	inc dptr	;dptr=8501h
	movx a,@dptr	; store the number in register a from memory location 8501h
	mul ab	;multiply A and B
	inc dptr	;increment DPTR
	movx @dptr,a	;store the higher byte in memory location in 8501h
	mov a,b	;copy the contents of register b in register a
	inc dptr	;increment DPTR
	movx @dptr,a	;store the lower byte in memory location 8502h
	lcall 0003h	;end of asm file

INPUT	OUTPUT

2d)	Write an ALP to find division of two 8 bit numbers.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500h	;dptr=8501h
	movx a,@dptr	;store the number in register a from memory location 8500h
	mov b,a	;copy the number in register B
	dec dpl	; dptr=8500h
	movx a, @dptr	store the number in register a from memory location 8501h
	div ab	;divide A by B
	mov dptr, #9500H	;increment DPTR
	movx a, @dptr	;store the quotient in memory location in 8501h
	mov a,b	;store the reminder in register b
	inc dptr	;increment DPTR
	movx @dptr,a	;store the reminder in memory location 8502h
	lcall 0003h	;end of asm file

INPUT	OUTPUT

2e)	Write an ALP to square of a 8 bit numbers.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500h	;dptr=8500h
	movx a,@dptr	;store the number in register a from memory location 8500h
	mov b,a	;copy the number in register B
	mul ab	;multiply A and B
	inc dptr	;increment DPTR
	movx @dptr,a	;store the higher byte in memory location in 8501h
	mov a,b	;copy the contents of register b in register a
	inc dptr	;increment DPTR
	movx @dptr,a	;store the lower byte in memory location 8502h
	lcall 0003h	;end of asm file

INPUT	OUTPUT

2f)	Write an ALP to cube of a 8 bit numbers.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500h	;dptr=8500h
	movx a,@dptr	;store the number in register a from memory location 8500h
	mov b,a	;copy the number in register B
	mov r3,a	;save in register r3
	mul ab	;multiply A and B
	mov r4,b	;save the first lower byte in r4
	mov b,r3	;load once again register B with number which has to cube
	mul ab	;multiply first higher byte stored in register A with B
	mov dptr,#9002H	;Load DPTR by 9002H
	movx @dptr,a	; store the result in DPTR
	mov r2,b	;save the second lower byte in r2
	mov a,r4	;store the first lower byte in register in a from r4
	mov b,r3	;store the contents of register in B
	mul ab	;multiply register A and B
	add a,r2	;add register A and register r2
	dec dpl	; decrement dptr
	movx @dptr,a	;store the higher byte in memory location in 9001h
	mov a,#00h	;a=00h
	addc a,b	;add register A and B with carry
	dec dpl	;decrement dpl
	movx @dptr,a	;copy the contents of A in memory location pointed by DPTR
	lcall 0003h	;end of asm file

INPUT	OUTPUT

2g)	Write an ALP to find addition of two 16 bit numbers.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8503h	;dptr=contents of 8503h
	movx a,@dptr	;get the lower byte from memory location 8503h in register A
	mov r1,a	;store content of A in register r1
	mov dptr,#8501h	;dptr=contents of 8501h
	movx a,@dptr	;get the lower byte from memory location 8501h in register A
	Add a, r1	;A=A+R1
	mov dptr,#9002h	; DPTR= 9002h
	movx @dptr,a	; 9002H=A
	mov dptr,#8502h	; DPTR= 8502h
	movx a,@dptr	;A= content of 8502h
	mov r2,a	; R2=A
	mov dptr,#8500h	; DPTR= 8500h
	movx a,@dptr	; A=content of 8500H
	Addc a, r2	;A=A+R2+C
	jnc Next	;jump if no carry to next
	inc r7	;if carry then increment register r7
Next:	mov dptr,#9001h	;DPTR=9001h
	movx @dptr,a	;dptr=store the lower byte in memory location 9001h
	dec dpl	;decrement DPL i.e. DPTR=9000h
	mov a,r7	; A=R7
	movx @dptr,a	;store the carry in memory location in 9000h
	lcall 0003h	;end of asm file

INPUT	OUTPUT

2h)	Write an ALP to find subtraction of two 16 bit numbers.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8503h	;dptr=contents of 8503h
	movx a,@dptr	;get the lower byte from memory location 8503h in register A
	mov r1,a	;store content of A in register r1
	mov dptr,#8501h	;dptr=contents of 8501h
	movx a,@dptr	;get the lower byte from memory location 8501h in register A
	subb a, r1	;A=A+R1
	mov dptr,#9002h	; DPTR= 9002h
	movx @dptr,a	; 9002H=A
	mov dptr,#8502h	; DPTR= 8502h
	movx a,@dptr	;A= content of 8502h
	mov r2,a	; R2=A
	mov dptr,#8500h	; DPTR= 8500h
	movx a,@dptr	; A=content of 8500H
	subb a, r2	;A=A+R2+C
	jnc Next	;jump if no carry to next
	inc r7	;if carry then increment register r7
Next:	mov dptr,#9001h	;DPTR=9001h
	movx @dptr,a	;dptr=store the lower byte in memory location 9001h
	dec dpl	;decrement DPL i.e. DPTR=9000h
	mov a,r7	; A=R7
	movx @dptr,a	;store the carry in memory location in 9000h
	lcall 0003h	;end of asm file

INPUT	OUTPUT

2i)	Write an ALP to square of a 16 bit numbers.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr, #8501h	;dptr=8501h
	movx a, @dptr	;store the number in register a from memory location 8501h
	mov r0, a	; store the content reg a in r0
	dec dpl	; dpl=dpl-1
	movx a, @dptr	;store the number in register a from memory location 8500h
	mov r1, a	; store the content reg a in r1
	mov a, r0	; store the content reg r0 in a
	mov b,a	; store the content reg a in b
	mul ab	; a=axb
	mov dptr, #9005h	;dptr=9005h
	movx @dptr, a	;store the number in register a from memory location 9005h
	mov r2, b	; store the content reg b in r2
	mov a, r0	; store the content of reg r0 in a
	mov b, r1	; store the content reg r1 in b
	mul ab	; a=axb
	add a, r2	; add content r2 with a
	mov r3, a	; store the content of reg a in r3
	mov r2, b	; store the content of reg b in r2
	clr a	; a=0
	addc a, r2	; a=a+c+r2
	mov a, r0	; store the content of reg r0 in a

	mov b, r1	; store the content of reg r1 in b
	mul ab	; a=axb
	mov r7, a	; store the content of reg a in r7
	mov r2, b	; store the content of reg b in r2
	mov a, r1	; store the content of reg r1 in a
	mov b, r1	; store the content of reg r1 in b
	mul ab	; a=axb
	add a, r2	; a=a+r2
	mov r6, a	; store the content of reg a in r6
	mov a,b	; store the content of reg b in a
	mov r5,a	; store the content of reg a in r5
	mov a, r3	; store the content of reg r3 in a
	add a, r7	;a=a+r7
	dec dpl	;dpl=dpl-1
	movx @dptr, a	;store the number in register a from memory location
	mov a, r4	; store the content of reg r4 in a
	addc a, r6	; a=a+c+r6
	dec dpl	;dpl=dpl-1
	movx @dptr, a	;store the number in register a from memory location
	mov a, r5	; store the content of reg r5 in a
	dec dpl	;dpl=dpl-1
	movx @dptr,a	;store the number in register a from memory location
	lcall 0003h	end

INPUT	OUTPUT

3	**Counter**
3a)	Write an ALP to generate Hex up counter.
3b)	Write an ALP to generate Hex down counter.
3c)	Write an ALP to generate BCD up counter
3d)	Write an ALP to generate BCD down counter.

3a)	Write an ALP to generate Hex up counter.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov a,#00H	;load the accumulator with 00H
Up:	lcall Delay	;call delay
	inc a	; increment accumulator by 1
	cjne a,#40H,Up	;compare accumulator with 40h if not equal jump up
	lcall 0003H	; end
Delay:	mov r0,#0FFH	; load r0 by FFH
Back1:	mov r1,#0FFH	; load r1 by FFH
Back:	mov r2,#0FFH	; load r2 by FFH
Here:	djnz r2,Here	; decrement r2 if not equal to zero jump here
	djnz r1, Back	;decrement r1 if not equal to zero jump Back
	djnz r0,Back1	;decrement r0 if not equal to zero jump Back1
	RET	;return to main

INPUT	OUTPUT

3b)	Write an ALP to generate Hex down counter.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov a,#020H	;load the accumulator with 20H
Up:	lcall Delay	;call delay
	dec a	; decrement accumulator by 1
	cjne a,#00h,Up	;compare accumulator with 00H if not equal jump up
	lcall 0003H	; end
Delay:	mov r0,#0FFH	; load r0 by FFH
Back1:	mov r1,#0FFH	; load r1 by FFH
Back:	mov r2,#0FFH	; load r2 by FFH
here:	djnz r2,here	; decrement r2 if not equal to zero jump here
	djnz r1, Back	;decrement r1 if not equal to zero jump Back
	djnz r0,Back1	;decrement r0 if not equal to zero jump Back1
	RET	;return to main

INPUT	OUTPUT

3c)	Write an ALP to generate BCD up counter

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov a,#00H	;load the accumulator with 00H
Up:	lcall Delay	;call delay
	add a,#01H	; add accumulator with 0 1H
	da a	;decimal adjust accumulator
	cjne a,#30H,Up	;compare accumulator with 30h if not equal jump up
	lcall 0003H	; end
Delay:	mov r0,#0FFH	; load r0 by FFH
Back1:	mov r1,#0FFH	; load r1 by FFH
Back:	mov r2,#0FFH	; load r2 by FFH
Here:	djnz r2,Here	; decrement r2 if not equal to zero jump here
	djnz r1, Back	;decrement r1 if not equal to zero jump Back
	djnz r0,Back1	;decrement r0 if not equal to zero jump Back1
	RET	;return to main

INPUT	OUTPUT

3d)	Write an ALP to generate BCD down counter.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov a,#30H	;load the accumulator with 30H
Up:	lcall Delay	;call delay
	add a,#99H	; add accumulator with 99H
	da a	;decimal adjust accumulator
	cjne a,#00H,Up	;compare accumulator with 00h if not equal jump up
	lcall 0003H	; end
Delay:	mov r0,#0FFH	; load r0 by FFH
Back1:	mov r1,#0FFH	; load r1 by FFH
Back:	mov r2,#0FFH	; load r2 by FFH
Here:	djnz r2,Here	; decrement r2 if not equal to zero jump here
	djnz r1, Back	;decrement r1 if not equal to zero jump Back
	djnz r0,Back1	;decrement r0 if not equal to zero jump Back1
	RET	;return to main

INPUT	OUTPUT

4	**Boolean and logical instructions (bit manipulation).**
4a)	Write an ALP to compute the following. IF X=0; THEN NUM1 (AND) NUM2, IF X=1; THEN NUM1 (OR) NUM2, IF X=2; THEN NUM1 (XOR) NUM2, ELSE RES =00, RES IS 23H LOCATION Using logical instructions in byte level.

4a)	Write an ALP to compute the following. IF X=0; THEN NUM1 (AND) NUM2, IF X=1; THEN NUM1 (OR) NUM2, IF X=2; THEN NUM1 (XOR) NUM2, ELSE RES =00, RES IS 23H LOCATION Using logical instructions in byte level.

Label	Opcode and Operands	Comments
	org 0000h	
	ljmp 8000h	
	org 8000h	
	mov a, 20h	; Store the content of 20H in A
	mov r1, a	; Store the value of A in R1
	mov a, 21h	; Store the content of 21H in A
	cjne r1, #0, ckor	; compare the content of R1 with zero, if yes go to ckor otherwise go to next instruction
	anl a, 22h	; A=A and content of 22H
	mov 23h, a	; move the content of A in 23H
	sjmp end1	;End
ckor:	cjne r1, #1, ckxor	; if R1 is not equal to 1 go to ckxor, otherwise go to next instruction
	orl a, 22h	; Or the content of A with content of 22H
	mov 23h, a	; move the content of A in 23H
	sjmp end1	;end
ckxor:	cjne r1, #2h, other	; if R1 is not equal to 2 go to other, otherwise go to next instruction
	xrl a, 22h	; Ex-or the content of A with content of 22H
	mov 23h, a	; move the content of A in 23H
	sjmp end1	;end
other:	clr a	; A=0
end1:	mov 23h, a	; move the content of A in 23H
here:	sjmp here	;unconditional jump to here.
	end	;end

INPUT	OUTPUT

5.	**Conditional call and return instructions.**
5a)	Write a program to toggle all the bits of port 1 continuously by sending the values 55H and AAH using call and return instructions.
5b)	Write an ALP to find factorial of a number using call and return instructions.

5a)	Write a program to toggle all the bits of port 1 continuously by sending the values 55H and AAH using call and return instructions.

Label	Opcode and Operands	Comments
	org 0000h	
	ljmp 8000h	
	org 8000h	
	mov r5, #05h	; r5=05h
go:	mov a, #55h	;a=55h
	mov P1, a	; ;store the number in from memory location
	lcall 300h	; Long call to 300h i.e delay program
	mov a, #0aah	; move the content aaH to A
	mov P1, a	; move the content of A in P1
	lcall 300h	; Long call to 300h i.e delay program
	djnz r5, go	; R5=R5-1, if R5 is not equal to zero then jump to go
	lcall 0003h	; Long call to 300h i.e delay program
	org 300h	; Start delay program
here:	mov r0, #0ffh	; R0=FFH
back1:	mov r1, #0ffh	; R1=FFH
back:	djnz r2, back	; R2=R2-1, if R2 is not equal to zero then jump to back
	djnz r1, back1	; R1=R1-1, if R1 is not equal to zero then jump to back1
	djnz r0, here	; R0=R0-1, if R2 is not equal to zero then jump to here
	ret	; Return to main program

INPUT	OUTPUT

5b)	Write an ALP to find factorial of a number using call and return instructions.

Label	Opcode and Operands	Comments
	org 0000h	
	ljmp 8000h	
	org 8000h	
	mov a, 40h	;Move the content of 40H in A
	mov r0, a	; Move the content of A in R0
	lcall 9000h	;call 9000h
	lcall 3000h	; call 3000h
	org 9000h	; Originate 9000h
fact:	cjne r0, #01h, next	; if R0 is not equal to 1 then jump to next
	ret	; Return to main program
next:	dec r0	; R0=R0-1
	mov b, r0	; Move the content of R0 in b
	mul ab	;A=AxB
	mov r5, a	; Move the content of A in R5
	ljmp 9000h	; Go to 9000h
	end	; end

INPUT	OUTPUT

6	**Code conversion programs – BCD to ASCII, ASCII to BCD, ASCII to decimal, Decimal to ASCII, Hexa decimal to and Decimal to hexa.**
6a)	Write an ALP to convert hexadecimal number to decimal number.
6b)	Write an ALP to convert decimal number to hexadecimal number.
6c)	Write an ALP to convert packed BCD number to ASCII number.
6d)	Write an ALP to convert ASCII number to BCD number

6a)	Write an ALP to convert hexadecimal number to decimal number.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500H	;dptr=8500h
	movx a,@dptr	;copy the contents of memory location pointed by DPTR into register A
	mov b,#64H	;B=64H
	div ab	;division A/B
	inc dptr	;increment DPTR=8501H
	movx @dptr,a	;copy the contents of memory location pointed by DPTR into register A
	mov a,b	; move the content of B into A register
	mov b,#0AH	;Store the number 0AH into register B
	div ab	; division A/B
	inc dptr	; increment DPTR=8502H
	movx @dptr,a	; store the contents of register A in memory location pointed by DPTR
	inc dptr	;increment DPTR=8503H
	mov a,b	;move the content of B into A register
	movx @dptr,a	; store the contents of register A in memory location pointed by DPTR
	lcall 0003h	;end of asm file

INPUT	OUTPUT

6b)	Write an ALP to convert decimal number to hexadecimal number.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500H	;dptr=8500h
	movx a,@dptr	;copy the contents of memory location pointed by DPTR into reg A
	mov b,#64H	;B=64H
	mul ab	;multiply A and B
	mov r2,a	;store the contents of A in register r2
	inc dptr	;increment DPTR=8501H
	movx a ,@dptr	;copy the contents of memory location pointed by DPTR into register A
	mov b,#0AH	;Store the number 0AH into register B
	mul ab	; division A/B
	mov b,r2	;store the contents of R2 in register B
	add a,b	;add register A and B
	mov b,a	;store the contents of A in register B
	inc dptr	; increment DPTR=8502H
	movx a,@dptr	; copy the contents of memory location pointed by DPTR into register A
	add a,b	;add register A and B
	inc dptr	;increment DPTR=8503H
	movx @dptr,a	; store the contents of register A in memory location pointed by DPTR
	lcall 0003h	;end of asm file

INPUT	OUTPUT

6c)	Write an ALP to convert packed BCD number to ASCII number.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500h	;dptr=8500h
	movx a,@dptr	;store the contents of memory location pointed by DPTR into register A
	mov r0,a	;move the content of A to r0.
	anl a,#0fh	;make logical AND function with register A and immediate data 0FH
	orl a,#30h	;make logical OR function with register A and immediate data 30H
	inc dptr	;increment DPTR
	movx @dptr,a	;save the result in memory location 8501H
	mov a,r0	;get the once again BCD number in register A
	anl a,#0f0h	;make logical AND function with reg A and immediate data 0F0h
	swap a	;swap the contents of register A
	orl a,#30h	;make logical OR function with reg A and immediate data 30H
	inc dptr	;increment DPTR
	movx @dptr,a	;save the result in memory location 8502H
	lcall 0003h	;end of asm file

INPUT	OUTPUT

6d)	Write an ALP to convert ASCII number to BCD number

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov dptr,#8500h	;dptr=8500h
	movx a,@dptr	;store the contents of memory location pointed by DPTR into reg A
	anl a,#0fh	;make logical AND function with register A and immediate data 0FH
	mov b,a	;store in register B
	inc dptr	;increment DPTR
	movx a,@dptr	;get the second ASCII number in reg A from memory location 8501H
	anl a,#0fh	;make logical AND function with reg A and immediate data 0F0h
	swap a	;swap the contents of register A
	orl a,b	;make logical OR function with register A and B
	inc dptr	;increment DPTR =8502H
	movx @dptr,a	;save the result(BCD Number.) in memory location 8502H
	lcall 0003h	;end of asm file

INPUT	OUTPUT

7	**Programs to generate delay, Programs using serial port and on-chip timer/counters.**
7a)	Write an ALP to toggle the content of port 0 continuously using timer delay in between.
7b)	Write an ALP to transmit characters to a PC HyperTerminal using the serial port and display on the serial window.

7a)	Write an ALP to toggle the content of port 0 continuously using timer delay in between.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov r0, #0ah	; R0=0AH
go:	mov a, #55h	; A=55H
	mov P0, a	; Move the content of A in P0
	acall delay	; Call delay program
	mov a, #0aah	; A=AAh
	mov P0, a	; Move the content of A in P0
	acall delay	; Call delay program
	djnz r0, go	; R0=R0-1, if R0 is not equal to zero then jump to go
	lcall 0003h	; End of main program
delay:	mov TMOD, #01h	; Load TMOD
start:	mov TL0, #00h	; Load TL0
	mov TH0, #00h	;Load TH0
	setb TR0	; TR0=1;
here:	jnb TF0, here	;if TF0 is not equal 1 then jump to here
	clr TR0	; TR0=0
	clr TF0	;TF0=0
	ret	;Return to main program

7b)	Write an ALP to transmit characters to a PC HyperTerminal using the serial port and display on the serial window.

Label	Opcode and Operands	Comments
	org 0000h ljmp 8000h org 8000h	
	mov TMOD, #20h	; TMOD= 20H
	mov TH1, #-3	;TH1=-3H
	mov SCON, #50h	;SCON= 50H
	setb TR0	;TR0=1;
again:	mov a, #'H'	;Load letter 'H' in A
	acall trans	;Call transmit program
	mov a, #'I'	;Load letter 'I' in A
	acall trans	;Call transmit program
	mov a, #'T'	;Load letter 'T' in A
	acall trans	;Call transmit program
again:	sjmp again	;Short jump to again
trans:	mov SBUF, a	; Load SBUF with letter stored in A
here:	jnb TI, here	; if TI is not equal 1 jump to here
	ret	; Return
	end	; End

8	**Stepper motor interface.**
8a)	Write a C program to rotate stepper motor in clockwise/anticlockwise direction.

8a)	Write a C program to rotate stepper motor in clockwise/anticlockwise direction.

```
#include<reg51.h>

sbit sw=P1^6;

void delay(unsigned int );

void main()
{
sw=1;
while(1)
{
if(sw==0)
{
P2=0x66;
delay(22000);
P2=0x33;
delay(22000);
P2=0x99;
delay(22000);
P2=0xcc;
delay(22000);
}
else
{
P2=0xcc;
delay(22000);
P2=0x99;
delay(22000);
P2=0x33;
delay(22000);
P2=0x66;
delay(22000);
}
}
}

void delay(unsigned int itime)
{
unsigned int I, j;
for(i=0;i<=itime;i++);
for(j=0;j<=6000;j++);
}
```

9	**DC motor interface for direction and speed control using PWM.**
9a)Write a C program to show the on off control of DC motor.	

9a)	Write a C program to show the on off control of DC motor.

```
#include<reg51.h>

sbit ok=P2^4;

void turn_onoff(unsigned int);

void  main(void)
{
TCON=0;
TMOD=0x20;
while(1)
{
ok=0;
turn_onoff(50000);
ok=1;
turn_onoff(50000);
}
}



void turn_onoff(unsigned int)
{
unsigned int i;
for(i=0;i<itime;i++)
{
TL0=0x00;
TH0=0xff;
TR0=1;
while(!TF0);
{
TF0=0;
TR0=0;
}
}
```

10	**Alphanumerical LCD panel interface.**
10a)	Write a C program to send letters to LCD using delays.

10a)	Write a C program to send letters to LCD using delays.

```
#include<reg51.h>

sbit rs=P2^4;
sbit rw=P2^5;
sbit en=P2^6;

void main()
{
void lcdcmd(unsigned char value);
void lcddata (unsigned char value);
void delay(unsigned int itime);

lcdcmd(0x38);
delay(250);
lcdcmd(0x0c);
delay(250);
lcdcmd(0x01);
delay(250);
lcdcmd(0x80);
delay(250);
lcddata('H');
delay(250);
lcddata('I');
delay(250);
lcddata('T');
delay(250);
lcddata('-');
delay(250);
lcddata('E');
delay(250);
lcddata('E');
delay(250);
}
```

```
void lcdcmd(unsigned char value)
{
P0=value;
rs=0;
rw=0;
en=1;
delay(1);
en=0;
}


void lcddata(unsigned char value)
{
P0=value;
rs=1;
rw=0;
en=1;
delay(1);
en=0;
}


void delay(unsigned int itime)
{
unsigned int i. j;
for(i=0;i<itime;i++)
for(j=0;j<1275;j++);
}
```

11	**Generate different waveforms: Sine, Square, Triangular, Ramp using DAC interface.**
11a)	Write a C program to generate Square wave using DAC interface to 8051.
11b)	Write a C program to generate Ramp wave using DAC interface to 8051.
11c)	Write a C program to generate triangular wave using DAC interface to 8051.
11d)	Write a C program to generate Sine wave using DAC interface to 8051.

11a)	Write a C program to generate Square wave using DAC interface to 8051.

```
#include<reg51.h>
void delay(unsigned int );

void main(void)
{
while(1)
{
P1=0x00;
delay(250);
P1=0xff;
delay(250);
}
}


void delay(unsigned itime)
{
unsigned int i;
for(i=0;i<=itime;i++);
}
```

11b)	Write a C program to generate Ramp wave using DAC interface to 8051.

```
#include<reg51.h>
void delay(unsigned int );

void main(void)
{
unsigned int I;
while(1)
{
for(i=0;i<255;i++)
{
P2=i;
delay(2);
}
}
}


void delay(unsigned itime)
{
unsigned int i;
for(i=0;i<=itime;i++);
}
```

11c)	Write a C program to generate triangular wave using DAC interface to 8051.

```
#include<reg51.h>
void delay(unsigned int );

void main(void)
{
unsigned int  i,j;
while(1)
{
for(i=0;i<255;i++)
{
P2=i;
delay(2);
}
for(j=255;j>0;j--)
{
P2=j;
delay(2);
}
}
}


void delay(unsigned itime)
{
unsigned int i;
for(i=0;i<=itime;i++);
}
```

11d)	Write a C program to generate Sine wave using DAC interface to 8051.

```
#include<reg51.h>

sfr  dacdata=0x90;

void main(void)
{
unsigned char sine_value[12]={128, 192,238,255,238,192,128,64,17,0,17,64};
unsigned int x;
while(1)
{
for(x=0;x<12;x++)
dacdata=sine_value[x];
}
}
```

12	**Elevator interface.**
12a)	Write a C program to show control and operation of elevator using 8051.

12a)	Write a C program to show control and operation of elevator using 8051.

```
#include<reg51.h>
void delay (unsigned int);
main()
{
unsigned char Flr[9]={0xff,0x00,0x03,0xff,0x06,0xff,0xff,0xff,0x09};
unsigned char Fclr[9]={0xff,0x0e0,0x0d3,0xff,0x0b6,0xff,0xff,0xff,0x79};
unsigned char ReqFlr, CurFlr=0x01, i,j;
P0=0x00;
P0=0x0f0;
while(1)
{
P1=0x0f;
ReqFlr=P1|0x0f0;
while(ReqFlr==0x0ff)
ReqFlr=P1|0x0f0;
ReqFlr=~ReqFlr;
if(CurFlr==ReqFlr)
{
P0=FClr[CurFlr];
continue;
}
else if (CurFlr>ReqFlr)
{
i=Flr[CurFlr]-Flr[ReqFlr];
j=Flr[CurFlr];
for(;i>0;i--)
{
P0=0x0f0|j;
j--;
delay(50000);
}
}
else
{
i=Flr[ReqFlr]-Flr[CurFlr];
j=Flr[CurFlr];
for(;i>0;i--)
{
```

```
P0=0x0f0|j;
j++;
delay(50000);
}
}
CurFlr=ReqFlr;
P0=FClr[CurFlr];
}
}
void delay(unsigned int x)
{
for(;x>0;x--);
}
```

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MENTORSHIP / ACADEMIC COUNSELLING

Department of : CS&E / E&CE / E&EE / ME

Ta. Hukkeri, Dist. Belagavi, Karnataka, INDIA
www.hsit.ac.in;Phone: 08333-278887; Fax: 08333-278886; Email: principal@hsit.ac.in

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MENTORSHIP / ACADEMIC COUNSELLING

Department of: _____

Photo

Name of the Student: _____

_____

Name of Parent/Guardian: _____

Year of Admission: _____

USN	2	H	N							

Permanant Address: _____

_____

Present Address: _____

_____

Phone: _____ Mobile: _____ Email: _____

Student Mobile : _____ Student mail: _____

Name of the Faculty Advisor / Mentor: _____

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Educational History:

Exam	Year of Passing	Percentage of marks	Institute Name & Place	CET Rank
S.S.L.C				
PUC/Diolom				

Achievements:

	Educational	Sports	Co-curricular/ Extra curricular
S.S.L.C			
PUC/Diploma			

Family Details:

Person	Name	Occupation	Mobile	Email
Father				
Mother				
Guardian				

No of Brothers:	**Elder:**	**Younger:**
No of Sisters:	**Elder:**	**Younger:**

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Physical / Health Statistics:

Height (cm):		Weight(Kg):		Blood Group:	
Allergic to:					
Suffering from any disease/ailment:					
Undergone any surgery:					
Addicted to:					
Identification Marks:					

Future Aims & Goals:

1. Interested in Competitive Exams: GATE / CAT/CMAT/GRE/TOEFL/IES/IAS / IPS / IFS / KAS / KES / Public Sector etc.
2. Interested in Higher Education: M.E./ M.Tech / MBA / MS
3. Interested in Entrepreneurship (Area / Line of activity):
4. Interested in acquiring training on additional skills (Name of Skill) :

Co-curricular Interests :

Technical Quiz / Technical Debate / Essay Writing / Industrial Visits / Industrial Training / Technical Paper Presentation / Project
Exhibitions etc:

Sports Interests :

Cricket / Football / Volleyball / Swimming / Table Tennis / Chess / Caron / Athletics / Yoga/Any other

Extra Curricular Interests / Hobbies :

Singing / Dancing / Acting / Mimicry / Light Vocal Music / Musical Instruments / Philately / Numismatics etc.

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SEMESTER WISE ACADEMIC PERFORMANCE AND OBSERVATION REPORT

Roll No.:	SEM :	Div:	Cycle :							II
Sub .Abbr.										Observations/Suggestions
Sub Code -										
IA - I										
IA - II										
IA - III										
Avg. IA										
Credit Letter										
Grade										
Grade Point										
Credit Point										
End Sem.Results	Semeter Credit					Total Credit				S.G.P.A

Achievements:

MENTOR HOD Principal

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BREAK DURING GRADUATION

Date	No of Backlogs	Ineligible to	REMARKS

MENTOR **HOD** **Principal**

RE - ADMISSION DETAILS

Date	No of Backlogs	Admitted to	REMARKS

MENTOR **HOD** **Principal**

STUDENTS COUNSELLING

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Date: 24-04-2018

NOTICE

The following students are hereby informed to attend the counseling meeting with parents to discuss about low performance in the 2nd IA as per the schedule given below at HOD cabin without fail.

IV Semester Date of Counseling: 24-04-2018 Time: 2:00 PM

Names ??

USN	Signature	USN	Signature
2HN15EE036		2HN16EE020	
2HN15EE050		2HN16EE032	
2HN15EE053		2HN16EE033	
2HN16EE001		2HN16EE035	
2HN16EE003		2HN16EE037	
2HN16EE004		2HN16EE043	
2HN16EE006		2HN16EE048	
2HN16EE011		2HN17EE400	
2HN16EE013		2HN17EE401	
2HN16EE016		2HN17EE402	
2HN16EE018		2HN17EE403	
2HN16EE019			

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Acdaemics	
Students counselling	
Even Sem(2017-18)	

ACADEMIC COUNSELING/GUIDANCE REPORT

Class: IV - Sem

R.N.	Name of the student	Explanation/Reason(s) for irregularities.	Action Initiated / Advice given	Signature of Student
	2HN16EE020	Not studied well	Solve the question papers.	
	2HN16EE032	I'm not under stood the subject	Study regularly.	
	2HN16EE035	—	—	—
	2HN16EE033	Not prepared well	Study well. Solve VTU question papers	
	2HN16EE037	Because of confusion	Prepare on daily basis. Do the exercises	
	2HN16EE043	Not Prepared well	Study regularly	
	2HN16EE048	Not prepared well	Solve VTU question papers	
	2HN17EE400	Not studied	Study well & Solve VTU QP.	
	2HN17EE401	confused in question paper	Solve VTU question papers	
	2HN17EE402	Not prepared well	Be Attentive in the class.	
	2HN17EE403	Not prepared well	Solve VTU QP. Study regularly.	
	~~2HN16EE007~~			—

Committee Members Remarks (if any): —

Name & Sign. of Committee Members: 1. B.R. Umarani 2. S.D. Hiremath 3. O.B. Heddurshetti

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Students counselling
Even Sem(2017-18)

ACADEMIC COUNSELING/GUIDANCE REPORT

Class: IV-Sem

S.N.	Name of the student	Explanation/Reason(s) for irregularities.	Action Initiated / Advice given	Signature of Student
	2HN15EE036	Not prepared well	Refer V.T.U Q.P & Q. Bank	
	2HN15EE050	Not Studied	Study Regularly	
	2HN15EE053	Not prepared well	Study regularly	
	2HN16EE001	Not studied well.	Solve V.T.U Q.P.	
	003	Not studied well.	Solve VTU question papers & Study regularly	
	004	Not studied well	Solve V.T.U Q.P.	
	005	Not studied well.	Study regularly and solve V.T.U Q.P.	
	011	Health problem	Be regular to the Class	
	013	Family function.	Attend the classes regularly	
	016	got Confused	Study regularly to avoid Confusion	
	018	Not studied well	Refer Q.P and solve	
	019	Didn't prepare well	Do hard work.	

Committee Members Remarks (if any): —

Name & Sign. of Committee Members: 1. B.R. Umarani 2. S.D. Hiremath 3. O.B. Heddurshetti

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Date: 24-04-2018

NOTICE

The following students are hereby informed to attend the counseling meeting with parents to discuss about low performance in the 2nd IA as per the schedule given below at HOD cabin without fail.

VI Semester Date of Counseling: 24-04-2018 Time: 2:00 PM

USN	Signature	USN	Signature
2HN15EE008			
2HN15EE019			
2HN15EE032			
2HN15EE043			
2HN15EE421			
2HN16EE402			

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ACADEMIC COUNSELING/GUIDANCE REPORT

Class: VI-Sem

R.N.	Name of the student	Explanation/Reason(s) for irregularities.	Action Initiated / Advice given	Signature of Student
01	Bhushan Mandekar.	I was not feeling well	Keep mentain your health & Study well	
02	Laxmi. Karoshi	Not well	"	
03	Pooja Kharai	Some health issues	"	

Committee Members Remarks (if any): —

Name & Sign. of Committee Members: 1. 2. 3.

B.R. Umarani O.B. Heddarshetti

HOD

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Electrical & Electronic Engg

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STUDENTS FEEDBACK

CONSOLIDATED FEEDBACK-1 REPORT ODD SEM. (2017-18)

Name	Designation	Subject	Sem.	Class Strength	Respondent	A	B	C	D	%	Average
A U Neshti	Asst. Prof.	RES(15EE563)	5	66	16	119	34	6	1	93.8	90.7
A U Neshti	Asst. Prof.	T&G (15EE33)	3	52	45	295	79	28	48	87.6	90.7
B R Umarane	Assoc. Prof	DSD (15EE35)	3	52	46	372	75	6	7	96.3	96.3
H R Zinage	Asst. Prof.	E&EM (15EE36)	3	52	46	393	52	4	11	95.9	97.5
H R Zinage	Asst. Prof.	CTPS (10EE71)	7	61	45	431	18	1	0	99.1	97.5
K B Negalur	Asst. Prof.	IDA (10EE74)	7	61	45	402	21	23	4	96.4	96.4
M A Mulla	Asst. Prof.	SNS (15EE54)	5	66	41	383	21	6	0	98.3	97.8
M A Mulla	Asst. Prof.	AEC (15EE34)	3	52	46	406	50	1	3	97.3	97.8
M P Yanagimath	Asst. Prof.	MC (15EE52)	5	66	45	385	64	1	0	97.0	97.0
O B Heddurshetti	Asst. Prof.	PE (15EE53)	5	66	45	359	64	18	9	94.3	96.3
O B Heddurshetti	Asst. Prof.	EPU (10EE72)	7	61	45	428	15	2	5	98.4	96.3
P M Mura Ri	Asst. Prof.	ECA (15EE32)	3	52	45	440	6	0	4	99.2	98.2
P M Murari	Asst. Prof.	ME (15EE51)	5	66	45	391	55	4	0	97.2	98.2
R H Nayak	Asst. Prof.	VLSI (10EE764)	7	61	38	292	44	33	11	92.4	92.4
S D Hirekodi	Assoc. Prof	HV (10EE73)	7	61	44	420	18	2	0	99.0	99.2
S D Hirekodi	Assoc. Prof	TC (10EE752)	7	61	30	294	4	2	0	99.4	99.2
S G Huddar	Asst. Prof.	E&C (15EE553)	5	66	44	345	72	17	6	94.3	94.3
S S Birade	Asst. Prof.	PLC (15EE562)	5	66	34	273	53	12	2	95.1	95.1
S S Birade	Asst. Prof.	PLC (10EE756)	7	61	22	190	11	16	3	95.2	95.1
S.I.Shivamoggimath	Asst. Prof.	M-3(15MAT31)	3	52	46	408	33	10	9	96.5	96.5

20/10/17
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E&E Engg Dept.	
Academic	
Feedback-1	
2017-18(Even-Sem)	

DEPARTMENT OF EEE
CONSOLIDATED FEEDBACK REPORT

NAME	DESIGNATION	SUBJECT	SEMESTER	CLASS STRENGTH	RESPONDENTED	A	B	C	D	PERCENTAGE	AVERAGE
A U NESHTI	ASST. PROF.	15EE64	6	47	41	300	69	27	14	91.9	91.9
B R UMARANE	ASSOC. PROF	15EE45	4	54	43	349	44	19	18	93.6	93.6
Dr. SUDHEENDRA H.	PROFESSOR	10EE836	8	57	30	282	9	6	3	98	98
H R ZINAGE	ASST. PROF.	10EE82	8	57	30	299	1	0	0	99.9	99.9
K B NEGALUR	ASST. PROF.	15EE61	6	47	41	344	54	9	3	96.0	96.0
M P YANAGIMATH	ASST. PROF.	15EE63	6	47	41	402	8	0	0	99.6	99.6
O B HEDDURSHETTI	ASST. PROF.	10EE81	8	57	30	293	7	0	0	99.5	98.7
O B HEDDURSHETTI	ASST. PROF.	15EE662	6	47	41	373	32	5	0	97.9	98.7
P M MURARI	ASST. PROF.	15EE62	6	47	40	338	44	17	1	95.9	94.7
P M MURARI	ASST. PROF.	15EE43	4	54	44	364	37	13	26	93.5	94.7
R H NAYAK	ASST. PROF.	15EE44	4	54	45	406	27	8	9	96.8	96.8
S B PATIL	ASST. PROF.	15EE65	6	47	41	304	72	24	10	92.6	92.6
S D HIREKODI	ASSOC. PROF	10EE842	8	57	16	157	3	0	0	99.6	99.6
S G HUDDAR	ASST. PROF.	10EE844	8	57	13	129	1	0	0	99.8	96.85
S G HUDDAR	ASST. PROF.	15EE42	4	54	44	356	51	16	17	93.9	96.85
S S BIRADE	ASST. PROF.	15EE46	4	54	44	354	52	20	14	93.9	93.9
S. L. PATIL	PROFESSOR	15MAT41	4	54	44	389	35	11	5	96.7	96.7

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NIDASOSHI-591236.

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E&E Engg Dept.
Academic
Feedback-2
2017-18 (Even-Sem)

DEPARTMENT OF EEE

CONSOLIDATED FEEDBACK REPORT

NAME	DESIGNATION	SUBJECT	SEMESTER	CLASS STRENGTH	RESPONDENTS	A	B	C	D	PERCENTAGE	AVERAGE
A U NESHTI	ASST. PROF.	15EE64	6	47	40	309	64	17	10	93.6	93.6
B R UMARANE	ASSOC. PROF	15EE45	4	54	49	446	30	9	5	97.4	97.4
Dr. SUDHEENDRA H.	PROFESSOR	10EE836	8	57	40	384	16	0	0	99.2	99.2
H R ZINAGE	ASST. PROF.	10EE82	8	57	41	383	3	12	12	96.9	96.9
K B NEGALUR	ASST. PROF.	15EE61	6	47	41	342	45	17	6	95.2	95.2
M P YANAGIMATH	ASST. PROF.	15EE63	6	47	41	391	15	3	1	98.8	98.8
O B HEDDURSHETTI	ASST. PROF.	10EE81	8	57	41	367	33	8	2	97.3	98
O B HEDDURSHETTI	ASST. PROF.	15EE662	6	47	41	389	18	2	1	98.7	98
P M MURARI	ASST. PROF.	15EE62	6	47	41	364	33	9	4	96.9	95.9
P M MURARI	ASST. PROF.	15EE43	4	54	50	421	45	21	13	94.9	95.9
R H NAYAK	ASST. PROF.	15EE44	4	54	49	448	24	5	13	97.0	97.0
S B PATIL	ASST. PROF.	15EE65	6	47	40	347	39	11	3	96.5	96.5
S D HIREKODI	ASSOC. PROF	10EE842	8	57	29	290	0	0	0	100	100
S G HUDDAR	ASST. PROF.	10EE844	8	57	13	103	15	2	10	92.4	94.7
S G HUDDAR	ASST. PROF.	15EE42	4	54	50	450	33	9	8	97	94.7
S S BIRADE	ASST. PROF.	15EE46	4	54	49	414	38	28	10	94.9	94.9
S. L. PATIL	PROFESSOR	15MAT41	4	54	49	437	34	10	9	96.6	96.6

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