THIRD INTERNAL ASSESSMENT

Sem: VIII
Date: 19/05/2018

Sub: Multimedia Communication
Time: 3:00pm-4:00pm

Sub. Code: 10EC841
Max. Marks: 25

Note: Answer two full questions, draw sketches wherever necessary.

Q. No		Discription of Question	Marks	CO	RBT Level
1	a	Explain the ATM cell formats.	6	C412A.5	L1,L2
		OR			
	b	Explain the general structure of ATM switch.	6	C412A.5	L1,L2
	c	Explain LAN emulation in ATM.	6	C412A.5	L1,L2
		OR			
	d	Explain the unicast and multicast protocol architecture with reference to ATM.	6	C412A.5	L1,L2
2	a	Explain TCP/IP protocol suite.	6	C412A.6	L1,L2
		OR			
	b	Explain classical IP over ATM LAN(IPOA).	6	C412A.6	L1,L2
	c	Explain RTP and RTCP.	7	C412A.6	L1,L2
		OR			
	d	Explain the real time transport protocol and real time transport control protocolusage and package format.	7	C412A.6	L1,L2

Course Coordinator

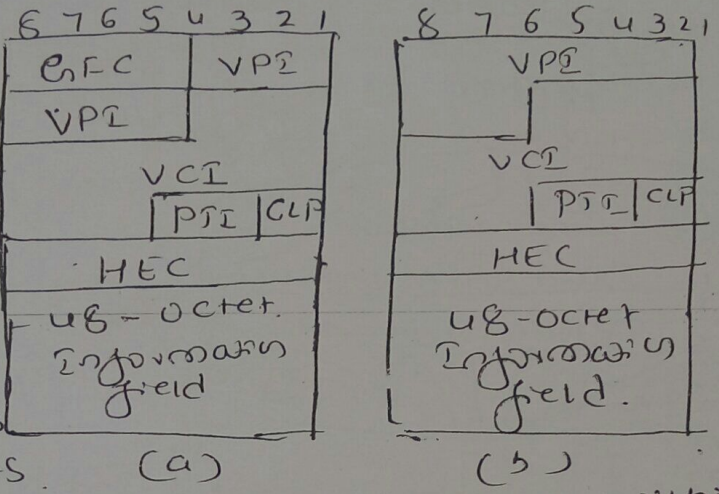
Module Coordinator

HOD

II IA **SCHEME OF EVALUATION**

Sem : VIII	Subject : MMC	Sub Code : 10EC81	Date : 19/05/2018

Q. No.	Bit	Description	Marks	CO's	RBT LEVEL
1)	a)	ATM Cell formats			

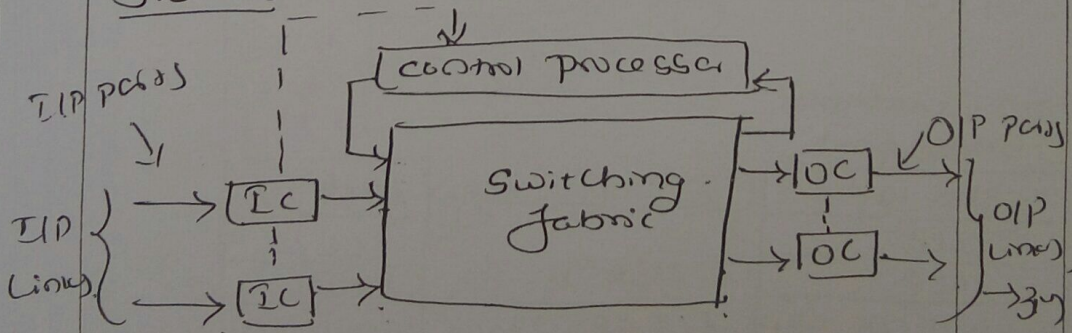


→ 3M. CO1 2A.1 L1, L2

(a) User-Network Segment (b) within Network, NNI to NNI Interface.

Each of cell format Explanation → 3M.

1) b) General Architecture of ATM Switch :



Explanation of above block diagram → 3M. CO1 2A.5 L1, L2

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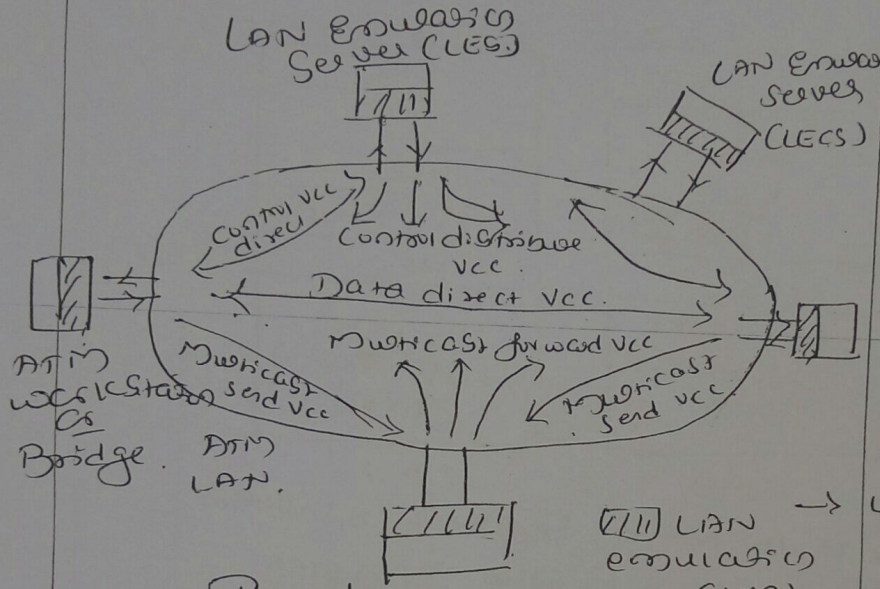
SCHEME OF EVALUATION

Sem : 8th | Subject : MMC | Sub Code : 10EC84 | Date : 19/5/2018

Q. No.	Bit	Description	Marks	CO's	RBT LEVEL
1)	c)	LAN emulation in ATM.			



→ 4M

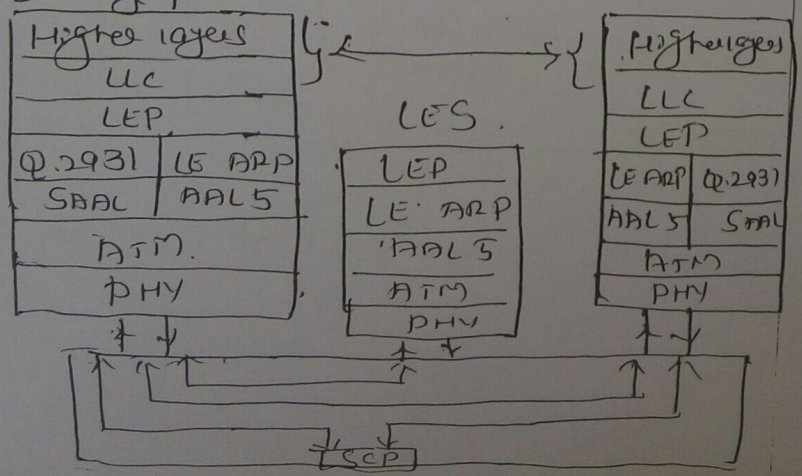
CO1 | PO1, PO2, PO5 | L1, L2

Broadcast & unknown address Server (BUS)

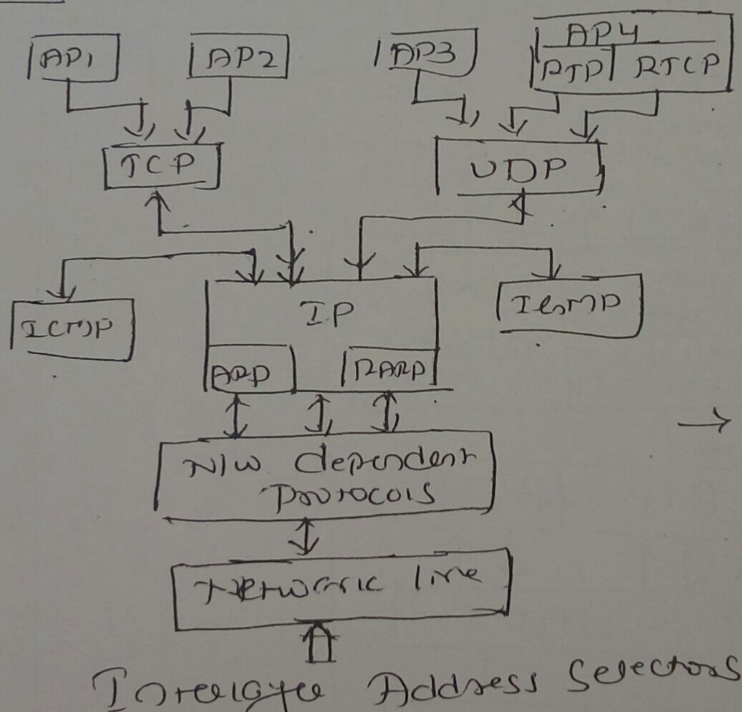
Fig: LAN Emulation : (a) Terminology and Networking Components

Explanation of above block diagram → 2M

1)	d)	ATM workstation or bridge port			

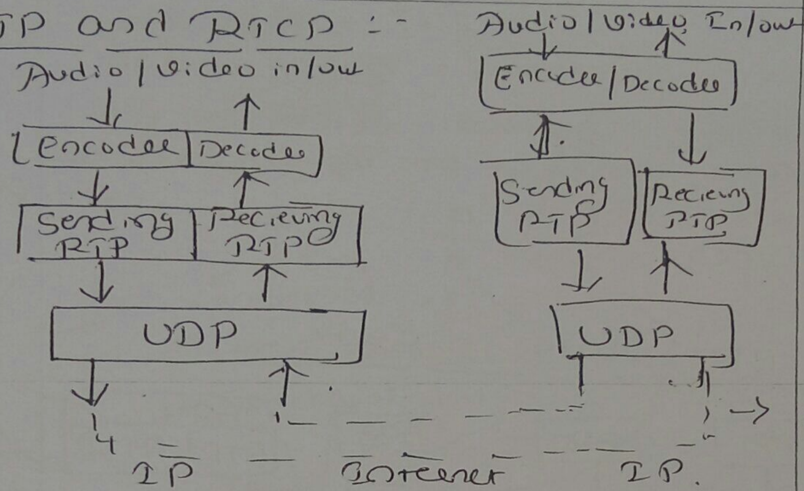


SCHEME OF EVALUATION

Sem : 8	Subject : MMC	Sub Code : 10EC841	Date : 19/5/2018		
Q. No.	**Bit**	**Description**	**Marks**	**CO's**	**RBT LEVEL**
1)	d)	The block diagram shows the unicast protocol architecture. Multicast protocol architecture is same change in ATM workstation. But, we need to use multicast send VCCs.	→ 8M	C412A5	L1, L2
2)	a)	TCP/IP Protocol Suite:  Explanation of above block → 2M	→ 4M	C412A6	L1, L2.
2)	b)	Classical IP over ATM LAN. The block diagram will be same multicast protocol architecture → 4M. Explanation of block → 2M		C412A6	L1, L2

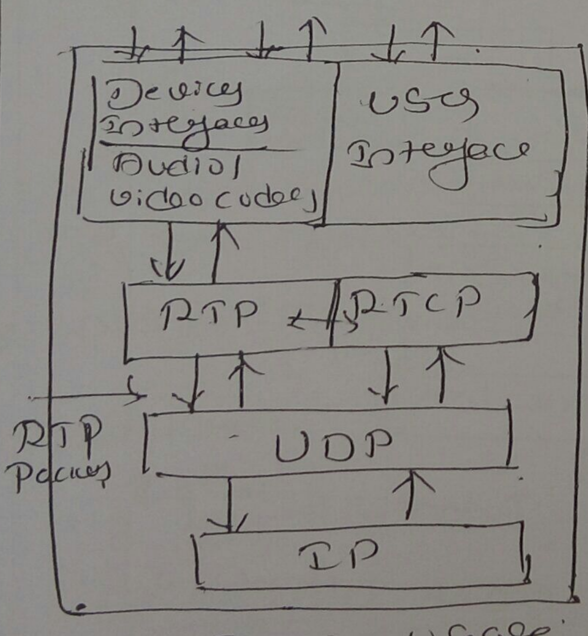
SCHEME OF EVALUATION

Sem : 8th	Subject : MMC	Sub Code : 10EC841	Date : 19/5/18		
Q. No.	**Bit**	**Description**	**Marks**	**CO's**	**RBT LEVEL**
2)	c)	RTP and RTCP :-			



→ 2M

Fig: Real time transport protocol



→ 2M

C408.6 L1, L2

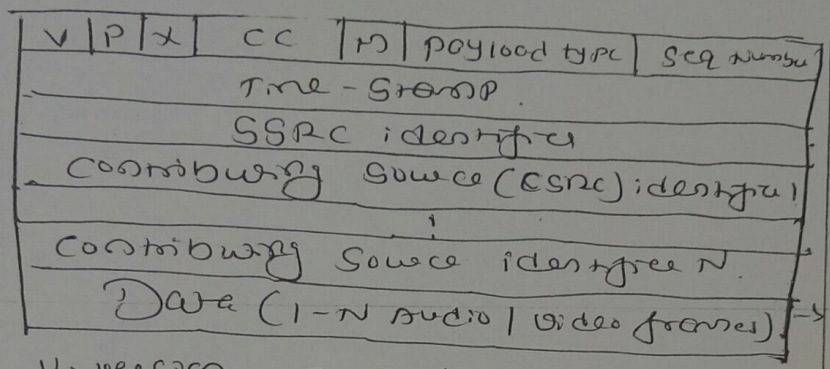
Fig: RTCP usage

Explanation of above block diagram → 3M

SCHEME OF EVALUATION

Sem : 8 | Subject : MMC | Sub Code : 10EC841 | Date : 19/5/18

Q. No.	Bit	Description	Marks	CO's	RBT LEVEL
2)	d)	RTP Packet format :-			



4M | CO1,2,3 | L1, L2

V - version, P = Pad, X = extension flag
CC = CSRC count ($N \leq 15$) M = marker bit.
Explanation of above format is 3M