



SECOND INTERNAL ASSESSMENT

Sem: III
 Date: 16/10/2018

Sub: Analog Electronics
 Time: 11am -12 noon

Sub. Code:17EC33
 Max. Marks:30

Note: Answer two full questions, draw sketches wherever necessary.

| Q. No | Discription of Question | Ma rks | CO | RBT Level |
|-----------|---|--------|--------|-----------|
| 1 | a Explain transistor series voltage regulator with neat sketches. | 7 | C203.4 | L2 |
| | b For a harmonic distortion reading of $D_2 = 0.1$, $D_3 = 0.02$ and $D_4 = 0.01$, with $I_1 = 4A$ and $R_c = 8\Omega$ calculate the total harmonic distortion, fundamental power component and total power | 8 | C203.4 | L3 |
| OR | | | | |
| 2 | a Explain Class D power amplifier with neat block diagram and waveforms | 7 | C203.4 | L2 |
| | b Determine the regulated voltage and circuit currents for the shunt regulator shown in figure 2(b) | 8 | C203.4 | L3 |
| | | | | |
| 3 | a Derive an expression for Z_0, Z_i and A_v for CE fixed bias configuration using re model | 7 | C203.1 | L2 |
| | b For the emitter-follower network of Fig.3(b) determine: i) r_e ii) Z_i iii) Z_o iv) A_v . | 8 | C203.1 | L3 |
| | | | | |
| OR | | | | |
| 4 | a Define h parameters and draw the h- parameter model equivalent circuits for CE and CB transistor | 8 | C203.1 | L2 |
| | b Given $I_E = 2.5 mA$, $h_{fe} = 140$, $h_{oe} = 20 mS$ (milli mho), and $h_{ob} = 0.5 mS$, determine: i) The CE hybrid equivalent circuit. ii) The CB re model. | 7 | C203.1 | L3 |

Prof. S B Akkole
 Course Coordinator

Prof. S B Akkole
 Module Coordinator

Dr. V G. Kasabegoudar
 HOD



IA -2 SCHEME OF EVALUATION

| Sem : | Bit | Subject : | Sub Code : | Date : | Marks | CO's | RBT LEVEL |
|-------|-----|--|------------|------------|--------|------|-----------|
| III | | Analog Electronics | 17EC33 | 16/10/2018 | | | |
| 1 | a | <p>Drawing series voltage regulator ckt - (3M)</p> <p>Explanation : $V_o = V_z - V_{BE}$</p> <p>If V_p voltage decreases V_{BE} increases $\therefore V_o$ increases converse is also true</p> <p>ckt diagram</p> | | 7 | C203-S | L2 | |
| | (b) | <p>$THD = \sqrt{D_2^2 + D_3^2 + D_4^2} = \sqrt{0.1^2 + 0.02^2 + 0.01^2}$</p> <p>$= 0.1$</p> <p>Fundamental power $P_1 = I_1^2 R_c = \frac{4^2 \times 8}{2} = 64W$</p> <p>Total power $P = (1 + THD^2) \cdot P_1 = (1 + 0.1^2) 64$</p> <p>$= 66.64W$</p> | | 8 | C203-S | L3 | |
| 2 | (a) | <p>Block diagram - 3M</p> <p>waveform - 2M</p> <p>Explanation - 3M</p> <p>Block diagram</p> <ul style="list-style-type: none"> - i/p waveform converted to pulse type at comparator - pulses are amplified & fed to LPF to get original signal - more than 90% | | 7 | C203-S | L2 | |

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IA - SCHEME OF EVALUATION

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|--------|-----------|--|------------|-------|--------|-----------|
| II | A E | 17EC33 | 16/10/2018 | | | |
| Q. No. | Bit | Description | | | | |
| 2 | (b) | $V_L = 8.2 + 0.7 = 8.9V$ — (2M) $I_L = \frac{V_L}{R_L} = \frac{8.9}{100} = 89mA$ — (2M) $I_S = \frac{V_i - V_L}{R_s} = \frac{22 - 8.9}{120} = 109mA$ — (2M) $\therefore I_C = I_S - I_L = 109 - 89 = 20mA$ — (2M) | | 8M. | C203-5 | L3 |
| 3 | (a) | re model ckt diagram 2M. + 1M for fixed bias $Z_i = \beta r_e \parallel R_B$ — (1) $Z_o = R_C \parallel r_o$ — (1) $A_v = -\frac{R_C \parallel r_o}{r_e}$ — (2) | | 7 | C203-1 | L2 |
| | (b) | $r_e = \frac{26 \times 10^{-3}}{I_E} = \frac{26 \times 10^{-3}}{2.062 \times 10^{-3}} = 12.61 \Omega$ — (1) $I_B = \frac{20.42mA}{\beta} = \frac{20.42mA}{100} = 0.2042mA$ — (1) $I_B = \frac{V_{CC} - V_{BE}}{R_B + (\beta + 1)R_E} = \frac{20 - 0.7}{220 \times 10^3 + (100 + 1) \times 3.3 \times 10^3} = 20.42 \mu A$ — (1) $R_B = \beta r_e + (\beta + 1)R_E = (100)(12.61) + 101 \times 3.3 \times 10^3 = 334.56k\Omega$ — (1) $\therefore Z_i = 26 \parallel R_B = \frac{26 \times 334.56 \times 10^3}{334.56 \times 10^3 + 26} = 132.72 \Omega$ — (1) $Z_o = R_C \parallel r_e = 3.3k\Omega \parallel 12.61 \Omega = 12.56 \Omega$ — (2) $A_v = \frac{v_o}{v_i} = \frac{R_E}{R_E + r_e} = \frac{3.3k\Omega}{3.3k\Omega + 12.61} \approx 1$ — (2) | | 8 | C203-1 | L3 |

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|--------|-----------|--|--------|--------|
| IV | AE | 17EC33 | 16/10 | |
| Q. No. | Bit | Description | Marks | CO's |
| H | (a) | $h_{11} = \frac{V_i}{I_i} \Big _{V_o=0} ; h_{12} = \frac{V_i}{V_o} \Big _{I_i=0}$ $h_{21} = \frac{I_o}{I_i} \Big _{V_o=0} ; h_{22} = \frac{I_o}{V_o} \Big _{I_i=0}$ <p>Defining above h-parameters (2m)</p> <p>Drawing with figs 3 + 3m for CE & CB Model</p> <p>Circuit diagrams for CE and CB models. The CE model shows a base terminal (B) with input current I_i and voltage V_i, a collector terminal (C) with output current I_o and voltage V_o, and an emitter terminal (E) with output current I_e and voltage V_{ce}. Parameters h_{ie}, h_{re}, h_{fe}, and h_{oe} are indicated. The CB model shows an emitter terminal (E) with input current I_e and voltage V_{eb}, a collector terminal (C) with output current I_c and voltage V_{cb}, and a base terminal (B) with output current I_b and voltage V_{cb}. Parameters h_{ib}, h_{fb}, and h_{cb} are indicated.</p> | 8 | C203-1 |
| (b) | (i) | $I_E = 2.5 \text{ mA} \therefore r_e = \frac{26 \times 10^{-3}}{2.5 \text{ mA}} = 10.4 \Omega$ $h_{ie} = \beta r_e = (100)(10.4) = 1.04 \text{ k}\Omega$ $r_o = \frac{1}{h_{oe}} = \frac{1}{20 \mu\text{S}} = 50 \text{ k}\Omega$ | 7 | C203-1 |

Staff in-Charge

Module Coordinator

Nidasoshi-591 236, Taq: Hukkeri, Dist: Belagavi, Karnataka, India.
 Phone: +91-8333-278887, Fax: 278886, Web: www.hsit.ac.in, E-mail: principal@hsit.ac.in

Nidasoshi-591 236 T...



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