

S J P N Trust's Hirasugar Institute of Technology, Nidasoshi

**FACULTY PROFILE**

- 1. **Name in full** : Mahesh P Yanagimath
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- 4. **Date of Birth** : 20/07/1982
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DT: Belgaum



**6. Educational qualifications :**

Sl. No	Degree	University/Institution	Year	Specialization
1	M.Tech.	KLECET Belgaum	2010	VLSI and Embedded Systems
2	B.E.	HIT Nidasoshi	2004	Electrical and Electronics Engineering

**7. Details of previous employment:**

Sl. No	Name of the Institution/Organization	Period		Designation
		From	To	
1	Hindalco Industries Private Limited, Belgaum (Aditya Birla Group)	Dec-2004	Dec-2005	Trainee Engineer
2	Hirasugar Institute of Technology, Nidasoshi	16-02-2006	31-01-2010	Lecturer
3	Hirasugar Institute of Technology, Nidasoshi	01-02-2010	31-07-2015	Senior Lecturer
4	Hirasugar Institute of Technology, Nidasoshi	01-08-2015	Till Date	Asst.Professor
Total Experience in years				12 years

**8. Date of Service in this Institution:** 16-02-2006

**9. Total Experience in years:** 12 years

**10. Subjects of Interest:**

- **Signals And Systems**
- **Digital Signal Processing**
- **VLSI Circuits and Design**
- **Microcontroller's**
- **Embedded Systems**
- **Logic Design**

## 11. Publications in National and International Journals & Conferences:

### International Journals

- Paper entitled “**Power system automation using Ethernet**” in Journal of advances in science and Technology, ISSN:2230-9659, Volume-12, issue-25(special issue), Dec 2016.
- Paper entitled “**VLSI implementation of high speed FFT processor**” in Journal of advances in science and Technology, ISSN:2230-9659, Volume-12, issue-25(special issue), Dec 2016.
- Paper entitled “**Ethernet based online monitoring system for power transformer in a smart grid**” published in International Journal of Technology and Science, ISSN (Online) 2350-1111, (Print) 2350-1103 Volume IX, Issue 1, 2016 pp. 27-30.
- Paper Entitled “**Monitoring and control strategies of smart grid system**” published in **Multidisciplinary Journal of Research in Engineering and Technology, Peer Reviewed International Journal (ISSN: 2348-6953)**.
- Paper Entitled “**Universal Serial bus based Human Interface device for Handheld devices**” published in Elsevier- Science and Technology Publications , Jan-2015.
- Paper Entitled “**Ethernet Controller as solution for Embedded Applications, New Developments and Innovations**” Published in **IOSR Journal of Electronics and Communication Engineering (ISSN: 2278-2834, ISBN 2278-8735, PP 39-43)**.

### National/International Conferences

- Presented paper entitled “**Power system automation using Ethernet**” in International conference on Recent innovations in engineering and science on 8<sup>th</sup> and 9<sup>th</sup> Oct 2016 at Hirasugar institute of technology, Nidasoshi.
- Presented paper entitled “**VLSI implementation of high speed FFT processor**” in International conference on Recent innovations in engineering and science on 8<sup>th</sup> and 9<sup>th</sup> Oct 2016 at Hirasugar institute of technology, Nidasoshi.
- Paper entitled “**VLSI implementation of high speed FFT processor**” in Journal of advances in science and Technology, ISSN:2230-9659, Volume-12, issue-25(special issue), Dec 2016.
- Paper entitled “**Ethernet based online monitoring system for power transformer in a smart grid**” in International conference on Smart Electronic systems[ICSES-2016] at KLECET Belagavi during May 20-22, 2016.
- Presented a paper entitled “**Monitoring and control strategies of smart grid system**” in **National Conference on “Trends in industrial power electronics and control”** at KLECET Belgaum during 29<sup>th</sup> to 30<sup>th</sup> Jan 2015.
- Presented a paper entitled “**Universal Serial bus based Human Interface device for Handheld devices**” in **National conference on “Advanced Technologies in Electrical and Electronic systems”** during 21<sup>st</sup> and 22<sup>nd</sup> March 2014.
- Presented and Published a Paper Entitled “**Ethernet Controller as solution for Embedded Applications, New Developments and Innovations**” in **2<sup>nd</sup> International conference on Emerging Trends in Engineering (ICETE-2013)** held on 22<sup>nd</sup> and 23<sup>rd</sup> Feb 2013 at Dr. J.J Magadum college of Engineering, Jayasingpur.
- Presented a paper entitled “**Microcontroller Based Embedded Ethernet Controller for Small memory**” in the “**National conference on Recent Advancements in Engineering (NCRAE12)**” held on 22<sup>nd</sup> and 23<sup>rd</sup> June 2012 at Sanjeevan Engineering and Technology Institute, Panhala.
- Presented and Published a Paper Entitled “**Internet Protocol Stack for Embedded system for small memory**” in the **National Conference on emerging trends in Engineering and Technology SANJIVANI VISION 2011** HELD AT Sanjivani rural education Society’s College of engineering, Kopargaon on 12<sup>th</sup> and 13<sup>th</sup> March 2011.

**12. Training Programmes/Workshops/Seminars/STTPs/Symposiums Attended:**

Sl. No.	Topic	Period	Date	Venue
1	Attended workshop on the topic <b>"Embedded System design using Microcontroller"</b>	07 Day's	21 <sup>st</sup> Sept 2007 to 27 <sup>th</sup> Sept 2007	KLECET Belgaum
2	Attended workshop on the topic <b>"Digital Design using Verilog"</b>	01 Day	29 <sup>th</sup> Feb 2008	KLECET Belgaum
3	Participated in Short term Training on <b>"Mechatronics"</b>	05 Days	23 <sup>rd</sup> March 2009 to 27 <sup>th</sup> March 2009	S J P N Trust's Polytechnic, Nidasoshi
4	National Workshop on <b>"Applications of Mathematics in Engineering"</b>	02 Days	30 <sup>th</sup> Aug 2010 to 31 <sup>st</sup> Aug 2010	HIT Nidasoshi
5	Attended Workshop on <b>"MSP430 Microcontroller"</b>	02 Days	16 <sup>th</sup> Apr 2012 to 17 <sup>th</sup> Apr 2012	KLECET Belgaum
6	National Workshop on <b>"Getting started with MATLAB"</b>	03 Days	12 <sup>th</sup> - July 2012 to 14 <sup>th</sup> July 2012	Manipal Institute of Technology, Manipal.
7	Attended Workshop on <b>"Signal and Systems"</b>	03 Days	4 <sup>th</sup> Oct 2012 to 6 <sup>th</sup> Oct 2012	HIT Nidasoshi
8	Attended Workshop on <b>"Research writing and publishing"</b>	02 Days	29 <sup>th</sup> Dec 2012 to 30 <sup>th</sup> Dec 2012	AITM Bhatkal
9	Participated in the workshop on <b>"Statistical Methods and Tools in Engineering Research"</b>	01 Day	31 <sup>st</sup> OCT 2013	SDM Dharwad
10	Undergone the Training the Trainer workshop on <b>"Intellectual Property Rights"</b>	01 Day	6 <sup>th</sup> Jan 2014	VTU Belgaum
11	Attended workshop on <b>"The power of FPGA and ASIC design with Mentor graphics"</b>	02 Days	10 <sup>th</sup> March 2014 to 11 <sup>th</sup> March 2014	GIT Belgaum
12	Faculty Development Program on <b>"Research Methodology"</b>	05 Days	1 <sup>st</sup> Oct 2014 To 5 <sup>th</sup> Oct 2014	KLECET Belgaum
13	Guest lecture on the topic <b>"Real world interfacing with microcontroller"</b> .	01 day	25 <sup>th</sup> March 2015	Sharad Institute of technology, college of engineering, Yadrav-Ichlkaranji-416121
14	Attended <b>International Symposium on Advances in Power Distribution Engineering: Distributed Generation, Micro grid and Renewable Integration.</b>	02 day	28 <sup>th</sup> August 2015 To 29 <sup>th</sup> August 2015	NIE, Mysore
15	Attended <b>International Symposium on "Smart Grid Technologies: Recent initiatives, Challenges and opportunities.</b>	02 day	27 <sup>th</sup> and 28 <sup>th</sup> Janaury 2017	NIE institution of technology, Mysuru.

**14. Details of Memberships in Professional Societies:**

- *IEEE Professional Member (MIEEE). IEEE Membership No: 93275167.*
- *Life Member of Indian society of technical education (LMISTE)*

Signature of the Faculty/Staff

Signature of the HOD