

S J P N Trust's Hirasugar Institute of Technology, Nidasoshi

FACULTY PROFILE

1. **Name in full :** Dr. Basavaraj V Madiggond



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3. **Email ID :** bvmadiggond.eee@hsit.ac.in

4. **Date of Birth :** 01/06/1970

5. **Address for communication:** Professor & Head
Department of EEE
HSIT, Nidasoshi,
Karnataka
Pin - 591 236

6. **Educational qualifications :**

Sl. No	Degree	University/Institution	Year	Specialization
1.	Ph.D.	JNTU Hyderabad	2018	Power Electronics
1	M.E.	GUG Kalaburgi	2000	Power Electronics
2	B.E.	KUD Dharwad	1994	Electrical & Electronics Engineering

7. **Details of previous employment:**

Sl. No	Name of the Institution/Organization	Period		Designation
		From	To	
1	Hirasugar Institute of Technology, Nidasoshi	19/06/18	Till date	Professor & HOD
2	Jain College of Engineering, Belagavi	16/07/2013	18/06/2018	Associate Professor & I/C HOD
3	SDM Institute of Technology, Ujire	13-07-2008	15/07/2013	Asst. Prof, Dean (Admin) & HOD
4	Maratha Mandal Engg College, Belagavi	July 2006	July 2008	Asst. Prof & HOD
5	KLE's College of Engg & Tech, Belagavi	October 2002	July 2006	Lecturer
6	Rural Engineering College, Hulakoti	February 2001	October 2002	Lecturer
7	STSKK's Polytechnic, Gadag	July 1997	February 2001	Principal & HOD
8	KLE's C B Kolli Polytechnic, Haveri	August 1993	July 1997	Lecturer
Total Experience in years				25 Years

8. **Date of Service in this Institution:** 19/07/2018

9. **Total Experience in years:** 25 Years

10. **Subjects of Interest:**

- Power Electronics
- Electrical Machines
- Industrial Drives
- Network Analysis
- Machine Design

11. **Publications in National and International Journals & Conferences:**

- “INTERNATIONAL CONFERENCES : 05

Optimization of filter elements in DC-DC closed loop Boost Converter

Authors : Vasant Upadhyaya, Basavaraj Madiggond

Venue: SDM Educational Society's SDM Institute of Technology, Ujire, South Canara Dist, Karnataka.

Held on 18th May 2018.

International Conference on Recent Trends in Technology 2K18 in collaboration with IJCA & IJERT

Design and Simulation of Multiphase Synchronous DC-DC Converter

Authors : Basavaraj Madiggond, H.N. Nagaraja

Venue : KLE Society's Dr. M S Sheshagiri College of Engineering and Technology, Belagavi, Karnataka.

Held on May 20-22, 2016.

International Conference on Smart Electronic Systems ICSES 2016

ISSN 2350-1111

Pg No : 31 -34

Behavioral Analysis of Three stage Interleaved Synchronous DC-DC Converter for VRM Applications

Authors : Basavaraj Madiggond, H.N. Nagaraja

Venue : Jain College of Engineering, Belagavi 14, Karnataka, India.

Held on 25th April 2015

International Conference on Emerging Trends in Engineering.-2015

ISBN : 978-93-84893-28-6

Paper No-15, Pg No: 81

Design Parameters for Synchronous Buck Converter

Authors : Basavaraj Madiggond, H.N. Nagaraja

Venue : Chitkara University Chandigarh

Held during 26 October 2012

Power Electronics And Energy Systems (PEES-12)

ISBN : 978-81-920249-8-1

Pg No : 56 -61

A Novel Approach to Improve the Efficiency of Synchronous Buck Converter

Authors : Basavaraj Madiggond, H.N. Nagaraja

Venue : Shri Vaishnav Institute Of Technology And Science, Indore, Madhya Pradesh, India.

Held during : January 23 - January 25 2014

AICTE Sponsored

International Conference on Instrumentation, Communication, Electrical & Electronics

- **Technical Papers presented:**

INTERNATIONAL JOURNALS : 05

Design Optimization for Low Voltage DC-DC Converter with Coupled Inductor Topology

Journal Name : Asian Power Electronics Journal,

Vol. 9, No. 1, Dec. 2015

Page No : 20 – 27

Loss Analysis of Single Stage Synchronous Buck Converter

Journal Name : International Journal of Emerging Technology And Advanced Engineering

ISSN : 2250 - 2459

Vol No : 2

Issue No :4

Year / Month of Publication : April 2012

Page No : 403 – 405

Design Issues in Single Stage Synchronous Buck Converter

Journal Name : International Journal of Emerging Technology And Advanced Engineering

ISSN : 2250 - 2459

Vol No : 2

Issue No :4

Year / Month of Publication : April 2012

Page No : 452 – 456

Design Issues with a Synchronous Buck Converter for VRM

Journal Name : International Journal of Advances in Electrical And Electronics Engineering

Vol No : 1

Issue No : 2

ISSN : 2319 -1112

Year / Month of Publication : December 2012

Page No : 262 – 270

Behavioral Analysis of Three stage Interleaved Synchronous DC-DC Converter for VRM Applications.

Journal Name : International Journal of Emerging Technology in Computer Science and Electronics.

ISSN : 0976-1353

Vol No : 14

Issue No :2

12. Books Published:

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13. Workshop / Seminars Conducted:

- VTU and ISTE sponsored one day workshop on "EFFECTIVE CONDUCTION OF BASIC ELECTRICAL LABORATORY" on 3rd August 2018.
- Organized 3 days Hands on Workshop on MATLAB Simulink during 11 -13 Aug 2016.
- Coordinator in a Faculty Development Programme on " **DSP & its Applications**", held on 26th to 28th July 2010 at SDMIT, Ujire, Karnataka.
- Coordinator in a National Level Workshop on " **Engineering Pedagogy**", held on 29th & 31th July 2010 at SDMIT, Ujire, Karnataka.
- Coordinator in a national Level Faculty development Programme on "**Embedded Systems & Energy Management**", held on March 2006 at KLE,s College of Engineering & Technology, Balgaum, Karnataka.

14. Training Programmes/Workshops/Seminars/STTPs/Symposiums Attended:

Sl. No.	Topic	Period		Venue
		From	TO	
1	National Workshop on "Modeling And Simulation Of Power Electronic Converters Using Simulink"	02-07-15	03-07-15	PSG College of Technology, Coimbatore
2	Inter University Accelerator Centre Acquaintance Programme on "Research Facility"	18-11-14	--	VTU Belagavi
3	One day workshop on "Electrical Design, Estimation & Costing"	21-04-14	--	JCE Belagavi
4	One day workshop on "Power System Planning"	10-10-13	--	JCE Belagavi
5	National Level Seminar on "Ethical Issues in Engineering System Design"	24-08-13	--	SDMCET Dharwad
6	Two Week ISTE Workshop on "Analog Electronics" by NMEICT (MHRD) by IIT Kharagpur	4-06-13	14-06-13	SDMIT Ujire
7	National Workshop on "Modeling and Control of Power Electronics Systems"	18-03-13	20-03-13	PESIT Bengaluru
8	National Level Workshop on "Renewable Sources and Control"	20-12-12	21-12-12	NITK Surtkal
9	Two day ISTE Workshop on "Akash for Education" by IIT Bombay.	10-11-12	11-11-12	SDMIT Ujire
10	Train the Trainers program on Industrial Automation by Robert-Bosch	02-07-12	28-07-12	VTU Regional office, Mysore
11	Five days Workshop on " High Impact Teaching Skills" by Mission 10X	14-06-10	18-06-10	NMAMIT Nitte
12	Two day Mission 10X Learning Approach Practitioners Certificate	13-09-10	14-09-10	NMAMIT Nitte

13	Two day workshop on "Fine Tuning of Draft Syllabus of BE/ M.Tech course"	16-04-10	17-04-10	PESIT Bengaluru
14	One day workshop on "VTU Thesis Development using Latex" by VTU e-learning Centre, Mysore	31-12-09	--	SJCE Mysore
15	Two day workshop on "Research Methodologies" by VTU e-learning Centre, Mysore	29-12-09	30-12-09	SJCE Mysore
16	Five day Staff Development Program on "Modern Controller Design Techniques"	09-06-08	13-06-08	MIT Manipal
17	One day workshop on "Digital Design using FPGA"	09-02-08	--	BVB CET Hubli
18	Short term course on " Intelligent Control of Electric Drives "	31-07-06	04-08-06	IIT Roorkee
19	Staff Development Program on "Professional Thinking"	22-10-05	--	KLECET Belagavi
20	Two day workshop on "CMOS VLSI & ASIC DESIGNS using Microwind tool"	31-08-07	01-09-07	BMS College of Engg Bengaluru
21	AICTE-ISTE Sponsored Short term training program on "Modeling Techniques in Electrical Sciences using MATLAB"	26-03-07	30-03-07	Dr AIT Bangalore
22	AICTE-ISTE Sponsored Short term training program on "Applications of Power Electronics to Power Systems"	06-12-04	10-12-04	Gayatri Vidyaparishad Coleege of Engg, Visakhpatnam
23	Two day National Levelworkshop on " Modern Trends in Power Systems"	11-09-04	12-09-04	GIT Belagavi

Awards/Achievements:

- **Consistently achieved more than 90% results in the subjects handled.**
- **Member, Board of Examiners of Visvesvaraya Technological University, Belagavi for the Academic year 2016-17.**
- **A final year Project "Implementation of Biogas using Solar Tracking" guided by me has secured consolation prize at National level Hackathon (Hardware Edition) held at NIT Trichy, during July 2018.**
- **Executive President of JCE Kannada Balagavi from 2013 to 2018.**
- **Delivered many invited technical & Motivational talks.**
- **Awarded with Best Technical Paper during KNOWLEDGE UTSAV, a National Symposium held at Jain Global Campus Bengaluru during August 28, 2010.**
- **Editor of College Magazine "DHITIKA" at SDM Institute of Technology and "SAMBHRAMA" at HSIT Nidasoshi.**
- **Worked as Rotaract President, Haveri during 1996.**

15. Details of Memberships in Professional Societies:

- LMISTE
- YHAI

16. Any other information

Signature of the Faculty/Staff

Signature of the HOD