

S J P N Trust's Hirasugar Institute of Technology, Nidasoshi

**FACULTY PROFILE**

1. **Name in full** Sunita.S.Malaj
2. **Contact Nos.** +91 - 9731795803
3. **Email ID** ssmalaj.ece@hsit.ac.in
4. **Date of Birth** 24-07-1973
5. **Address for communication** Asst Prof,  
Dept. of ECE,  
HIT, Nidasoshi,  
Karnataka  
Pin - 591 236



A/p:- Siddapurwadi  
Tq:- Chikodi  
Dt:- Belgaum  
Karnataka  
Pin - 591201

6. **Educational qualifications :**

Sl. No	Degree	University/Institution	Year	Specialization
1	M. E ( E & TC)	Shivaji University,Kolhapur	2013	E & TC
2	B.E.	KUD,Dharwad	1995	Electronics & Communication

7. **Details of previous employment:**

Sl. No	Name of the Institution/Organization	Period		Designation
		From	To	
1	S.J.P.N Trust's Polytechnic,Nidasoshi	01/03/1996	05/08/2008	Lecturer
2	Hirasugar Institute of Technology, Nidasoshi	01/08/2009	16/08/2012	Sr. Lecturer
3	K.L.E college of Engineering & Technology,chikodi	16/08/2012	27/11/2015	Asst Prof
4	Hirasugar Institute of Technology, Nidasoshi	01/02/2016	Till Now	Asst Prof
Total Experience				21 Years

8. **Date of Service in this Institution:01/02/2016**

9. **Total Experience in years:** 20 Years

**10. Subjects of Interest:**

- **Microwave & Radar Engineering**
- **Basic Electronics**
- **Real Time systems**
- **Real Time Operating systems**
- **Digital communication**
- **Microcontrollers**

**11. Publications in National and International Journals & Conferences:**

12. 1. Published research paper entitled “VHDL Implementation of Interval Arithmetic algorithms for Single Precision Floating Point Numbers”, in IJSER Journal Volume 4, Issue 3 Feb 2013 Edition.
2. Published research paper entitled “RFID & GSM Based Toll Tax System” Published In Jan 2017, Issue 1, Volume 7 Edition
3. Published Paper “Design & Implementation of low power CMOS Flash ADC in Cadence tool” in Journal of Advances in Science & Technology.

**2. Technical Papers presented:**

- Published research paper “Perspectives in Anisotropic Diffusion for Low Level Vision”, in “IEECSE National Conference Proceedings.
- Presented Paper “Design & Implementation of low power CMOS Flash ADC in Cadence tool” at ICRIE-16 , HIT, Nidasoshi.

**13. Books Published:**

**14. Workshop / Seminars Conducted:**

Sl.No	Topic	Period		Venue
		From	To	
1	Matlab Programming	15 Aug 2009	19 Aug 2009	D.P.P.C.E.T, Kolhapur
2	Akash for Education	10 Nov 2012	11 Nov 2012	KLECET, Chikodi
1	Analog Electronics	04 Jun 2013	14 Jun 2013	KLECET, Chikodi

2	Emerging Technological problems & Solutions in microelectronics embedded system & VLSI Design	22 July 2013	25 July 2013	KLE Engg College, Belgaum
3	Signals & Systems	2 Jan 2014	12 Jan 2014	KLE Engg College, Chikodi
4	Out Come Based Education	24 Jan 2014	25 Jan 2014	KLE Institute of Tech, Hubli
5	Computer Programming & Networking	May 20 to Jun 21 2014	05 July 2014	KLE Engg College, Chikodi
6	Image Processing & its Applications	23 Jan 2017	25 Jan 2017	SGBIT, Belgaum

**15. Awards/Achievements:**

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**16. Details of Memberships in Professional Societies:**

- LMISTE
- Institute of Engineers, India.

**17. Any other information**

Signature of the Faculty/Staff

Signature of the HOD