

S J P N Trust's Hirasugar Institute of Technology, Nidasoshi

**FACULTY PROFILE**

- 1. **Name in full** : Dattatray Mahadeo Kumbhar
- 2. **Contact Nos.** : 7353545488
- 3. **Email ID** : dmkhith@yahoo.in
- 4. **Date of Birth** : 10/07/1976
- 5. **Address for communication:** E & C Dept.  
HIT Nidasoshi.



6. **Educational qualifications :**

Sl. No	Degree	University/Institution	Year	Specialization
1	Ph.D	---	---	---
2	M.Tech.	VTU Belgaum	2015	Digital Electronics
3	B.E.	Shivaji University Kolhapur	2000	Electronics

7. **Details of previous employment:**

Sl. No	Name of the Institution/Organization	Period		Designation
		From	To	
1	Renutron Power Corporation , Kolhapur	01/06 2000	01/08/2007	Senior Engineer
Total Experience in years				7

8. **Date of Service in this Institution: 02/08/2007**

9. **Total Experience in years: 16.7**

10. **Subjects of Interest:**

<sup>35</sup>/<sub>17</sub> **Power Electronics**

<sup>35</sup>/<sub>17</sub> **Linear IC's**

11. **Publications in National and International Journals & Conferences:**

**National/International Journals**

35  
17

**“Boosting Input Voltage And Improving PF using PFC Circuit”** published in Volume 4, Issue 1 November 2013 by IJNTEC

**National/International Conferences**

35  
17

**“Maximum power point tracking using Fuzzy Logic controller”** published paper in Journal of advances in Science and Technology Vol. 12, Issue No. 25, Dec. 2026 ISSN 2230-9659.

**12. Technical Papers presented:**

35  
17

**“Maximum power point tracking using Fuzzy Logic controller”** presented paper in the international conference on recent innovations in Engineering(iCRiE-16) held at Hirasugar Institute of Technology, Nidasoshi.

**13. Books Published:**

35  
17

**14. Workshop / Seminars Conducted:**

35  
17

**15. Training Programmes/Workshops/Seminars/STTPs/Symposiums Attended:**

Sl. No.	Topic	Period	Date	Venue
1	Analog and Mixed Mode VLSI Design	2 Days	20,21 March 2009	VDRIT, Haliyal
2	CMOS VLSI & ASIC DESIGN using MICROWIND tool	2 Days	27,28 Aug 2009	SIT, Tumur
3	Application of Mathematics in Engineering	2 Days	30,31 Aug 2010	HIT, Nidasoshi
4	Signals And Systems	3 Days	4 to 6 Oct 2012	HIT, Nidasoshi
5	Overview of arm Microcontroller & Its Applications	1 Days	28 Sept. 2013	SDMCRT, Dharwad
6	Network Simulator 2 for wired Wireless Network	3 Days	17 to 19 Oct 2013	SIT Yadrav.
7	Internet Of Things	2 Days	30, 31 Dec. 2016	SGM COE Mahagaon.

**16. Awards/Achievements:**

35  
17

**17. Details of Memberships in Professional Societies:**

35  
17

ISTE—LM 62343

**18. Any other information**

Signature of the Faculty/Staff

Signature of the HOD