



CURRICULUM VITAE

Name:	Amit Uday Neshti	
Designation:	Assistant Professor	
Department:	Electrical and Electronics Engineering	
Specialization & Research Interests:	Digital Electronics	
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2. ACADEMIC QUALIFICATION (in reverse Chronological order):

Degree	Year	University/Board
M.Tech (Digital Electronics)	2015	VTU, Belgavi
B.E(Electrical and Electronics Engineering)	2008	VTU, Belgavi

3. ANY OTHER QUALIFICATION: Nil

4. PROFESSIONAL EXPERIENCE:

Organization/Institute/University	Position Held	Duration
Hirasugar Institute of Technology, Nidasoshi	Lecturer	02/02/2009 to 01/04/2016
Hirasugar Institute of Technology, Nidasoshi	Asst. Professor	01/04/2016 to till date

5. ADMINISTRATIVE ASSIGNMENTS:**NIL****6. COURSES TAUGHT:**

- I. Basic Electrical Engineering
- II. Transformer and generator
- III. Electrical circuit analysis
- IV. Logic design
- V. Digital system design
- VI. Power generation and economics
- VII. Electromagnetic field theory
- VIII. Transformer and induction machines
- IX. Direct current and synchronous machines
- X. Renewable energy sources
- XI. High voltage engineering
- XII. Electrical machine design
- XIII. Digital signal processing
- XIV. Industrial drives and application
- XV. Introduction to C programming

7. RESEARCH SUPERVISION:

- A. Ph.D: NIL
- B: M.Tech: NIL
- C. BE/B.Tech Degree Oriented (Bachelor Level Dissertation):
 - i. Awarded :14
 - ii. Submitted : Nil
 - iii. Ongoing :01

8. CONTRIBUTION TO CORPORATE LIFE OF THE INSTITUTE:

NIL

9. MEMBERSHIP OF SOCIETIES/PROFESSIONAL BODIES:

Life time member of "Indian Society for Technical Education

10. PUBLICATIONS:**A. BOOKS/MONOGRAPHS:**

NIL

B. PAPERS IN REFEREED/PEER REVIEWED JOURNALS:

- i) Publication in journal of advances in science and technology entitled "Power system automation using Ethernet".
- ii) Publication in Recent trends in engineering and technology entitled "Internet of Things Based Vehicle Accident Prevention and Detection System" ISBN-978-93-91535-97-1

C. PAPERS IN CONFERENCES PROCEEDINGS:

- i) Publication in journal of advances in science and technology entitled "Power system automation using Ethernet".
- ii) Published a paper entitled "Virtual Model for Automatic Synchronization of Alternator with Infinite Bus Bar Using MATLAB SIMULINK" in the Proceedings of 2nd **International** conference on Recent Innovations in Science, Engineering & Technology (ICRISET-2019) held on 18th & 19th July 2019 at Hirasugar Institute of Technology Nidasoshi, Belagavi .
- iii)) Published paper entitled " internet of Things Based Vehicle Accident Prevention and Detection System" in the Proceedings of conference Recent trends in engineering and technology held on at Hirasugar Institute of Technology Nidasoshi, Belagavi .

11. Patents/Copyrights/IPR (If Any)

NIL

12. INVITED TALKS:

NIL

13. RESEARCH PROJECTS (COMPLETED/ONGOING):

NIL

**14. PARTICIPATION & PRESENTATIONS IN SEMINARS /SYMPOSIA/
WORKSHOPS/ CONFERENCES:**

- I. Presented paper entitled “ internet of Things Based Vehicle Accident Prevention and Detection System” in the Proceedings of conference Recent trends in engineering and technology held on at Hirasugar Institute of Technology Nidasoshi, Belagavi .
- II. Attended AICTE Sponsored 2-weeks faculty development program on “Current practices and critical issues for OBE an Engineering Education/Institution” from 27th Jan to 7th Feb 2020 held at Basaveshwar Engineering College (A), Bagalkot.
- III. Attended one-week online Faculty Development Program on “Recent Trends in Power Systems” organized by Sharad Institute of Technology, College of Engineering, Yadrav (Ichalkaranji) from 22/06/2020 to 26/06/2020.
- IV. Participated in “Awareness Program on Intellectual Property Rights” organized by Rajgad Dnyanpeeth’s Shri Chhatrapati Shivajiraje College of Engineering on 15/05/2020.
- V. Attended webinar on “INDUSTRY 4.0” on Monday 22nd June 2020 organized KLE College of Engineering, Belagavi.
- VI. Attended webinar on “IPR: Patent Filling Procedure” on 10/06/2020 organised by Dept. of EEE SDM College of Engineering & Technology, Dharwad.
- VII. Attended webinar on ““Protective Switchgear-Impacts & Applications” conducted on 15 June 2020 by Department of Electrical & Electronics Engineering, Basaveshwar Engineering College(A), Bagalkot.
- VIII. Published a paper entitled “Virtual Model for Automatic Synchronization of Alternator with Infinite Bus Bar Using MATLAB SIMULINK” in the Proceedings of 2nd **International** conference on Recent Innovations in Science, Engineering & Technology (ICRISET-2019) held on 18th & 19th July 2019 at Hirasugar Institute of Technology Nidasoshi, Belagavi.
- IX. Publication in journal of advances in science and technology entitled “Power system automation using Ethernet”.
- X. Attended a one-week FDP on “Internet of Things and Its Real Time Applications” held at HSIT Nidasoshi, Belagavi during 28th Jan 2018 to 2nd Feb 2019.

- XI. Attended three days faculty development program on “Computer Aided Electrical Drawing” (Using AutoCAD) in collaboration with VTU, Belgaum and AUTODESK from 12th to 14th December 2013 at Hirasugar Institute of Technology, Nidasoshi.
- XII. Attended workshop on “Effective Conduction of Basic Electrical Engineering Laboratory (18ELEEL17)” 3 aug 2018 at HIT,Nidasoshi.
- XIII. Attended three days workshop on signals and system at HIT Nidasoshi
- XIV. Two days workshop on Applications of mathematics in Engineering at HIT Nidasoshi.

15. AWARDS, FELLOWSHIPS & OTHER DISTINCTIONS:

NIL

16. ANY OTHER SIGNIFICANT INFORMATION:

- Developed Electrical circuit analysis subject hardware laboratory.
- Developed Introduction to C programming subject laboratory.
- Developed manual for electrical machine using scilab code.
- Contribution in creating formula book for first year basic electrical engineering subject.
- Developed formula book for electrical circuit analysis subject.



(Amit U Neshti)