CURRICULUM VITAE

Name:	Prof. SACHIN S PATIL	
Designation:	Assistant Professor	
Department:	Electronics & Communication Engineering.	
Specialization & Research Interests:	VLSI Design & Embedded Systems	
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2. ACADEMIC QUALIFICATION :

Degree	Year	University/Board
M. Tech	2011	BVBCET, Huballi
B.E.	2001	HSIT Nidasoshi

3.   ANY OTHER QUALIFICATION:

NONE

4.   PROFESSIONAL EXPERIENCE:

Organization/Institute/University	Position Held	Duration
Hirasugar Institute of Technology, Nidasoshi	Assistant Professor	02.05.2005 to Till Now
Vista Infotech Pvt Ltd, Bangalore	Embedded Developer	23.12.2002 to 07.02.2005

5.   ADMINISTRATIVE ASSIGNMENTS:

Position Held	Duration	Nature of Work
Alumni Association HSIT President	2011 to 2014	Managing & coordinating Alma mater
Alumni Association HSIT Vice-President	2014 to Till Date	coordinating Alma mater
Department Mini-Project Coordinator	2017 to Till Date	Coordinating the Project work for ECE Department
Department NBA Criteria 7 Head	2015 to Till Date	Handling all the tasks of Criteria 7 of the NBA
Chairman LEAD Committee	2020 to Till Date	Coordinating the LEAD Activities for College
NSS Program Officer	2022 to Till Date	Organizing NSS activity

6.   COURSES TAUGHT:

- Microprocessor 8086
- Microprocessor 8085
- Microcontroller 8051
- Microcontroller ARM Cortex M3
- ARM Microcontroller and Embedded system
- Digital Electronics
- Logic Design
- Fundamentals of CMOS VLSI
- Fundamentals of HDL (VHDL & Verilog)
- Verilog HDL

- ➢ Embedded System Design
- ➢ Real Time Systems
- ➢ Analog Electronics Circuits
- ➢ Computer Communication Networks
- ➢ Network Security
- ➢ Basic Electronics

7. RESEARCH SUPERVISION:

A. Ph.D:
- **i. Awarded**   : None
- **ii. Submitted**   : None
- **iii. Ongoing**   : None

B: M.Tech:
- **i. Awarded**   : None
- **ii. Submitted**   : None
- **iii. Ongoing**   : None

C. BE/B.Tech Degree Oriented (Bachelor Level Dissertation):
- **i. Awarded**   18
- **ii. Submitted**   18
- **iii. Ongoing**   01

8. CONTRIBUTION TO CORPORATE LIFE OF THE INSTITUTE:

1. Founder of Alumni Association HIT Nidasoshi
2. Always in contact with alumni and get help to students from alumni.
3. Member of Admission committee. Referred many students to take the admission in our college as well as in the Sister institutions.
4. Member of Dept. Program assessment and quality improvement committee.

9. MEMBERSHIP OF SOCIETIES/PROFESSIONAL BODIES:

1. Life Member of Indian Society for Technical Education– LM 62342.
2. Ignited Minds Professional & Academic Research Consortium.

10. PUBLICATIONS:

A. BOOKS/MONOGRAPHS:
1. Authored:
    i. None

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2.   Edited:
    i.   None
B.   PAPERS IN REFEREED/PEER REVIEWED JOURNALS:
    i.   None

C.   PAPERS IN CONFERENCES PROCEEDINGS:

- ➢ **Mr. Sachin S Patil**, Smt. Sujata S Kamate, "Design of efficient 32-bit floating point multiplier using standard IEEE754", A Journal of Emerging Technologies and Innovative Research, Volume VII, Issue X, October 2020(ISSN:2349-5162).
- ➢ **Mr. Sachin S Patil**, "Innovative Hybrid Power Generation on Highways", A Journal of Composition Theory, Volume XII, Issue VI, June 2019(ISSN:0731-6755).
- ➢ Mr. N M Patel, **Mr. Sachin S Patil**, "Design of Pipelined RISC MIPS Processor Using VLSI Technology", International Journal of Engineering Research and Applications, Volume 8, Issue 9,September 2018(ISSN: 2248-9622).
- ➢ Miss. S B Jondhale, Miss. T S Mulla,**Mr. Sachin S Patil** "Design and Implementation of 8 bit Barrel Shifter Using 2:1 Multiplexer in Verilog"Journal of Advances in Science & Technology, Vol. 12, Issue No. 25, pp.December 2016 (ISSN 2230-9659).

11.   Patents/Copyrights/IPR (If Any)

None

12.   INVITED TALKS:

- ➢ Delivered lecture as resource person for seminar on "Graphs & Charts reading and reasoning", on 20.08.2019 held at HSIT Nidasoshi for Induction program of 1ˢᵗ year students.
- ➢ As Guest lecture delivered lecture on "Operating System and its usage in Real Time Systems" on 20ᵗʰ June 2019 held at BCA college Nidasoshi.
- ➢ As Guest lecture delivered lecture on "Digital Logic" on 28ᵗʰ Feb 2019 held at BCA college Nidasoshi.

13.   RESEARCH PROJECTS *(COMPLETED/ONGOING):*

None

14.   PARTICIPATION & PRESENTATIONS IN SEMINARS /SYMPOSIA/ WORKSHOPS/ CONFERENCES:

ORGANIZED:

➢ Organized Webinar on "Art of success for Civil Services" on 16th July, 2020 held at HSIT, Nidasoshi in association with ALS, Delhi .

➢ Organized Webinar on "Education Abroad" on 11th July, 2020 held at HSIT, Nidasoshi in association with Transcend Institute of Foreign Studies, Belagavi .

➢ Organized 4 Days workshop on "PCB Designing and Testing" from 19th to 22th Sept, 2019 held at HSIT, Nidasoshi in association with Electrosal Pvt Ltd Nippani.

➢ Organized 3 Days workshop on "PCB Designing and Testing" from 1st to 3rd March, 2019 held at HSIT, Nidasoshi in association with Electrosal Pvt Ltd Nippani.

➢ Organized Seninar on the topic "Opportunities in VLSI Domain" on 06-10-2018 by the resource person Mr. Rajshekhar Karikatti, Senior Design Engineer, Sankalp Semiconductor Pvt. Ltd., Hubballi held at HIT, Nidasoshi.

➢ Organized 3 Days workshop on "Signals & Systems" on 4, 5,6 October 2012, by the resource person Dr. Subbanna Bhat , Eminent Professor, BVBCET, Hubballi held at HIT, Nidasoshi.

PARTICIPATED:

Sl.No.	Topic	Period		Venue
		From	**To**	
1	3 Days National level Online FDP on "Advanced Research trends in Image Processing"	14.07.2020	16.07.2020	HSIT, Nidasoshi
2	Webinar on " Art of Success for Civil Services"	16.07.2020	16.07.2020	HSIT, Nidasoshi

3	Webinar on " Education Abroad"	11.07.2020	11.07.2020	HSIT, Nidasoshi
4	1 Day online workshop "Training on Publishing Research Articles".	25.06.2020	25.06.2020	SGBIT, Belgaum
5	Webinar on "Career after COVID-19"	21.06.2020	21.06.2020	HSIT, Nidasoshi
6	5 Days TEQIP-III sponsored National level FDP on "Machine Learning based project architecture design"	15.06.2020	19.06.2020	BEC, Bagalkot
7	Webinar on "Introduction to DEVOPS"	11.06.2020	11.06.2020	Sri. Eshwar College of Engineering, Coimbatore
8	2 Days online FDP on "Skill Enhancement for Academicians"	08.06.2020	09.06.2020	JCE, Belagavi
9	5 Days FDP on "Emerging research area in engineering" in association with the Institution of Engineers(India)	05.06.2020	09.06.2020	SRM Institute of Science and Technology Chennai, TN
10	4 Days workshop on "Design & Simulation of Microchip Antenna using HFSS tool" conducted by Niha Solutions, Bangaluru.	26.02.2020	29.02.2020	HSIT, Nidasoshi
11	4 Day faculty program on "Machine Learning and Data Analytics" sponsored by VGST, Govt of Karnataka.	08.01.2020	11.01.2020	HSIT, Nidasoshi
12	One day training program on "National Energy Conservation week"	20.12.2019	20.12.2019	HSIT, Nidasoshi
13	6 Days workshop on "Internet of Things and Its Real Time Applications" organized by E&ICT Academy, NIT Warangal	28.01.2019	02.02.2019	HSIT, Nidasoshi
14	1 Day workshop on "ARM Cortex M3	11.11.2017	11.11.2017	SGBIT, Belagavi

	Microcontrollers"			
15	2 Days workshop on "Internet of Things(IOT)"	30.12.2016	31.12.2016	SGMEC, Mahagoan
16	1 Days workshop on "Exploring Research Avenues & IPR"	13.08.2016	13.08.2016	SDMCET, Dharwad
17	3 Days workshop on "FDP on Research Methodology"	13.02.2015	15.02.2015	BVBCET, Hubli
18	1 Day workshop on "Overview of ARM Microcontroller & Its Application"	28.09.2013	28.09.2013	SDMCET, Dharwad
19	2 Days workshop on "Introduction to Robotics"	31.05.2010	01.06.2010	SDMCET, Dharwad
20	2 Days workshop on "Fundamentals of HDL"	14.02.2008	15.02.2008	VKIT, Bangalore
21	1 Days workshop on"ARM Embedded Technologies"	11.09.2006	11.09.2006	VTU, Belgaum
22	Participated in 2 Days TEQIP sponsored training program on "Network Protocol Engineering"	19.12.2005	20.12.2005	NITK, Suratkal
23	Participated in 5 days short-term course on "Embedded system design using Micro controllers" under TEQIP of AICTE, New Delhi	24.10.2005	28.10.2005	IISc. Bangalore

15. AWARDS, FELLOWSHIPS & OTHER DISTINCTIONS:

➢ Pre-Placement training on the subject "Digital Electronics & Applications" in view of CADENCE Pool campus drive on 10.10.2019 held at HSIT Nidasoshi.

➢ Pre-Placement training on the subject "CMOS Fabrication" in view of CADENCE Pool campus drive on 11.10.2019 held at HSIT Nidasoshi.

➢ Worked as Jury for PRAXIS 2K19, A National level Tech Fest at KLECET Chikkodi, conducted on 02.03.2019 for the event "Circuit Debugging and Technophilia".

16.   ANY OTHER SIGNIFICANT INFORMATION:

➢ Active role or participation in fund procurement from external bodies :

Name of the Project/Proposal/Event	Sanctioned/Requested Amount	Date of Sanction/Request
Innovative Hybrid Power Generation on Highways	7000=00 KSCST, Bangalore	2018-19
Solar operated Smart Recycle Bin	7000=00 KSCST, Bangalore	2017-18
Design & Development of dual controlled solar powered smart pesticide & fertilizer spraying robot	6500=00 KSCST, Bangalore	2016-17
FPGA based implementation of AES Encryption & Decryption used in secure communication	9000=00 KSCST, Bangalore	2014-15
Intelligent Vehicle tracking system	4000=00 KSCST, Bangalore	2006-07
Wireless farm management using Microcontroller	5000=00 KSCST, Bangalore	2006-07
Anti-car thieving	5000=00 KSCST, Bangalore	2005-06

➢ Participated in National Level Online Technical E-Quiz on "Electrical Machines" conducted by HSIT, Nidasoshi on 10th July 2020 and scored 100% marks.

- Attended National Level Online Quiz on "Basics of Electrical & Electronics Engineering" on 11-07-2020 conducted by KLECET, Belagavi.

- Participated in Online Quiz on "Transmission & Distribution of Electrical power" conducted by EEE Dept. SJMIT, Chitradurga on12thJuly 2020 and scored 100% marks.

- Participated in National Level Online "Aptitude Quiz on" conducted by department of EEE, PDIT, TB Dam, Hospete on16th June 2020 and secured 80% marks.

- Participated in "Fundamentals of Electrical Engineering-02" E-Quiz on 10thJune, 2020 with passing score 87% organized by KLEIT, Hubballi.

- Participated in Online Quiz on "Basics of Electronics & Communication Engg." organized by ECE Dept, BEC, Shravanabelagola on16th June 2020.

Prof. SACHIN S. PATIL