



CURRICULUM VITAE

Name:	Dattatray Mahadeo Kumbhar	
Designation:	Assistant Professor	
Department:	Electronics and Communication Engineering	
Specialization & Research Interests:	Digital Electronics, Power Electronics	
Email IDs (Official & Personal)	dmkumbar.ece@hsit.ac.in	
Mobile No.:	7353545488	
Address:	A/P- Padal, Tal- Panhala, Dist: Kolhapur, State: Maharashtra., India.	

2. ACADEMIC QUALIFICATION (in reverse Chronological order):

Degree	Year	University/Board
Ph.D.	--	--
M.Tech.	2015	VTU, Belagavi
B.E.	2000	Shivaji University, Kolhapur

3. ANY OTHER QUALIFICATION:

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4. PROFESSIONAL EXPERIENCE:

Organization/Institute/University	Position Held	Duration
Hirasugar Institute of Technology, Nidasoshi	Asst. Professor	14-09-2016 to till date
Hirasugar Institute of Technology, Nidasoshi	Sr. Lecturer	02-02-2010 to 13-09-2016
Hirasugar Institute of Technology, Nidasoshi	Lecturer	02-08-2007 to 01-02-2010

5. ADMINISTRATIVE ASSIGNMENTS:

Position Held	Duration	Nature of Work
ED Cell Convener	Oct-2021 – Till Now	--

6. COURSES TAUGHT:

- Basic electronics
- Analog electronics
- Logic Design
- Electronic Instrumentation
- Network theory
- Linear IC's & applications
- Information theory and coding
- Basics of CMOS
- Management & Entrepreneurship
- Digital system design using Verilog
- Satellite communication
- VLSI Design
- Power Electronics

- Embedded system
- Sensors & signal conditioning
- Communication Theory

7. RESEARCH SUPERVISION:

A. Ph.D.:

- i. Awarded : ---
- ii. Submitted : ---
- iii. Ongoing : ---

B: M.Tech.

- i. Awarded : ---
- ii. Submitted : ---
- iii. Ongoing : ---

C. BE/B.Tech Degree Oriented (Bachelor Level Dissertation):

- i. Awarded : 17
- ii. Submitted : --
- iii. Ongoing : 01

8. CONTRIBUTION TO CORPORATE LIFE OF THE INSTITUTE:

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9. MEMBERSHIP OF SOCIETIES/PROFESSIONAL BODIES:

- ISTE—LM 62343.

10. PUBLICATIONS:

A. BOOKS/MONOGRAPHS:

- 1. Authored: --
- 2. Edited: --

B. PAPERS IN REFEREED/PEER REVIEWED JOURNALS: --**C. PAPERS IN CONFERENCES PROCEEDINGS:**

1. “Boosting Input Voltage and Improving PF using PFC Circuit” published in Volume 4, Issue 1 November 2013 by IJNTEC.
2. “Maximum power point tracking using Fuzzy Logic controller” published paper in Journal of advances in Science and Technology Vol. 12, Issue No. 25, Dec. 2016 ISSN 2230-9659.
3. “Maximum power point tracking using Fuzzy Logic controller” presented paper in the international conference on recent innovations in engineering (iCRiE-16) held at Hirasugar Institute of Technology, Nidasoshi.
4. “Agriculture Robot” published paper in Journal of Emerging Technologies and Innovative Research (JETIR) Volume 5, Issue 9, September 2018, ISSN-2349-5162.
5. “Comparison of DC to DC Converter for PV System to Track MPP” published paper in Journal of Emerging Technologies and Innovative Research Volume 6 Issue 6, June 2019, ISSN-2349-5162.
6. “Smart Floor Cleaning Robot” published paper in Journal of Emerging Technologies and Innovative Research Volume 7 Issue 10, October 2020, ISSN-2349-5162.
7. “A smart solution for in farm complete tractor activity tracking system over cloud” published paper in Journal of Emerging Technologies and Innovative Research Volume 8 Issue 10, October 2021, ISSN-2349-5162.
8. “IOT Based Virtual Doctor Robot for Non-Contact Treatment of Patients & Remote Medication” published paper in Journal of Emerging Technologies and Innovative Research Volume 9 Issue 11, November 2022, ISSN-2349-5162.
9. “An Artificial Intelligence Based User-Friendly Two-Way Sign Language Translator for Deaf and Dumb People of India” published paper in International Journal of Advanced Technology in Engineering and Science Volume 11 Issue 12, December 2023, ISSN-2348-7550.

11. Patents/Copyrights/IPR (If Any) --**12. INVITED TALKS: --****13. RESEARCH PROJECTS (COMPLETED/ONGOING): --**

**14. PARTICIPATION & PRESENTATIONS IN SEMINARS /SYMPOSIA/
WORKSHOPS/ CONFERENCES:**

Sl. No.	Topic	Period		Venue
		From	To	
1	Machine Learning & Data Science with Python	25-02-2023	01-04-2023	PHN Techonology Pvt. Ltd. Pune
2	Software Engineering and Testing Methodologies	16-03-2022	19-03-2022	HSIT, Nidasoshi
3	Two days online FDP on “Skill Enhancement for Academicians”	08/06/2020	9/06/2020	JCE Belgavi.
4	Workshop on “Design and Simulation of Micro-strip Antenna using HFSS tool”	26/02/2020	29/02/2020	HIT, Nidasoshi
5	Machine Learning and Data Analytics	08/01/2020	11/01/2020	HIT, Nidasoshi
6	Battery Management and Control Techniques in Electric Vehicles	07/12/2020	12/12/2020	HIT, Nidasoshi
7	“Digital circuits” NPTEL online certification	Sep 2020	Dec 2020	SWAYAM
8	Internet of Things and its real time applications	28/01/2019	02/02/2019	HIT, Nidasoshi
9	Internet Of Things	30/12/2016	31/12/2016	SGM COE Mahagaon.
10	Network Simulator 2 for wired Wireless Network	17/10/2013	19/10/2013	SIT Yadrav.
11	Overview of arm Microcontroller & Its Applications	28/09/2013	28/09/2013	SDMCRT, Dharwad
12	Signals And Systems	04/10/2012	06/10/2012	HIT, Nidasoshi
13	Application of Mathematics in Engineering	30/08/2010	31/08/2010	HIT, Nidasoshi
14	CMOS VLSI & ASIC DESIGN using MICROWIND tool	27/08/2009	28/08/2009	SIT, Tumakur
15	Analog and Mixed Mode VLSI Design	20/03/2009	21/03/2009	VDRIT, Haliyal

15. AWARDS, FELLOWSHIPS & OTHER DISTINCTIONS:

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16. ANY OTHER SIGNIFICANT INFORMATION:

- Successfully completed SWAYAM Course on “Digital circuits” in Dec 2020.



Dattatray Kumbhar